### TLF35584 (C-Step)

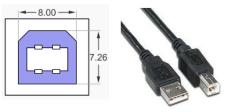
Getting started with the Evaluation Environment 19.09.2019 V2.1





#### **Board Setup**

- > The Evaluation Environment consist of two boards
  - TLF35584 Evaluation Board (green)
    - TLF35584 IC and all external components
    - Pin headers (soldered) to connect it
  - TLF-AURIX Companion Board (red)
    - Fully equipped with connector and firmware
    - Needs USB cable (Type B) for connection to your PC/Notebook



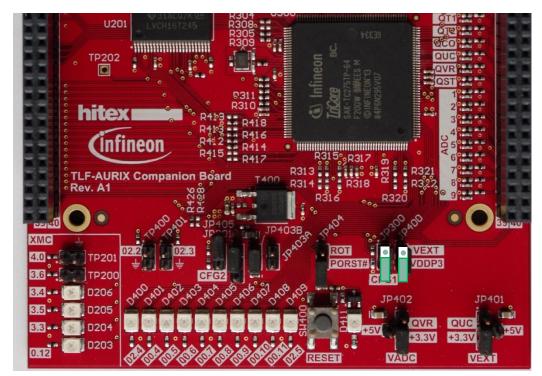


 GUI is available to be run on the PC, which is communicating with an attached Companion board to control and monitor the TLF35584 (ask FAE for the files)

#### Board Setup – Voltage Selection TLF35584QVVS1 (QUC=5V)



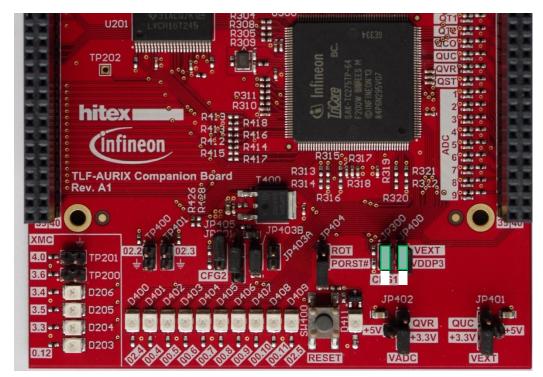
- Please verify the variant of TLF35584 used (check marking) against the configuration on the Companion Board
- > VS1/5V:
  - Open JP400
  - Open JP300



#### Board Setup – Voltage Selection TLF35584QVVS2 (QUC=3.3V)



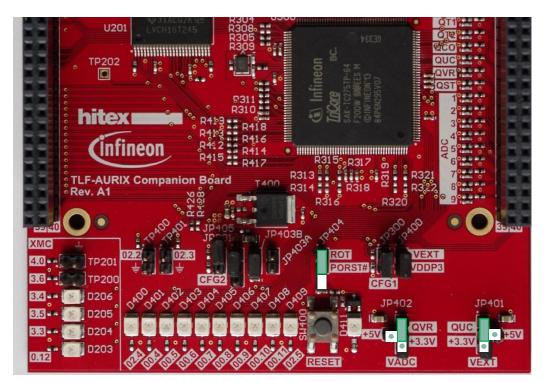
- Please verify the variant of TLF35584 used (check marking) against the configuration on the Companion Board
- > VS2/3.3V:
  - Close JP400
  - Close JP300





#### Board Setup – AURIX Supply and PORST (1)

- The Companion Board offers the possibility to supply the AURIX by the TLF35584 directly or by the Companion Board (USB)
- > Supply by TLF35584:
  - Set JP402(VADC) to "QVR"
  - Set JP401(VEXT) to "QUC"
  - Close JP404 (PORST = ROT)





#### Board Setup – AURIX Supply and PORST (2)

- The Companion Board offers the possibility to supply the AURIX by the TLF35584 directly or by the Companion Board (USB)
- Supply by Companion Board (USB):
  - Set JP402(VADC) to
    "3.3V" or "5V" according to the setting of JP300
    & JP 400 (Slide 3-4)
  - Set JP401(VEXT) to
     "3.3V" or "5V" according to the setting of JP300
     & JP 400 (Slide 3-4)
  - Open JP404
     (PORST != ROT)

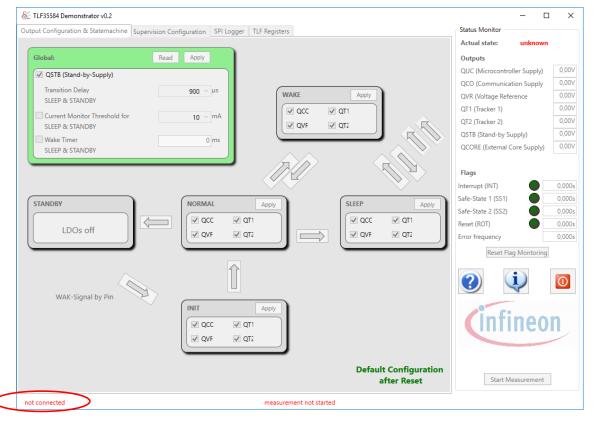


\* Only 3.3V option is shown in the picture!



#### How to get it up and running (1)

- > Before connecting the Companion Board to your PC!
- Start the GUI by executing the file "TLF\_Demonstrator\_V2.1.exe" from the GUI-files
  - GUI will come up showing "not connected" and appearing grey



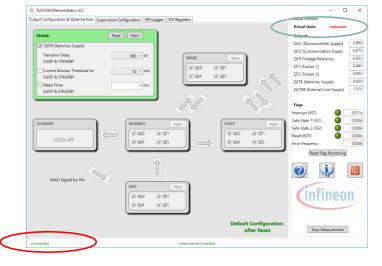


#### How to get it up and running (2)

- Ensure that the AURIX Firmware update to V2R2 has been done or simply redo it
  - Refer to the file
     "TLF35584-Evaluation-Environment\_Firmware-Update.pdf"
  - Otherwise the Companion Board cannot properly communicate with the TLF35584 C-Step Silicon
- Ensure that the TLF35584 Evaluation Board (green) is equipped with a B/C-Step silicon
  - Check the marking on the IC
    - A-Step: "35584VS1" (5V) or "35584VS2" (3V3)
    - B-Step:
      - Line 1: "35584"
      - Line 2: "VS1" (5V) or "VS2" (3V3)
    - C-Step:
      - Line 1: "TLF35584"
      - Line 2: "VS1" (5V) or "VS2" (3V3)

#### How to get it up and running (3)

- Make sure that the Evaluation and Companion Board are properly interconnected as visible on slide 7 (and right)
- Connect the Companion Board to an USB-Port of your PC using the USB cable (Type B)
  - Drivers-should install automatically
  - Some LEDs on the bottom left corner should start flashing (XMC is running and communicates with the GUI)
    - GUI shows "connected" (red circle)
    - Status monitor shows "unknown" as TLF35584 is still unsupplied (blue circle)
  - The XMC microcontroller is only used for PC interface, instruction of AURIX and silent monitoring of the SPI
  - All communication and function towards TLF35584 is only done by AURIX!







#### How to get it up and running (4)

- Connect a power supply to the TLF Evaluation Board
  - Use Banana-Jack connectors "GND" (black) and "BATP" (red one in the middle)
  - $V_{Bat}$  of 12V and a current limit of at least 1A
  - Switch it ON to apply the input voltage
  - GUI:
    - State diagram gets active and shows the TLF35584 in INIT state (red circle)
    - Measurement of output voltages of shows all LDOs being started (blue circle)
  - Check also LEDs on TLF35584 Evaluation board for status

put Configuration & Statemachine Supervision Configuration SPI Logger TLF Registers		Status Monitor	
		Actual state: INIT	
Global: Read Apply		Outputs	
QSTB (Stand-by-Supply)	/	QUC (Microcontroller Supp	oly) 3,
Transition Delay		QCO (Communication Sup	ply 5,
Transition Delay 900 V US WAKE	Apply	QVR (Voltage Reference	5,
	✓ QT1	QT1 (Tracker 1)	4
Current Monitor Threshold for 10 ~ mA		QT2 (Tracker 2)	4
Wake Timer Oms		QSTB (Stand-by Supply)	3
SLEEP & STANDBY		QCORE (External Core Sup	ply) 1
	() () () () () () () () () () () () () (	Flags	
	$\sim$	Interrupt (INT)	0,
		Safe-State 1 (SS1)	0
STANDBY NORMAL Apply	SLEEP Apply	Safe-State 2 (SS2)	0
	QCC QT1	Reset (ROT)	
LDOs off	QVF QT2	Error frequency	0
		Reset Flag Monto	
		Reset Flag Monto	ring
<b>∩</b>			_
WAK-Signal by Pin			
INIT Apply			
		(infine	on
V QVF V QT2			-
	Default Configuration after Reset	Stop Measurem	ant



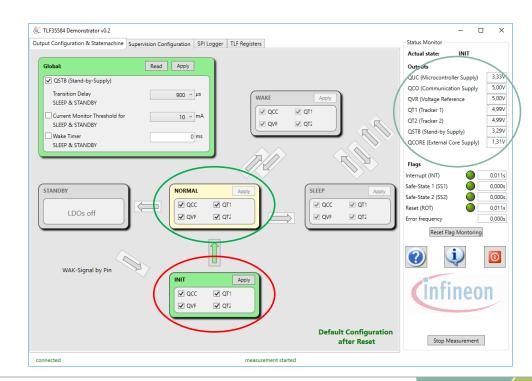
#### How to get it up and running (5)

- > The TLF35584 is up and running
  - All LDOs are kept ON and state is INIT
  - Default configuration is active
- > The AURIX microcontroller is up and running
  - Service of window watchdog and ERR monitoring is done according to the default configuration of the TLF35584

# Using the GUI to control TLF35584 and AURIX (1)



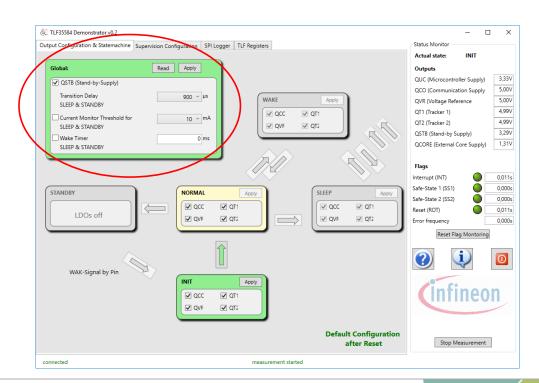
- The ribbon "Output Configuration & State machine"
  - A green "state-box" indicates the current state of TLF35584
  - LDOs can be switched ON and OFF using the checkbox in the respective state, the configuration is taken over when the respective apply button is pressed (e.g. red circle)
    - Output voltages can be read from the status monitor (Blue circle)
  - A green arrows indicates a possible state transition
    - E.g. INIT to NORMAL
    - A pre selection of the LDO configuration for the next state can be done before the request (green circle)



## Using the GUI to control TLF35584 and AURIX (2)



- > The ribbon "Output Configuration & State machine"
  - "Global" settings are used independently from state
    - Configuration for the Standby-LDO
    - Configurations for the movements into SLEEP and STANDBY (Delay time and Current Monitor)
      - Please mind the check-box for the current monitor is related to the option of the TLF35584 to shorten the transition time based on the current of the µC. In SLEEP it is mandatory to be below the current threshold, otherwise the TLF35584 will move to WAKE state.
    - Wake up timer



# Using the GUI to configure supervision functions (1)



- > The ribbon "Supervision Configuration"
  - Offers possibility to configure the Watchdogs and Error Monitoring
    - Active functions
    - Window sizes
    - Error thresholds
  - The AURIX will change its service functions accordingly
  - Failure Injection by
    - Missing trigger events
    - Modification of functional watchdog response table
    - Alternated ERR signal frequency

🗲 TLF35584 Demonstrate	or v0.1		
Output Configuration &	Statemachine Supervision Configuration SPI Logger TLF Registers	Status Monitor	
		Actual state: NORMAL	
Watchdog Cor	afiguration	Outputs	
watchuog coi	niguration.	QUC (Microcontroller Supply)	3,35V
Cycle Base:	○ 100ns	QCO (Communication Supply	5,04V
		QVR (Voltage Reference	4,99V
Window Watchdog	enable in Sleep Mode	QT1 (Tracker 1)	4,97V
Trigger Mode:	SPI (default)     WDI-Pin	QT2 (Tracker 2)	4,98V
Open-Window	50 1600 ms 🔲 User defined time offset for trigger after Closed Window	QSTB (Stand-by Supply)	3,37V
Closed-Window	50 1600 350 ms Time offset 0 ms	QCORE (External Core Supply)	1,28V
Error Threshold	1 15 9	Flags	
Missing trigger	0 10 0 times		L47,091s
		Safe-State 1 (SS1) 🛛 🔵 3.5	551,064
Signature Watchdog	enable in Sleep Mode	Safe-State 2 (SS2) 🛛 🔵 3.5	551,064s
Error Threshold	1 User defined time offset for trigger to heartbeat cycle	Reset (ROT)	0,000s
Heart-Beat	foo me Time offrat	Error frequency 1.3	141.789
mean-beat	50 1600 800 ms mile onset 0 ms	Reset Flag Montoring	
Missing trigger	0 10 times		
Response Table			0
Error Monitoring			
ERR pin monitoring	enable in Sleep Mode	Infinoa	
Alternate frequency	9999 Hz enable alternate frequency		
Use configuration afte	er reset		
	Read Load Save Apply	Stop Measurement	
connected	measurement started		

Use the "Apply" button to send changed configuration(red circle)

### Using the GUI to monitor the SPI and Failure-Events (1)

- > The ribbon "SPI Logger"
  - All communication between AURIX and TLF35584 is logged and stored in this ribbon
  - The list can be cleared and stored by the respective buttons
  - The button "Start" or "Stop Measurement" (red circle) can be used to pause logging and measurements
  - Interrupts will be logged and partially interpreted
  - The "Command Line" is not supported yet.

Output Co	onfiguration & St	atemachine	Supervision	Configuration	SPI Logger	TLF Registers Status Monitor		
						Actual state:	INIT	
SPI Co	ommunica	tion Log				Show Filter Outputs		
irection	Time(s)	Address	Data(HEX)	Data(BIN)	Name	QUC (Microcontr	oller Supply)	3,35
READ	11,880	0x14	0x00	00000000	IF	QCO (Communic	ation Supply	5,04
READ	11,891	0x12	0x00	00000000	SYSFAIL	QVR (Voltage Re	erence	4,9
READ	11,891	0x13	0x00	00000000	INITERR	QT1 (Tracker 1)		4,9
READ	11,891	0x20	0x01	00000001	PROTSTAT	QT2 (Tracker 2)		4,9
WRITE	11,891	0x03	0xAB	10101011	PROTCFG	QSTB (Stand-by Stand-by Stan	iupply)	3,3
WRITE	11,891	0x03	0xEF	11101111	PROTCFG	QCORE (External	Core Supply)	1,2
WRITE	11,891	0x03	0x56	01010110	PROTCFG			
WRITE	11,891	0x03	0x12	00010010	PROTCFG	Flags		
READ	11,891	0x20	0x70	01110000	PROTSTAT	Interrupt (INT)	<b>1</b> .	147,09
WRITE	11,891	0x04	0x0A	00001010	SYSPCFG0	≡ Safe-State 1 (SS1)	3.	551,06
WRITE	11,891	0x05	0x00	00000000	SYSPCFG1	Safe-State 2 (SS2)	3.	551,06
WRITE	11,891	0x06	0x9B	10011011	WDCFG0	Reset (ROT)	<u> </u>	0,00
WRITE	11,891	0x07	0x09	00001001	WDCFG1	Error frequency	1	.141.78
WRITE	11,891	0x08	0x0B	00001011	FWDCFG			
WRITE	11,891	0x09	0x06	00000110	WWDCFG0	Reset F	ag Montoring	
WRITE	11,891	0x0A	0x0B	00001011	WWDCFG1			
READ	11,891	0x20	0x70	01110000	PROTSTAT		<b>i</b> )	0
WRITE	11,891	0x03	0xDF	11011111	PROTCFG			
WRITE	11,891	0x03	0x34	00110100	PROTCFG			
READ	13,079	0x0F	0x01	00000001	WWDSCMD	(inf	inoo	n
WRITE	13,079	0x0F	0x00	00000000	WWDSCMD		IIICU	Ш,
						· · · · · · · · · · · · · · · · · · ·		_
ommand	d Line:					Clear Save		
<enter ?<="" td=""><td>a script comman</td><td>4&gt;</td><td></td><td></td><td></td><td></td><td></td><td>ı</td></enter>	a script comman	4>						ı
-catter t	s senge commun					Start M	leasurement	J





connected

### Using the GUI to manually write and read registers

- The ribbon "TLF Registers"
  - All registers of the TLF35584 (C-step) are accessible for reading and writing manually
  - "Read all" button to update all in one step (red circle)
  - Be aware that this write commands are purely delivered to the TLF35584
    - AURIX will not change for instance watchdog service settings
    - Possible faulty service or reaction has to be considered!

	onfiguration & Statemachine	 SPI Logger						Actual state:	INIT	
TLF35	584 Registers:							Outputs		
	-						*	QUC (Microcontrol	ler Supply)	3,3
0x00	DEVCFG0		0x		Read	Write		QCO (Communicat	ion Supply	4,9
0x01	DEVCFG1		0x		Read	Write		QVR (Voltage Refe	rence	5,0
0x02	DEVCFG2		0x		Read	Write		QT1 (Tracker 1)		4,9
0x03	PROTCFG		0x		Read			QT2 (Tracker 2)		5,0
0x04	SYSPCFG0		0x		Read	Prot. Write	Ε	QSTB (Stand-by Su	pply)	3,3
0x05	SYSPCFG1		0x		Read	Prot. Write		QCORE (External Co	ore Supply)	1,2
0x06	WDCFG0		0x		Read	Prot. Write				
0x07	WDCFG1		0x		Read	Prot. Write		Flags		
0x08	FWDCFG		0x		Read	Prot. Write		Interrupt (INT)		0,0
0x09	WWDCFG0		0x		Read	Prot. Write		Safe-State 1 (SS1)		0,00
0x0A	WWDCFG1		0x		Read	Prot. Write		Safe-State 2 (SS2)		0,0
0x0B	RSYSPCFG0		0x		Read			Reset (ROT)		0,0
0x0C	RSYSPCFG1		0x		Read			Error frequency		0,00
0x0D	RWDCFG0		0x		Read			Reset Flag	g Montoring	
0x0E	RWDCFG1		0x		Read					
0x0F	RFWDCFG		0x		Read				i)	0
0x10	RWWDCFG0		0x	06	Read				V	
0x11	RWWDCFG1		0x	OB	Read					
0x12	WKTIMCFG0		0x	00	Read	Write		Cinfi	neo	n
0x13	WKTIMCFG1		0x	00	Read	Write				-
0x14	WKTIMCFG2		0x	00	Read	Write			A COLORADO TO STATE	
0v15	DEVCTRI			00	Read	Write	*			
_	efresh using data from SPI log					Read all		( ) N	asurement	

easurement starte





#### Further Information

- > Further information can be found in the following documents:
  - Datasheet (C14-Step): TLF35584-Data-Sheet-20-Infineon.pdf
- Please mind the current Evaluation Environment is valid for the C-Step silicon of TLF35584 just after the firmware update of the Companion Boards AURIX microcontroller!
  - For information's please refer to
    - TLF35584-Evaluation-Environment\_Firmware-Update.pdf



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