PRODUCT BRIEF

# BRIGHTLANE™ Q6223 central switch

## 12-port automotive Ethernet switch with integrated 1000BASE-T1 PHYs and MACsec P/N MV-Q6223

The BRIGHTLANE™ Q6223 (MV-Q223) is a secure, managed automotive Ethernet switch designed for use in numerous applications, including as a central switch in zonal network architectures. It offers 90 Gbps of non-blocking switching capacity and features a wide array of advanced security features. The device is a member of the fourth generation of Infineon automotive Ethernet switches.

The 12-port switch includes two integrated IEEEE 802.3-compliant 1000/100BASE-T1 PHYs, 2x RGMII/MII/RMII, 4x multi-speed 2.5 Gbps SerDes (2.5G/1 Gbps) and 8x multispeed 10 Gbps SerDes (10G/5G/2.5G/1 Gbps). Additionally, two of the 10G SerDes can be configured as a PCIe Gen3 x2 interface.

The port interface options offer flexible configurations for connectivity to external devices, such as 2.5/5/10GBASE-T1 PHYs, or uplinks to host SoCs. This makes the device ideal for In-Vehicle Networking (IVN) applications, such as Advanced Driver Assist Systems (ADAS), zonal control modules, and central gateways.

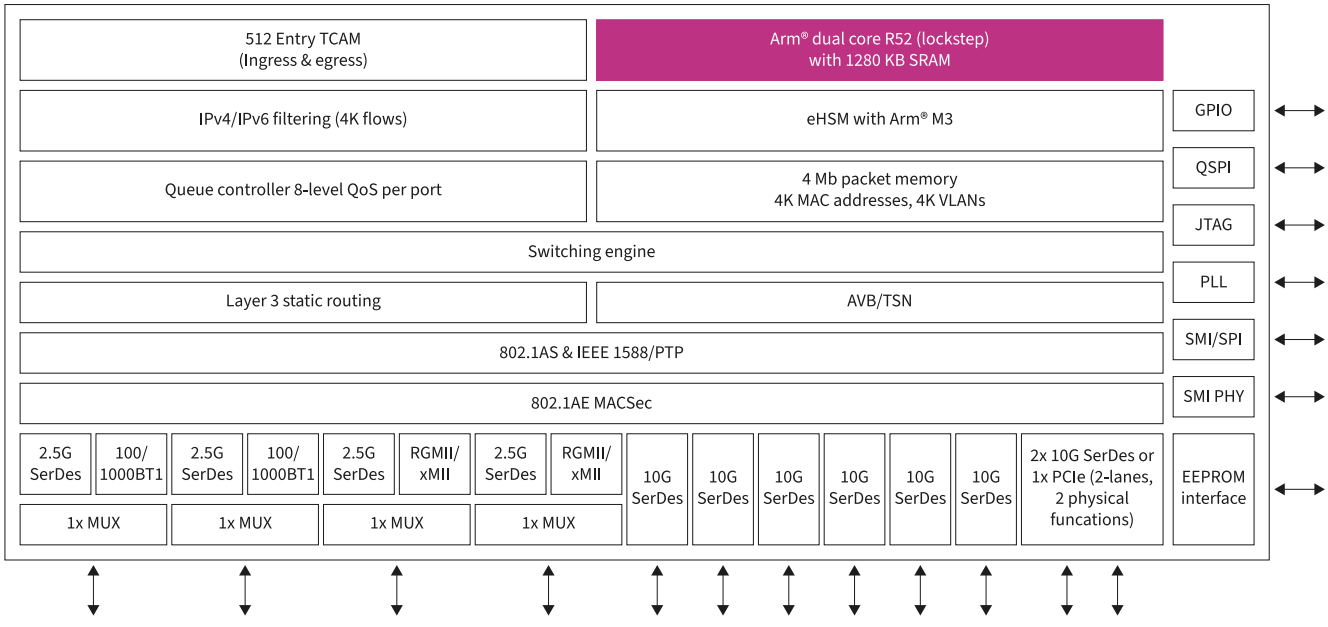
The MV-Q6223 includes a high-performance dual core Arm® R52 CPU that operates in lockstep, with dedicated on-chip memory to support Time Sensitive Networking (TSN) protocols such as Precision Time Protocol (PTP) and security firewall to protect from external malicious attacks.

The switch incorporates advanced security features including 802.1AE MACsec, which provides link security to prevent man-in-the- middle attacks; and a Denial of Service (DoS) engine.

A patented, enhanced TCAM implementation for Deep Packet Inspection (DPI) filters and classifies over 4,000 IPv4/IPv6 flows, and trusted boot functionality secures the vehicle network.

An embedded Hardware Security Module (HSM) enhances device security by supporting secure and encrypted boot and performs key management for features such as MACsec.

### Block diagram



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## Key features

| Features | Benefits |
|---|---|
| Processor | – Integrated dual core Arm® R52 CPU operating in lockstep |
| Security | – 802.1AE MACsec<br>– Embedded Hardware Security Module (eHSM)<br>– Secure boot and encrypted boot support |
| Switch port interfaces | – 2x 1000BASE-T1/100BASE-T1 PHYs<br>– 2x RGMII/MII/RMII<br>– 4x multi-speed SerDes (2.5G/1 Gbps)<br>– 8x multi-speed SerDes (10G/5G/2.5G/1 Gbps)<br>– 1x PCIe Gen 3 x2 supporting single root I/O virtualization (SR-IOV) |
| I/O interfaces | – Configurable GPIO<br>– JTAG interface for debugging<br>– SMI/SPI interface for configuration<br>– QSPI with configurable frequencies (19.2–83.3 MHz) |
| Time Sensitive Networking (TSN) support | – 802.1AS-2020<br>– 802.1Qat / Qav / Qbu / Qbv / Qci / Qcr<br>– 802.1CB |
| Automotive qualified | – AEC-Q100<br>– Automotive grade 2 (-40 to +105°C) |
| Package characteristics | – 17 x 17 mm, 360 pin TFBGA, 0.8 mm pitch |

## Target applications

– Central gateway
– Zonal control module
– In-vehicle infotainment
– Advanced Driver Assistance Systems (ADAS)
– Body domain controller

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