



PSoC® Creator™

Datapath Configuration Tool User Guide

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Introduction



The Datapath Configuration Tool is used to edit datapath instance configurations in a Verilog implementation of a PSoC component. This appendix provides instructions and information that will help you modify your Verilog files using the tool.

For information about implementing a component using Verilog, refer to the Component Author Guide.

General Functionality

Use the Datapath Configuration Tool to:

- read an existing Verilog file
- modify existing datapath configurations
- create new configurations
- delete existing configurations

You can then save changes back into the same Verilog file without affecting any of the other Verilog implementations. The tool works only with existing Verilog files; it cannot be used to create a new Verilog implementation.

After opening a Verilog file, it is parsed for existing datapath configurations. These could be defined as a parameter, localparam, or directly in the datapath as a named parameter. The tool displays all found configurations in the **Configuration** list drop down at the top of the application.

You can edit any bit field, edit comments, copy and paste data between configurations or within a single configuration, and perform whole datapath copy, paste, create or delete operations.

Some operations are applicable only for the datapath (such as deleting a datapath or editing the initial register values). The active datapath is the datapath to which the selected configuration belongs. If the configuration is defined as a parameter, no datapath is active and all datapath options are disabled.

Document History

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Revision	Date	Description of Change
**	5/7/15	New document.
*A	11/27/17	Minor updates.

Framework



Interface

C:\Program Files (x86)\Cypress\PSoC Creator\3.2\PSoC Creator\psoc\content\CyComponentLibrary\CyComponentLibrary.cylib\CRC_v2_40\CRC_v2_40.v - Datapath ...

File Edit View Tools Help

Configuration: **dpconfig0** — Active Configuration

CFGGRAM

Reset	Reg	Binary Value	FUNC	SRC A	SRC B	SHIFT	A0 WR SRC	A1 WR SRC	CFB EN	CI SEL	SI SEL	CMP SEL	Comment
☐	Reg0	10100001 01001000	XDR	A0	D0	SL	ALU	NONE	ENBL	CFGA	CFGA	CFGA	Calculate Lower Half
☐	Reg1	10110101 00011000	XDR	A1	D1	SL	NONE	ALU	ENBL	CFGA	CFGA	CFGA	Calculate Upper Half
☐	Reg2	00000000 00000000	PASS	A0	D0	PASS	NONE	NONE	DSBL	CFGA	CFGA	CFGA	Save Lower Half
☐	Reg3	00010100 00000000	PASS	A1	D1	PASS	NONE	NONE	DSBL	CFGA	CFGA	CFGA	Save Upper Half
☐	Reg4	00000000 11110000	PASS	A0	D0	PASS	F0	F1	DSBL	CFGA	CFGA	CFGA	
☐	Reg5	00000000 11110000	PASS	A0	D0	PASS	F0	F1	DSBL	CFGA	CFGA	CFGA	
☐	Reg6	00000000 11110000	PASS	A0	D0	PASS	F0	F1	DSBL	CFGA	CFGA	CFGA	
☐	Reg7	00000000 11110000	PASS	A0	D0	PASS	F0	F1	DSBL	CFGA	CFGA	CFGA	

CFG9

Reset	AMASK Value	A [7]	A [6]	A [5]	A [4]	A [3]	A [2]	A [1]	A [0]	Unused	Comment
☐	FF	1	1	1	1	1	1	1	1	00000000	

CFG11-10

Reset	CMASK1 Value	CMASK0 Value	C1 [7]	C1 [6]	C1 [5]	C1 [4]	C1 [3]	C1 [2]	C1 [1]	C1 [0]	C0 [7]	C0 [6]	C0 [5]	C0 [4]	C0 [3]	C0 [2]	C0 [1]	C0 [0]	Comment
☐	FF	FF	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	

CFG13-12

Reset	Binary Value	CMP SELB	CMP SELA	CI SELB	CI SELA	CMASK1 EN	CMASK0 EN	A MSK EN	DEF SI	SI SELB	SI SELA	Comment
☐	00001110 00000010	A1_D1	A1_D1	CHAIN	ROUTE	DSBL	DSBL	DSBL	DEF_0	DEFSI	ROUTE	

CFG15-14

Reset	Binary Value	PI SEL	SHIFT SEL	PI DYN	MSB SI	F1 INSEL	F0 INSEL	MSB EN	MSB SEL	CHAIN CMSB	CHAIN FB	CHAIN 1	CHAIN 0	Comment
☐	00000000 1????0000	ACC	SL			BUS	BUS	ENBL	dpMsbVal	NOCHN	NOCHN	NOCHN	NOCHN	

CFG17-16

Reset	Binary Value	Unused [15:13]	ADD SYNC	Unused [11:10]	F1 DYN	F0 DYN	F1 CK INV	F0 CK INV	FIFO FAST	FIFO CAP	FIFO EDGE	FIFO ASYNC	EXT CRCPRS	WRK16 CONCAT	Cor
☐	00000000 00000010	000		00					DP	AX	LEVEL	SYNC	ENBL	DSBL	

Unused Bit Fields

Disabled Bit Fields

Bit Field Parameter

The main form has the following elements:

- Configuration – Contains all configurations found in the active Verilog file. The current selection is called the “active configuration” or “selected configuration.”

- Configuration Register Data Grid – Contains the Reset register button to restore the whole register to its default configuration, a register binary value field, bit field configuration controls and comment field. The Binary value field is read-only. Bit field controls are typically a drop-down list with predefined values. CFG9, CFG10, CFG11 registers are mask registers and their values can be toggled by clicking on the individual bit buttons.
- Bit Field Parameters – Each bit field can be replaced by a parameter value instead of one of the constant value. When a bit-field is defined with a parameter that parameter name is displayed as the value and is displayed with the blue background color.
- Unused Bit Fields – Unused bit fields are read-only and are always initialized with a 0 value.
- Disabled Bit Fields – Bits that are not used in the active configuration. User can enable them using the context menu of these bits. Bits may be disabled with a particular revision of silicon. These bit fields are indicated as disabled with a gray background and no value in the field. They are always initialized with a 0 value.

Menus

File Menu

Menu Item	Shortcut	Description
Open	[Ctrl] + [o]	Displays a dialog to open an existing file (Filtered for .v Verilog files)
Close		Closes the active file
Recent Files		Provides access to previously opened files
Save	[Ctrl] + [s]	Saves the active document
Save As		Displays a dialog to save the active file to another file
Exit		Exit the PSoC Datapath Configuration Tool

Edit Menu

Menu Item	Shortcut	Description
Copy Datapath	[Ctrl] + [c]	Copies the selected datapath configuration
Paste Datapath	[Ctrl] + [v]	Pastes full datapath configuration from the Clipboard into the active datapath
Paste Dynamic		Pastes the full dynamic configuration information of the datapath configuration from the Clipboard into the active configuration (all 8 registers of the dynamic configuration are pasted)
Reset Datapath		Resets the current datapath configuration to the default bit field values
New Datapath		Add a new datapath instance
Delete Datapath		Delete a datapath instance

View Menu

Menu Item	Shortcut	Description
Initial Register Values		Displays a dialog to configure initial register values for the active datapath

Tools Menu

Menu Item	Shortcut	Description
Options > Splash screen on Startup		Check box to enable or diable the splash screen when the datapath configuration tool starts up.

Help Menu

Menu Item	Shortcut	Description
Documentation	[F1]	Open this documentation.
About		Opens the About dialog, which provides build information

General Tasks



This section covers general Datapath Configuration Tool tasks, including:

- Launching the Datapath Configuration Tool
- Opening a Verilog File
- Saving a File

Launching the Datapath Configuration Tool

To launch the tool, go to the **Start** menu and select **Cypress > PSoC Creator 1.0 > Component Development Kit > Datapath Configuration Tool**.

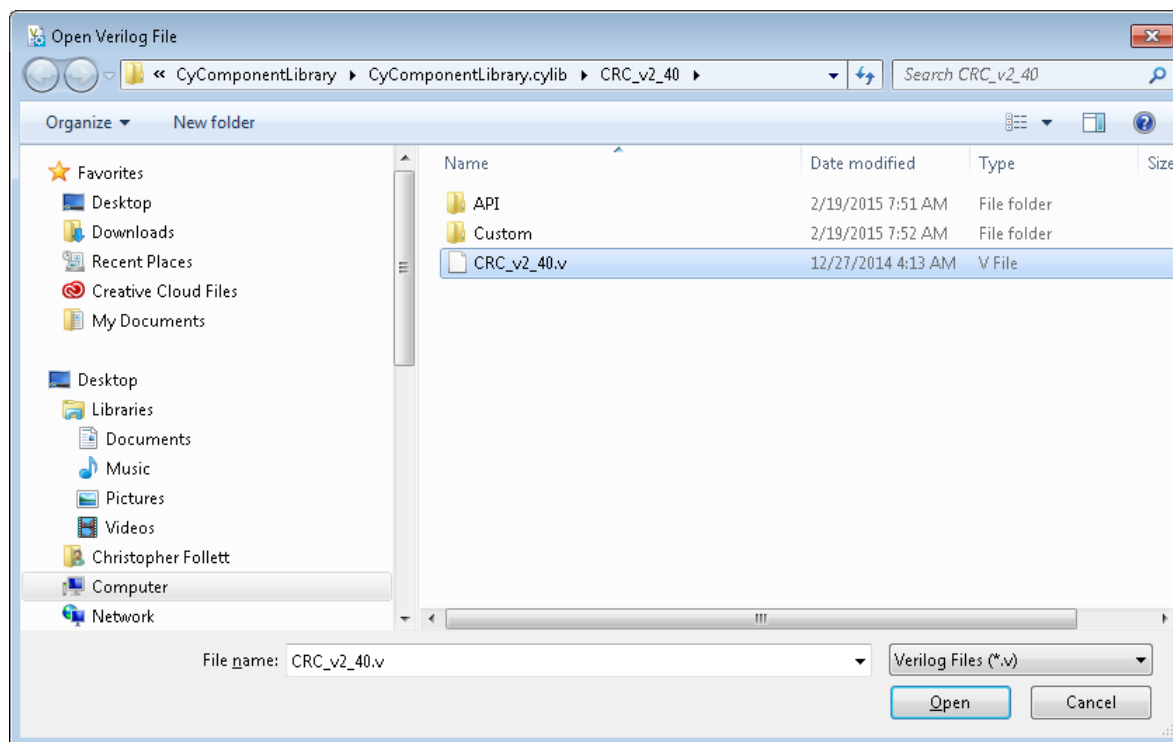
When the application launches, it will not contain any datapath configuration information.

To begin working with the tool you must open an existing Verilog file.

Opening a Verilog File

1. To open a Verilog file, select **Open...** from the **File** menu.

The Open Verilog Dialog displays.



Note: You can also open recent files without this dialog using **Recent Files** on the **File** menu or dragging a Verilog file directly onto the main form of the application.

2. Navigate to and select the file to update, and then click **Open**.

The dialog closes and the tool gathers all appropriate information from the Verilog file and updates the rest of the window.

Saving a File

To save the active file, select **Save** from the **File** menu.

To save a file with another name and/or in a different location, select **Save As** from the **File** menu.

Working with Bit Field Parameters



In addition to a fixed set of predefined constant fields, bit fields support a parameter value.

Adding Parameter to Enumerated Bit Fields

Enumerated bit fields contain an **Add Parameter** context menu item.

CFGGRAM													
Reset	Reg	Binary Value	FUNC	SRC A	SRC B	SHIFT	A0 WR SRC	A1 WR SRC	CFB EN	CI SEL	SI SEL	CMP SEL	Comment
	Reg0	10100001 01001000	XOR	A0	D0	SL	ALU	NONE	ENBL	CFGA	CFGA	CFGA	Calculate Lower Half
	Reg1	10110101 00011000	XOR	A1	D1	SL	NONE	ALU	ENBL	CFGA	CFGA	CFGA	Calculate Upper Half
	Reg2	00000000 00000000	PASS	A0	D0	PASS	NONE	NONE	DSBL	CFGA	CFGA	CFGA	Save Lower Half
	Reg3	00010100 00000000	PASS	A1	D1	PASS	NONE	NONE	DSBL	CFGA	CFGA	CFGA	Save Upper Half
	Reg4	00000000 11111000	PASS	A0	D0	PASS			DSBL	CFGA	CFGA	CFGA	
	Reg5	00000000 11111000	PASS	A0	D0	PASS			ENBL	CFGA	CFGA	CFGA	
	Reg6	00000000 11111000	PASS	A0	D0	PASS			DSBL	CFGA	CFGA	CFGA	
	Reg7	00000000 11111000	PASS	A0	D0	PASS			DSBL	CFGA	CFGA	CFGA	

This command allows you to provide any parameter name as an additional choice for the bit field. After adding a parameter, the drop down menu of the bit field will include all the predefined constants, as well as any added parameter value as selection choices. When the parameter value is selected, it will be displayed with blue color indicating that it is not one of the predefined values.

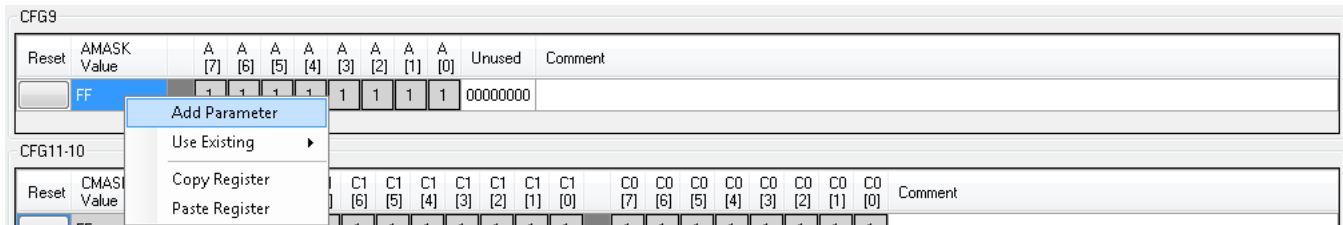
To change a parameter name, right click on the bit field and select **Edit Parameter** from the context menu.

CFG15-14													
Reset	Binary Value	PI SEL	SHIFT SEL	PI DYN	MSB SI	F1 INSEL	F0 INSEL	MSB EN	MSB SEL	CHAIN CMSB	CHAIN FB	CHAIN 1	CHAIN 0
	00000000 1????0000	ACC	SL			BUS	BUS	ENBL	dpMSB SEL	NOCHN	NOCHN	NOCHN	NOCHN

CFG17-16													
Reset	Binary Value	Unused [15:13]	ADD SYNC	Unused [11:10]	F1 DYN	F0 DYN	F1 CK INV	F0 CK INV	FIFO FAS	FIFO ASYNC	EXT CRCPRS	WRK16 CONCAT	Cor

Adding Parameter to Mask Bit Fields

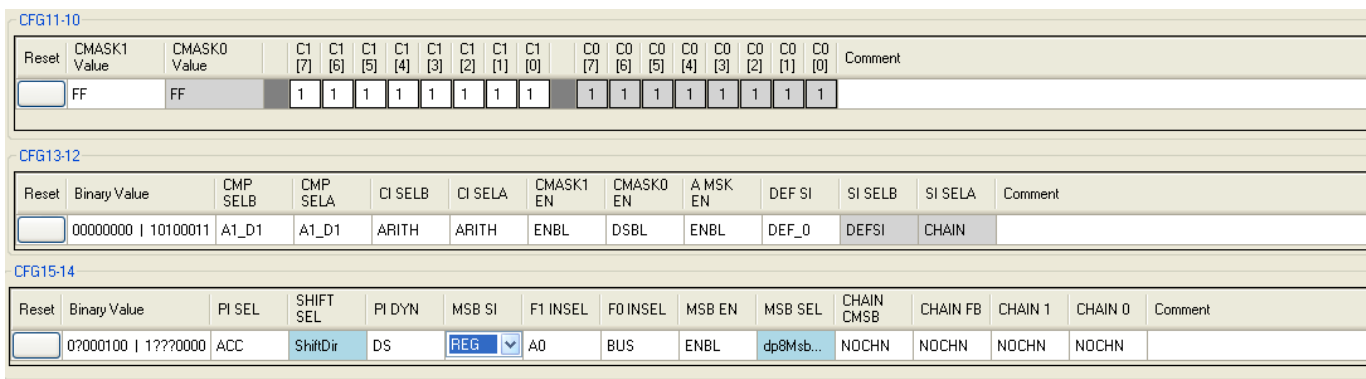
To add a parameter to a mask bit field, select **Add Parameter** from the context menu in the **Value** column, and type the name of parameter. After adding a parameter, separate bits become read-only.



To remove a parameter, select **Remove Parameter** from the context menu in the **Value** column.

Bit Field Dependencies

Datapath configuration has a few bits which have an influence on other bits. Depending on their value, other bit fields may become unused. To make it visual for user, some bit fields became disabled depending on the value of other fields.



On the screenshot above bits CMASK0, SI SELB, and SI SELA are disabled. The reason is that CMASK0 EN bit is set to DSBL and MSB SI bit is set to REG.

Working with Configurations



There are two methodologies that can be used to configure the elements of a datapath instance.

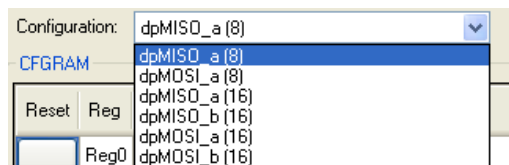
The first methodology directly places the configuration of the datapath in the datapath instance. For multi-byte datapath instances, a separate set of values is used for each byte of the overall instance. This is a common methodology used when a particular configuration is only used once in a Verilog file.

The second methodology assigns the configuration for a datapath to a parameter. That parameter is then used by datapath instances. This methodology is convenient when the same configuration is used by multiple instances to eliminate duplication and simplify maintenance of the component. This methodology is used by the Counter library component. Both methodologies can be used within the same Verilog file.

Both methodologies are supported by PSoC Datapath Configuration Tool. All configurations are added to the **Configuration** drop down list.

Configuration naming

For the datapath instance case the label is the datapath instance name. For the dp8, dp16, dp24 and dp32 datapath instances the configuration labels are suffixed with “_a”, “_b”, “_c” or “_d” depending on which byte of the instance the configuration is for. Also the datapath size (8, 16, 24, 32) is added at the end of the configuration name in the drop down. For the parameter instances case the label is the parameter name.



Editing Configuration

To make the configuration active, select it in the **Configuration** drop down list. Bit fields of each register can be changed by choosing one of the predefined values in the cell drop down list or adding a parameter. All changes made to the active configuration are stored automatically to object storage in the application, so the user can switch between different configurations without losing data. The data is not saved to the verilog file until the main menu operation “Save” or “Save As” is completed.

Copy, Paste Configuration

To copy the active configuration data to the Clipboard, select **Copy** from the **Edit** menu.

To paste the configuration data from the Clipboard, select **Paste** from the **Edit** menu. This command will replace all bit fields and comments of the active configuration with values from the Clipboard.

To paste only the dynamic registers data, select **Paste Dynamic** from the **Edit** menu.

Note: If you want to paste only the dynamic registers data, to copy it initially you should use the **Copy** command as well.

To copy a separate register, select **Copy Register** from the context menu of any bit field that belongs to that register.

To paste a separate register, select **Paste Register** from the context menu of any bit field that belongs to that register.

Resetting Configuration

To reset the current datapath configuration to the default bit field values, use **Reset Datapath** on the **Edit** menu.

To reset a particular register of the datapath configuration to its default values, press the button of the **Reset** column of data grid in that row on the main form.

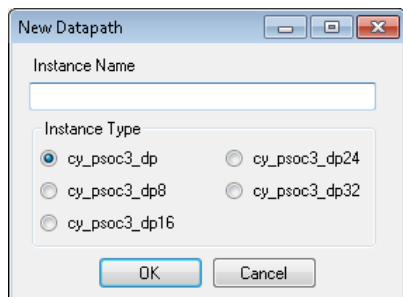
The default value for all of the bit fields, except the mask fields of CFG9-11, is **0**. For the mask bit fields of CFG9-11 the default is **1**. The default comment value for dynamic registers is **Idle** and for all other registers is an empty string.

Working with Datapath Instances



Creating a New Datapath Instance

To create a new datapath, select **New Datapath** from the **Edit** menu. The dialog **New Datapath** will appear.



Type the name of the new datapath in the **Instance Name** text box, select datapath type, and press **OK**.

A new datapath will be inserted at the end of the current verilog file before the **endmodule**.

New datapath configurations would be initialized with all of the default bit-field values, all input signals would be connected to 1'b0 and all output signals would be unconnected.

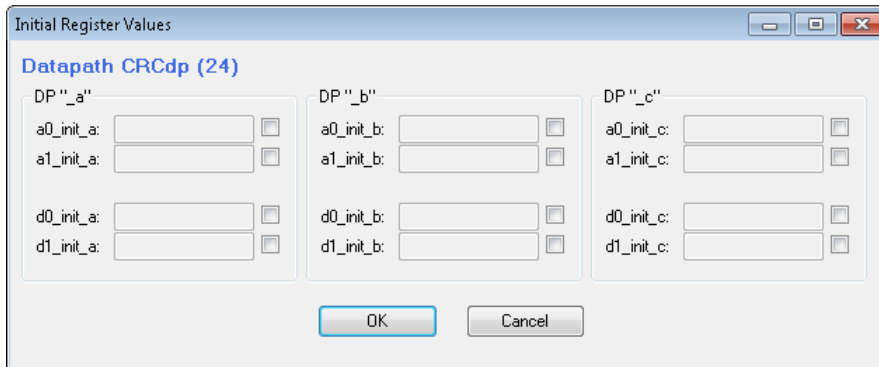
Deleting the Datapath Instance

To delete the active datapath select **Delete Datapath** from the **Edit** menu.

If there are no active datapaths, this option is disabled in the menu. If the datapath has multiple configurations (dp16, dp24, or dp32), all of them will be deleted.

Setting Initial Register Values

To set the initial register values for the active datapath, use **Initial Register Values** on the **View** menu. The dialog **Initial Register Values** will appear.



The dialog box titled "Initial Register Values" contains three columns for different datapaths: DP "_a", DP "_b", and DP "_c". Each column has four rows of input fields labeled a0_init, a1_init, d0_init, and d1_init, followed by a checkbox. At the bottom are "OK" and "Cancel" buttons.

Datapath CRCdp (24)		
DP "_a"	DP "_b"	DP "_c"
a0_init_a: ☐	a0_init_b: ☐	a0_init_c: ☐
a1_init_a: ☐	a1_init_b: ☐	a1_init_c: ☐
d0_init_a: ☐	d0_init_b: ☐	d0_init_c: ☐
d1_init_a: ☐	d1_init_b: ☐	d1_init_c: ☐

OK Cancel

Check the boxes near the entries that will be defined and enter either an 8-bit hexadecimal value (8'h?) or a parameter value in the edit field. Then press OK to submit changes. Initial values are passed to a datapath as named parameters, so any values not defined will not be passed to the datapath and will be set internally to their default values.

If there are no active datapaths, this option is disabled in the menu.

Support



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- Technical Support.

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