Join our Mission Future!

5th of May

09:00-09:30	Workshop introduction
09:30-09:45	Break
09:45-10:15	IPCEI + Infineon as part of IPCEI
10:15-10:45	Insight into INFINEON Technologies
10:45-11:15	Development center Villach – Design Enabling Services (DES) overview + DES VAL
11:15-12:00	Development Automotive Body Power Villach
12:00-13:00	Lunch break
13:00-13:45	Analog Mixed signal Design at Infineon
13:45-14:15	Analog Mixed signal Verification overview
14:15-14:30	Break
14:30-15:00	Internships and career opportunities in Infineon
15:00-16:00	Software tools used in Post-Silicon validation processes
16:00-17:30	Free
17:30-18:00	Free
18:00-21:00	Social Event
After 21:00	Free

6th of May

09:00-10:00	Turning sand into wafer, Path from idea to production
10:00-10:10	Break
10:10-10:40	PMS in uC: Structure and functionality overview
10:40-11:00	ADCs in uC: General approach to Post-Silicon testing
11:00-11:20	PLL basics and verification concept
11:20-11:30	Break
11:30-11:50	DTS sensor applications and evaluation procedures
11:50-12:10	Continuous Time Delta-Sigma ADC – Setup Preparation
12:10-12:30	Communication protocol (I2C/I3C) – Theory and implementation
12:30-13:30	Lunch break
13:30-14:00	Overview of Power & Sensor Systems division
14:00-14:30	Automated material management system
14:30-15:00	Q&A
15:00-15:30	Workshop summary and closing

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