

## XC2365

### Next Generation Safety Microcontroller with 32-Bit Performance



THE XC2365 is one member of the new XC2300 microcontroller family, a subset of the XC2000 family, with focus on safety-applications.

The XC2365 with its high-performance CPU features powerful peripherals like fast Dual-A/D converters, impressive PWM-units, flexible serial interfaces (Universal Serial Interfaces (USICs)), as well as a MultiCAN unit with 3 CAN nodes. It offers the perfect fit for demanding automotive safety applications such as airbag, power steering or low-end ABS-systems.

Integration of external components such as embedded voltage regulator, EEPROM emulation with additional flash modules and various on chip oscillators additionally optimizes system costs.

#### Applications

- Automotive Safety

#### Features

- High performance 16-/32-bit C166S V2 CPU with 5-stage pipeline
- Single clock cycle instruction execution with 12.5 ns instruction time at 80 MHz CPU clock
- 12.5 ns multiplication (16 x 16 bit), background division (32/16 bit) and multiply-and-accumulate (MAC) instructions
- Zero cycle jump execution
- Register-based design with Multiple Variable Register Banks
- Fast context switching support with two additional local register banks
- 16 Mbytes total linear address space for code and data
- 1024 Bytes on-chip SFR area (C166 family compatible)
- 16-priority-level interrupts system with up to 87 sources, sample-rate down to one clock cycle
- 8-channel interrupt-driven single-cycle data transfer facilities via peripheral event controller (PEC)
- Clock generation via on-chip PLL or via Prescaler
- 1 Kbyte on-chip stand-by RAM (SBRAM)
- 2 Kbytes on-chip dual-port RAM (DPRAM)
- 16 Kbytes on-chip data SRAM (DSRAM)
- Up to 32 Kbytes on-chip program/data SRAM (PSRAM)

- Up to 576 Kbytes on-chip Flash memory
  - Built-in error correction (ECC)
  - Up to 64 Kbytes ideal for data flash and EEPROM emulation
- Two synchronizable A/D converters with 16 channels, optional data pre-processing, and a conversion time down to 1.2  $\mu$ s
- 16-Channel general purpose capture/compare unit
- Two capture/compare units for flexible PWM signal generation (3 capture/compare channels and 1 compare channel)
- Multi-functional general purpose timer unit with 5 timers
- Six serial interface channels to be used as UART, LIN, buffered SPI, IIC Bus Interface, IIS Interface (optional feature; dedicated interfaces offered as well)
- On-Chip MultiCAN Interface (Rev. 2.0B active) with 32 message objects on 3 CAN nodes
- On-chip real time clock
- Programmable watchdog timer and oscillator watchdog
- Up to 75 general purpose I/O lines
- On-chip bootstrap loader
- Supported by a large range of development tools
- On-chip debug support via JTAG interface
- 100-pin green LQFP package, 0.5 mm (19.7 mil) pitch
- Single Power Supply from 3.0 V to 5.5 V
- Temperature range:
  - -40° to +85°C
  - -40° to +125°C

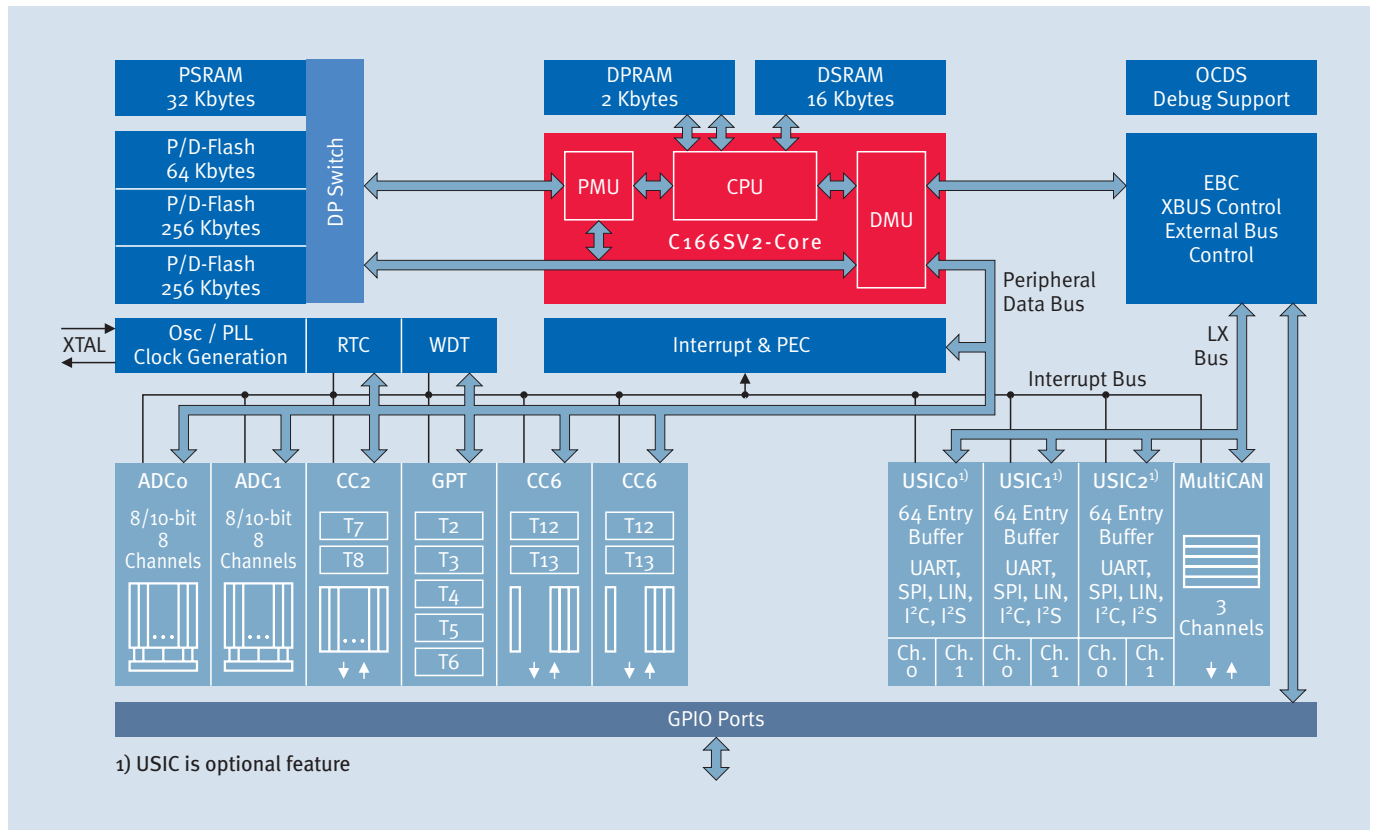
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## Microcontrollers



Never stop thinking

## Block Diagram XC2365



## Product Summary

Type	eFlash [Kbytes]	RAM [Kbytes]	Frequency [MHz]	Serial Interface	ADC Channels	Temperature Range [°C]	Package
SAF-XC2365-48F66L	384	26	66	6 x USIC, 3 CAN	16	-40 ... 85	PG-LQFP-100
SAK-XC2365-48F66L	384	26	66	6 x USIC, 3 CAN	16	-40 ... 125	PG-LQFP-100
SAF-XC2365-48F80L	384	26	80	6 x USIC, 3 CAN	16	-40 ... 85	PG-LQFP-100
SAK-XC2365-48F80L	384	26	80	6 x USIC, 3 CAN	16	-40 ... 125	PG-LQFP-100
SAF-XC2365-56F66L	448	34	66	6 x USIC, 3 CAN	16	-40 ... 85	PG-LQFP-100
SAK-XC2365-56F66L	448	34	66	6 x USIC, 3 CAN	16	-40 ... 125	PG-LQFP-100
SAF-XC2365-56F80L	448	34	80	6 x USIC, 3 CAN	16	-40 ... 85	PG-LQFP-100
SAK-XC2365-56F80L	448	34	80	6 x USIC, 3 CAN	16	-40 ... 125	PG-LQFP-100
SAF-XC2365-72F66L	576	50	66	6 x USIC, 3 CAN	16	-40 ... 85	PG-LQFP-100
SAK-XC2365-72F66L	576	50	66	6 x USIC, 3 CAN	16	-40 ... 125	PG-LQFP-100
SAF-XC2365-72F80L	576	50	80	6 x USIC, 3 CAN	16	-40 ... 85	PG-LQFP-100
SAK-XC2365-72F80L	576	50	80	6 x USIC, 3 CAN	16	-40 ... 125	PG-LQFP-100

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