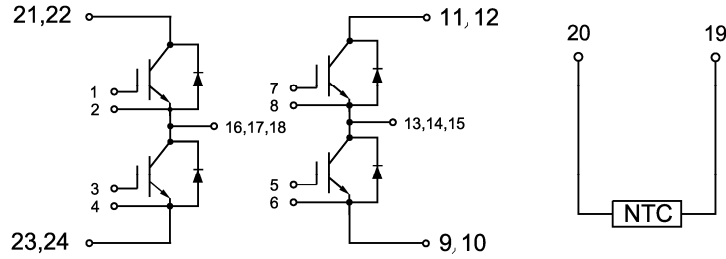
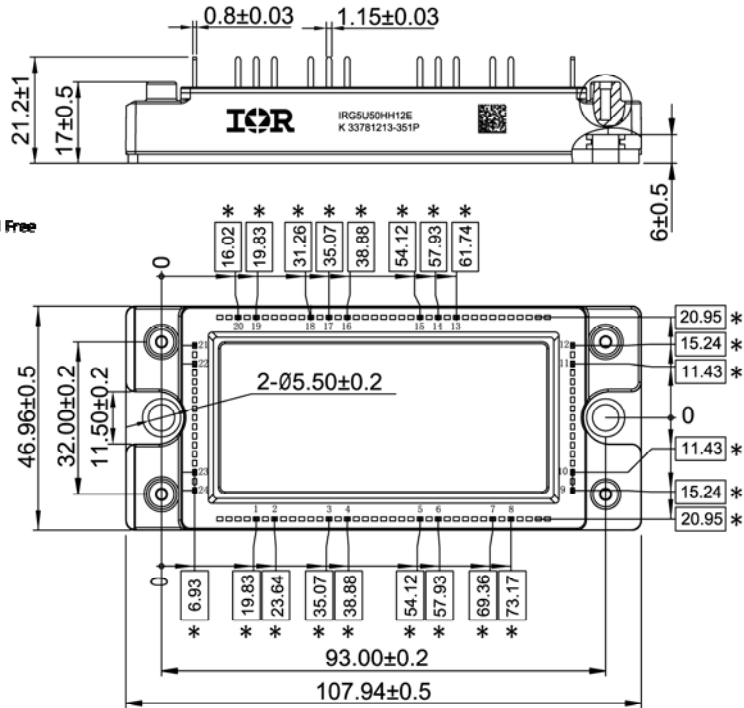


Internal Circuit:



Package Outline (Unit: mm):

Marking Information
K XXXXXXXX-YWWP
 Assembly Site Code Lot No. Date Code P = Lead Free
 Y = Year
 WW = Week



* = all dimensions with tolerance of $\varnothing 0.4$