International Rectifier

- Generation V Technology
- Ultra Low On-Resistance
- N-Channel MOSFET
- Surface Mount
- Very Low Gate Charge and Switching Losses
- Fully Avalanche Rated
- Lead-Free

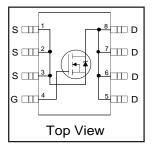
Description

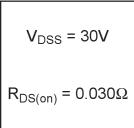
Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infra red, or wave soldering techniques.

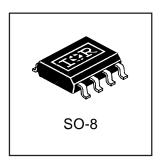
IRF9410PbF

HEXFET® Power MOSFET





Recommended upgrade: IRF7403 or IRF7413 Lower profile/smaller equivalent: IRF7603



Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

		Symbol	Maximum	Units	
Drain-Source Voltage		V_{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20	v	
Continuous Drain Current®	T _A = 25°C		7.0		
Continuous Diam Cullent	T _A = 70°C	I _D	5.8	A	
Pulsed Drain Current		I _{DM}	37		
Continuous Source Current (Diode Conduction)		Is	2.8		
Marrian Parran Dissipation (C)	T _A = 25°C	- Po	2.5	W	
Maximum Power Dissipation S	T _A = 70°C	LD.	1.6	vv	
Single Pulse Avalanche Energy ②	•	E _{AS}	70	mJ	
Avalanche Current		I _{AR}	4.2	А	
Repetitive Avalanche Energy		E _{AR}	0.25	mJ	
Peak Diode Recovery dv/dt ③		dv/dt	5.0	V/ ns	
Junction and Storage Temperature Rang	е	T _{J,} T _{STG}	-55 to + 150	°C	

Thermal Resistance Ratings

Parameter	Symbol	Limit	Units
Maximum Junction-to-Ambient ⑤	$R_{\theta JA}$	50	°C/W
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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Tyn	Max.	Units	Conditions
.,			ıyp.	IVIAA.		
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	30			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient		0.024		V/°C	Reference to 25°C, I _D = 1mA
			0.024	0.030		$V_{GS} = 10V, I_D = 7.0A \oplus$
R _{DS(on)}	Static Drain-to-Source On-Resistance		0.032	0.040	Ω	$V_{GS} = 5.0V, I_D = 4.0A$ @
			0.037	0.050		$V_{GS} = 4.5V, I_D = 3.5A \oplus$
V _{GS(th)}	Gate Threshold Voltage	1.0			V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
g _{fs}	Forward Transconductance		14		S	$V_{DS} = 15V, I_D = 7.0A$
	Drain-to-Source Leakage Current			2.0		$V_{DS} = 24V, V_{GS} = 0V$
I _{DSS}				25	μA	$V_{DS} = 24V, V_{GS} = 0V, T_{J} = 55^{\circ}C$
1	Gate-to-Source Forward Leakage			100	nA	$V_{GS} = 20V$
I _{GSS}	Gate-to-Source Reverse Leakage			-100	11/4	$V_{GS} = -20V$
Qg	Total Gate Charge		18	27		I _D = 2.0A
Q _{gs}	Gate-to-Source Charge		2.4	3.6	nC	$V_{DS} = 15V$
Q _{gd}	Gate-to-Drain ("Miller") Charge		4.9	7.4		V _{GS} = 10V, See Fig. 10 ⊕
t _{d(on)}	Turn-On Delay Time		7.3	15		V _{DD} = 25V
t _r	Rise Time		8.3	17	ns	I _D = 1.0A
t _{d(off)}	Turn-Off Delay Time		23	46	115	$R_G = 6.0\Omega, V_{GS} = 10V$
t _f	Fall Time		17	34		$R_D = 25\Omega \oplus$
C _{iss}	Input Capacitance		550			V _{GS} = 0V
Coss	Output Capacitance		260		pF	V _{DS} = 25V
C _{rss}	Reverse Transfer Capacitance		100			f = 1.0MHz, See Fig. 9

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			2.8		MOSFET symbol
	(Body Diode)			2.0	Α	showing the
I _{SM}	Pulsed Source Current			37	_ ^	integral reverse
	(Body Diode) ①			31		p-n junction diode.
V_{SD}	Diode Forward Voltage		0.78	1.0	V	$T_J = 25^{\circ}C$, $I_S = 2.0A$, $V_{GS} = 0V$ ③
t _{rr}	Reverse Recovery Time		40	80	ns	T _J = 25°C, I _F = 2.0A
Q _{rr}	Reverse RecoveryCharge		63	130	nC	di/dt = 100A/µs ③

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- ② Starting T_J = 25°C, L = 6.6mH R_G = 25 Ω , I_{AS} = 4.6A.
- $\label{eq:loss} \begin{tabular}{ll} \begin$
- 4 Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$.

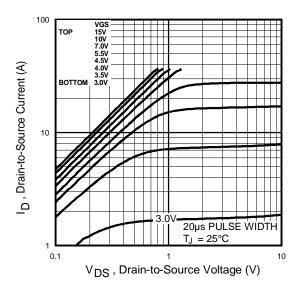


Fig 1. Typical Output Characteristics

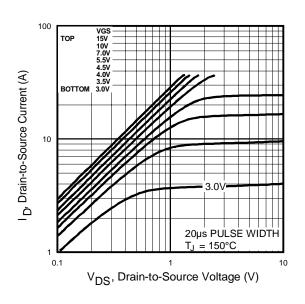


Fig 2. Typical Output Characteristics

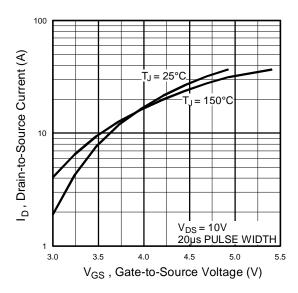


Fig 3. Typical Transfer Characteristics

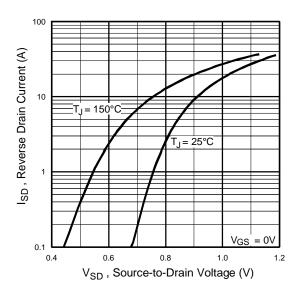


Fig 4. Typical Source-Drain Diode Forward Voltage

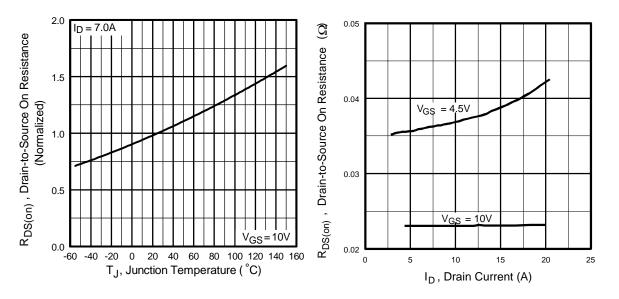


Fig 5. Normalized On-Resistance Vs. Temperature

Fig 6. Typical On-Resistance Vs. Drain Current

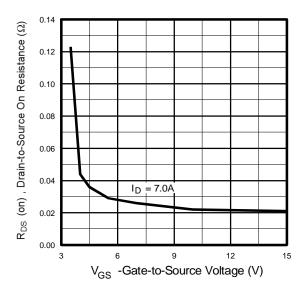


Fig 7. Typical On-Resistance Vs. Gate Voltage

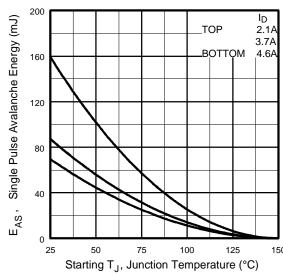


Fig 8. Maximum Avalanche Energy
Vs. Drain Current

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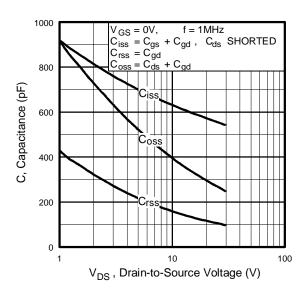


Fig 9. Typical Capacitance Vs. Drain-to-Source Voltage

Fig 10. Typical Gate Charge Vs. Gate-to-Source Voltage

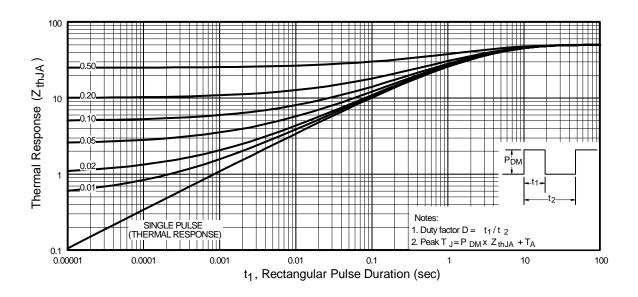


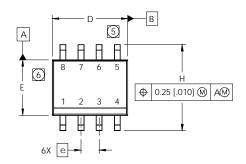
Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

International

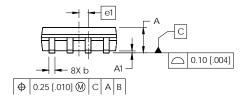
Rectifier

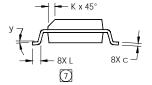
SO-8 Package Outline

Dimensions are shown in millimeters (inches)



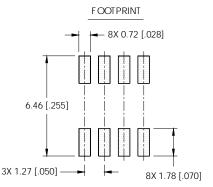
DIM	INC	HES	MILLIMETERS		
DIIVI	MIN	MAX	MIN	MAX	
Α	.0532	.0688	1.35	1.75	
A1	.0040	.0098	0.10	0.25	
b	.013	.020	0.33	0.51	
С	.0075	.0098	0.19	0.25	
D	.189	.1968	4.80	5.00	
Ε	.1497	.1574	3.80	4.00	
е	.050 B	ASIC	1.27 BASIC		
e1	.025 B	ASIC	0.635 BASIC		
Н	.2284	.2440	5.80	6.20	
K	.0099	.0196	0.25	0.50	
L	.016	.050	0.40	1.27	
У	0°	8°	0°	8°	





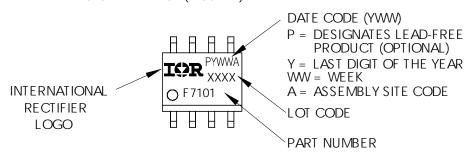
NOTES

- 1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
- 2. CONTROLLING DIMENSION: MILLIMETER
- 3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- 4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- (5) DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 [.006].
- (6) DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 [.010].
- DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.



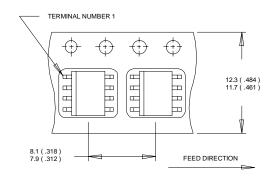
SO-8 Part Marking

EXAMPLE: THIS IS AN IRF7101 (MOSFET)

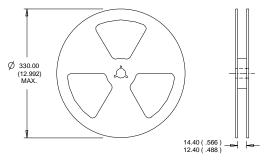


SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



- NOTES:
 1. CONTROLLING DIMENSION : MILLIMETER.
 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES:
 1. CONTROLLING DIMENSION: MILLIMETER.
- 2. OUTLINE CONFORMS TO EIA-481 & EIA-541

Data and specifications subject to change without notice. This product has been designed and qualified for the Consumer market. Qualifications Standards can be found on IR's Web site.



IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105 TAC Fax: (310) 252-7903

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