

Figure 11. Switching Parameter Definitions

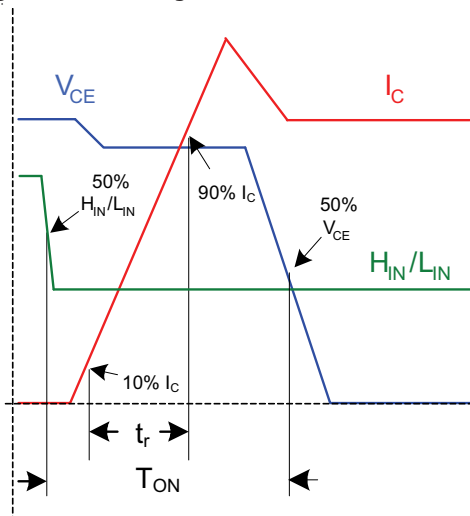


Figure 11a. Input to Output Propagation turn-on Delay Time

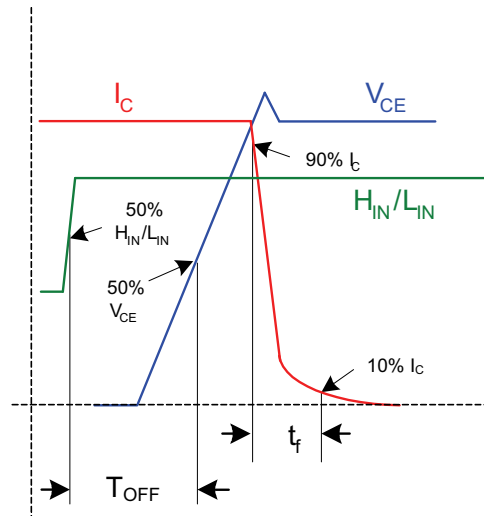


Figure 11b. Input to Output Propagation turn-off Delay Time

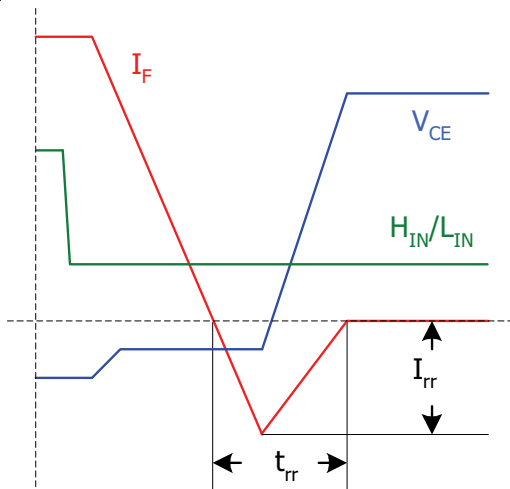


Figure 11c. Diode Reverse Recovery

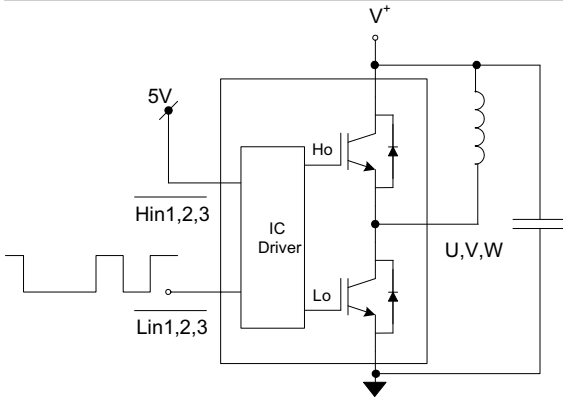


Figure CT1. Switching Loss Circuit

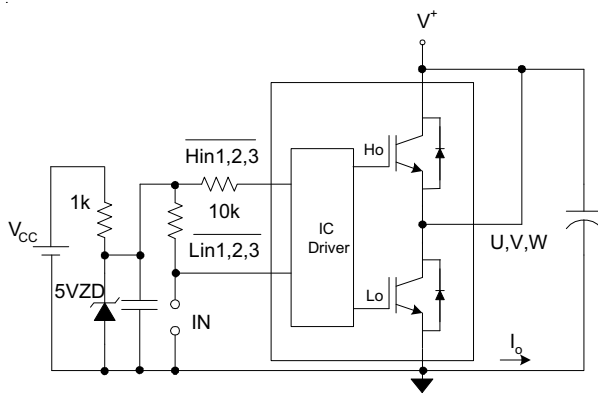
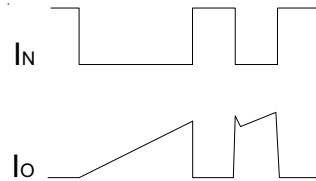


Figure CT2. S.C.SOA Circuit

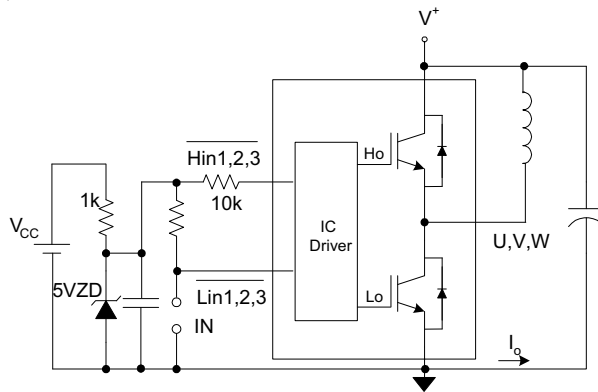
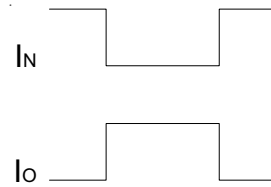
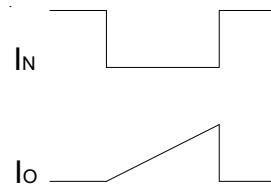


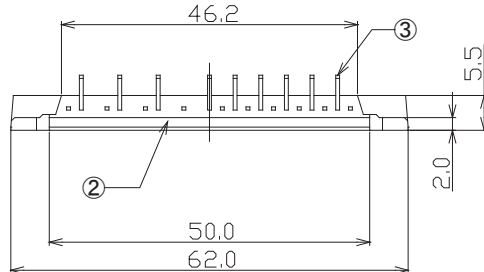
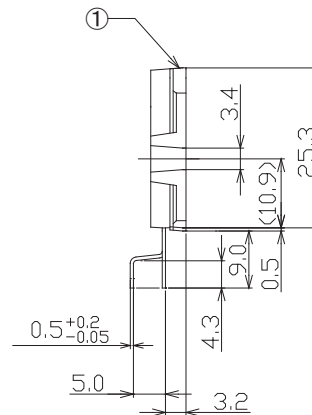
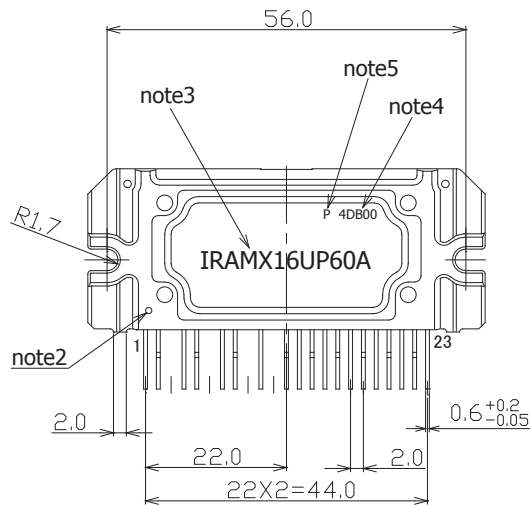
Figure CT3. R.B.SOA Circuit



IRAMX16UP60A

Package Outline IRAMX16UP60A

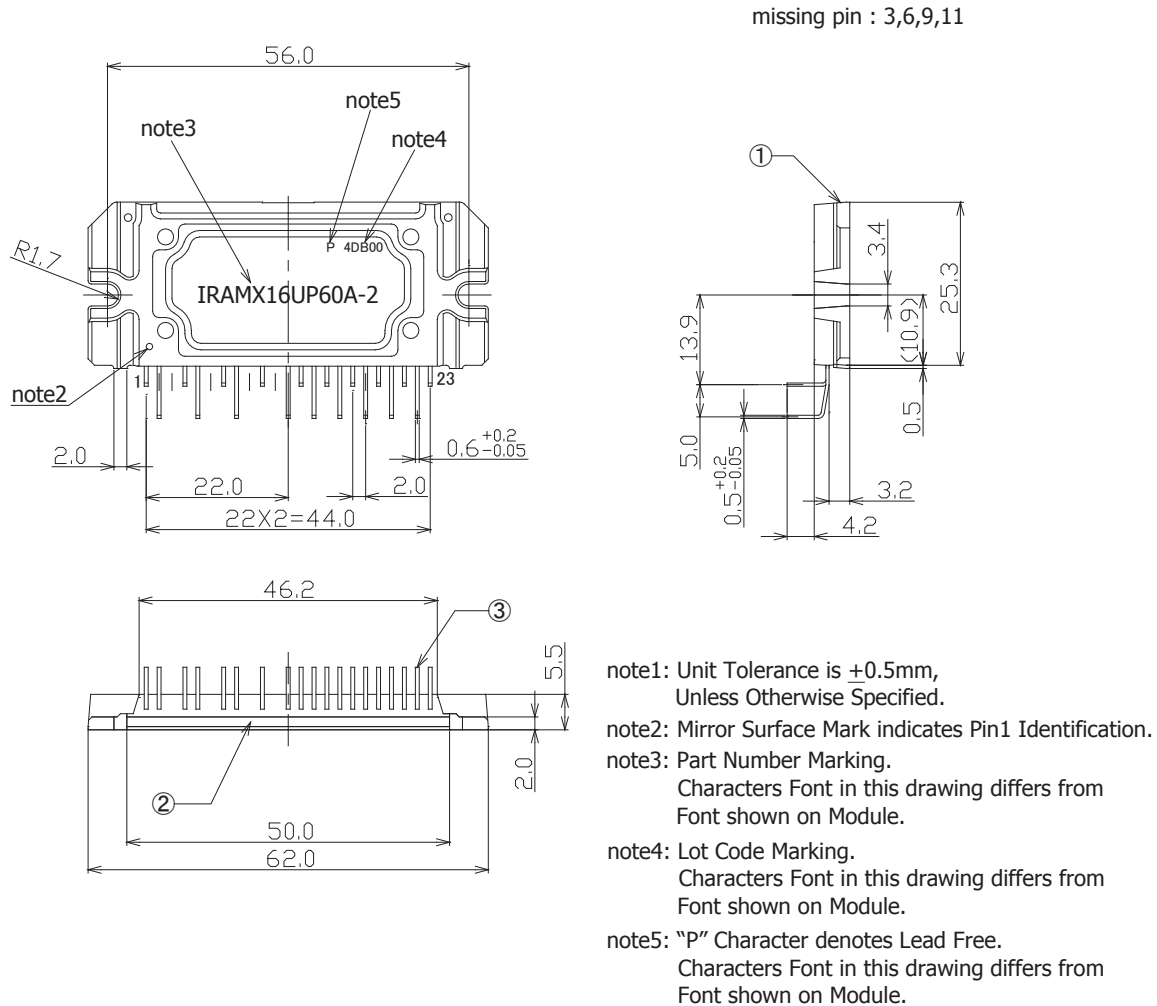
missing pin : 3,6,9,11



- note1: Unit Tolerance is ± 0.5 mm, Unless Otherwise Specified.
- note2: Mirror Surface Mark indicates Pin1 Identification.
- note3: Part Number Marking. Characters Font in this drawing differs from Font shown on Module.
- note4: Lot Code Marking. Characters Font in this drawing differs from Font shown on Module.
- note5: "P" Character denotes Lead Free. Characters Font in this drawing differs from Font shown on Module.

Dimensions in mm
For mounting instruction see AN-1049

Package Outline IRAMX16UP60A-2



Dimensions in mm
 For mounting instruction see AN-1049