



## Errata Sheet

1 July, 2000 / Release 1.2

**Device:** SAB-C517A-L  
**Stepping Code / Marking:** AA

**Package:** P-LCC-84  
P-MQFP-100

This Errata Sheet describes the deviations from the current user documentation. The classification and numbering system is module oriented in a continual ascending sequence over several derivatives, as well already solved deviations are included. So gaps inside this enumeration could occur.

The current documentation is: Data Sheet 10.97  
User's Manual 1.99  
Instruction Set Manual 05.98

**Note:** *Devices marked with EES- or ES are engineering samples which may not be completely tested in all functional and electrical characteristics, therefore they should be used for evaluation only.*

The specific test conditions for EES and ES are documented in a separate Status Sheet.

### Change summary to last Errata Sheet Rel. 1.1

- Changed the problem numbering system.
- Added two new items 'CCU. 1' and 'CCU. 2'.

## Functional Problems:

### CCU.1 Typing mistakes in the CCU configurations table (Table 6-5) on the User's Manual

On page 6-41 of the User's Manual, there are some typing mistakes on the descriptions for the Possible Modes in CCU configurations table, Table 6-5. These errors would not affect the functions of the C517A device, the possible modes supported by the Timers are correctly mentioned in the text. The correct typing is high-lighted in ***Bold Italic*** font style as shown in the table below.

**Table 6-5**  
**CCU Configurations**

Assigned Timer	Compare Register	Compare Output at	Possible Modes
Timer 2	CRCH/CRCL	P1.0/INT3#/CC0	Compare mode 0, 1 / <b><i>capture</i></b> + Reload
	CCH1/CCL1	P1.1/INT4/CC1	Compare mode 0, 1 / capture
	CCH2/CCL2	P1.2/INT5/CC2	Compare mode 0, 1 / capture
	CCH3/CCL3	P1.3/INT6/CC3	Compare mode 0, 1 / capture
	CCH4/CCL4	P1.4/INT2#/CC4	Compare mode 0, 1 / capture (see 6.3.4.1 and 6.3.4.2)
	CCH4/CCL4	P1.4/INT2#/CC4 P5.0/CCM0 to P5.7/CCM7	Compare mode 1 "Concurrent compare" (see 6.3.4.3)
	CMH0/CML0 to CMH7/CML7	P4.0/CM0 to P4.7/CM7	Compare mode <b><i>1</i></b> (see 6.3.4.4 and 6.3.4.4.2)
	COMSET COMCLR	P5.0/CCM0 to P5.7/CCM7	Compare mode 2 / " <b><i>Concurrent compare</i></b> " (see 6.3.4.5)
Compare Timer	CMH0/CML0 to CMH7/CML7	P4.0/CM0 to P4.7/CM7	Compare mode <b><i>0</i></b> (see 6.3.4.4 and 6.3.4.4.1)

### Workaround:

None.

**CCU. 2: Typing mistakes on the description for the function control bits COCAH1, COCAL1 and COCAH0, COCAL0**

On page 6-43 of the User's Manual, there is a typing mistake on the description for the Compare/capture mode control bits COCAH1, COCAL1 and COCAH0, COCAL0. This would not affect the functions of the C517A device. The correct typing which high-lighted in ***Bold Italic*** font style is shown as below:

COCAH1, COCAL1                      Compare/capture mode for the CC register ***1***.

COCAH0, COCAL0                      Compare/capture mode for the CC register ***0***.

**Workaround:**

None.

## Deviation from Electrical- and Timing Specification:

None.

## History List (since last CPU Step ES-AA)

### Functional Problems

Functional Problem	Short Description	Fixed
CCU. 1	Typing mistakes in the CCU configurations table (Table 6-5) on the User's Manual.	
CCU. 2	Typing mistakes on the description for the function control bits COCAH1, COCAL1 and COCAH0, COCAL0.	

### AC/DC Deviations

AC/DC Deviation	Short Description	Fixed
	None	

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