

Application Note AN-1098

IRS2101 and IR2101 Comparison

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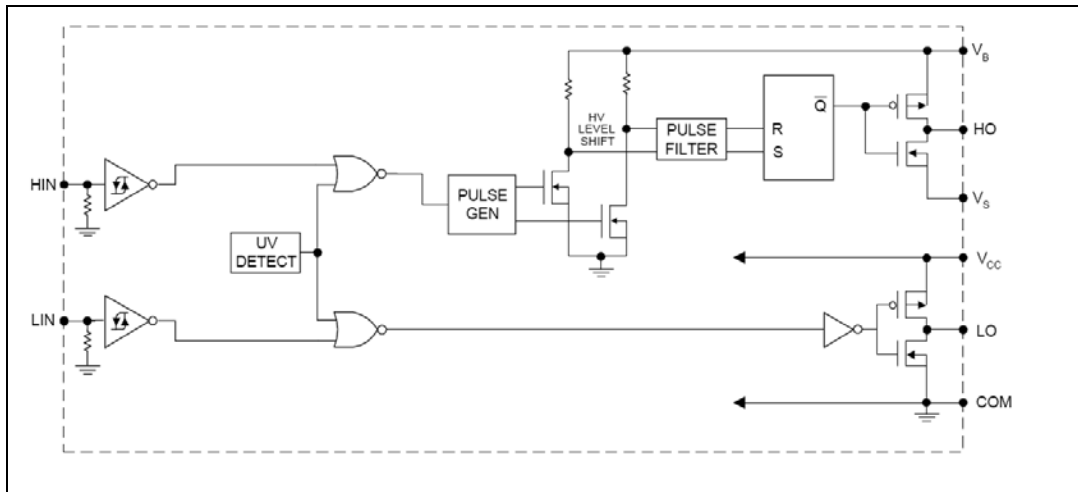
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Introduction

The IRS2101 is a new HVIC product that replaces the IR2101 and is pin-to-pin compatible with its corresponding predecessor. In many cases, little or no change is necessary to use the new product. This application note describes the various differences between the IRS2101 and the IR2101 HVICs.

The IRS2101 is a high voltage, high speed power MOSFET and IGBT driver with independent high and low side referenced output channels. Proprietary HVIC and latch immune CMOS technologies enable ruggedized monolithic construction. The logic input is compatible with standard CMOS or LSTTL outputs, down to 3.3 V logic. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high side configuration which operates up to 600 V.

Block Diagram



The IRS2101 and IR2101 share the same block diagram. The functionality of the two ICs is the same.

Electrical Characteristic Differences

All measurement conditions remain unchanged unless noted. Parameters not mentioned have not changed.

Absolute Maximum Ratings

There are no changes in the Absolute Maximum Ratings.

Recommended Operating Conditions

There are no changes in the Recommended Operating Conditions.

Dynamic Electrical Characteristics

Parameter		IR2101		IRS2101		Units
Symbol	Definition	typ	max	typ	max	
t_r	Turn-on rise time	100	170	70	170	ns
t_f	Turn-off fall time	50	90	35	90	

The IRS2101 has faster rise and fall times when compared to the IR2101.

Static Electrical Characteristics

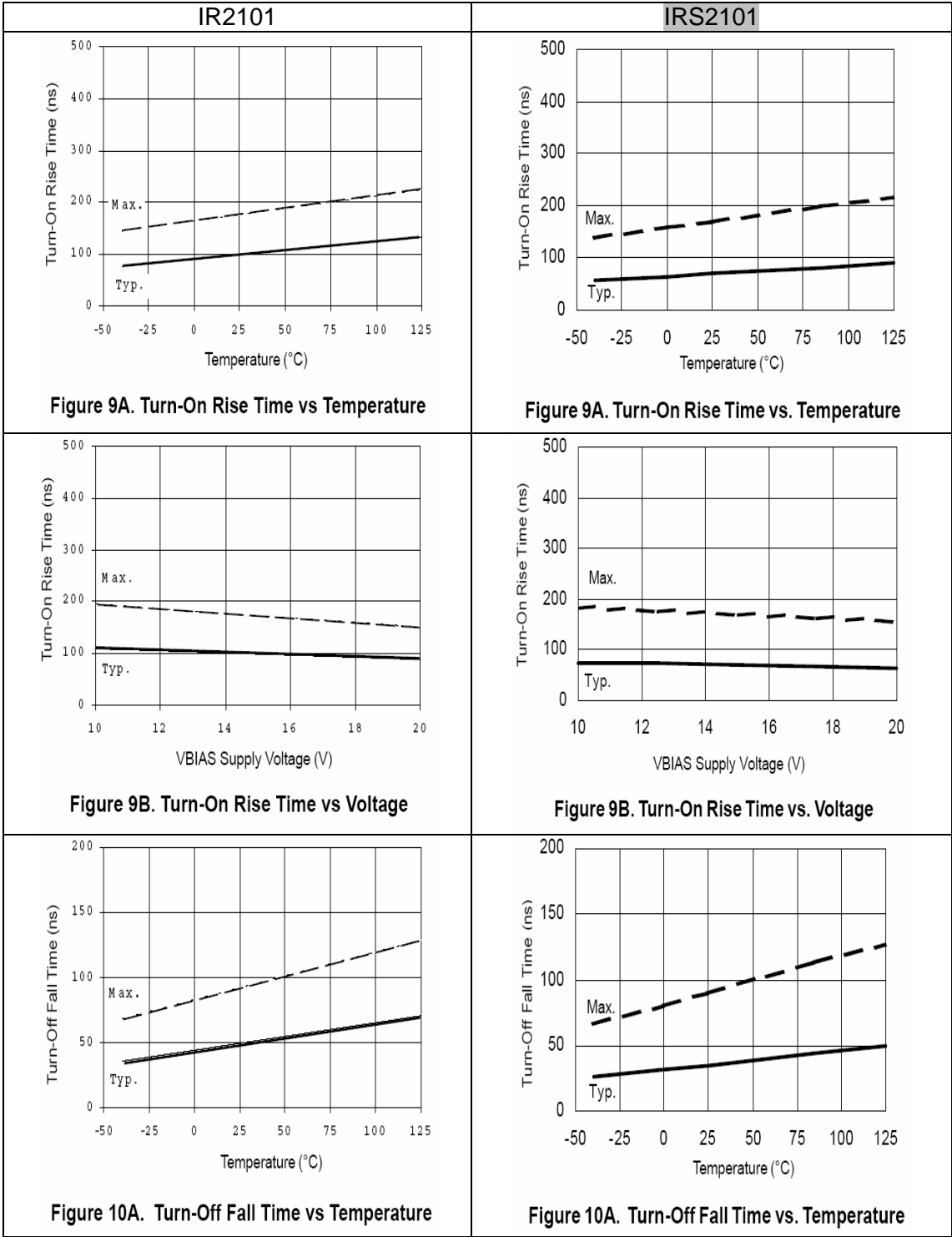
Parameter		IR2101			IRS2101			Units
Symbol	Definition	min	typ	max	min	typ	max	
V_{IH}	Logic "1" input voltage ($V_{CC} = 10\text{ V to }20\text{ V}$)	3	-	-	2.5	-	-	V
V_{IL}	Logic "0" input voltage ($V_{CC} = 10\text{ V to }20\text{ V}$)	-	-	0.8	-	-	0.8	
V_{OH}	High level output voltage, $V_{BIAS} - V_O$	-	-	0.1	-	0.05	0.2	
		I _o = 0 mA			I _o = 2 mA			
V_{OL}	Low level output voltage, V_O	-	-	0.1	-	0.02	0.1	mA
		I _o = 0 mA			I _o = 2 mA			
I_{O+}	Output high short circuit pulsed current ($V_O = 0\text{ V}$, $V_{IN} = \text{Logic "1"}$, $PW \leq 10\mu\text{s}$)	130	210	-	130	290	-	
I_{O-}	Output low short circuit pulsed current ($V_O = 15\text{ V}$, $V_{IN} = \text{Logic "0"}$, $PW \leq 10\mu\text{s}$)	270	360	-	270	600	-	

Some Static Electrical Characteristics are different for the IR2101 and the IRS2101 HVICs. With the IRS2101,

- V_{IH} has been reduced to 2.5 V for better 3.3 V logic compatibility.
- The V_{OH} and V_{OL} are tested using a new standardized test condition of I_o=2 mA. The output driver's on resistance is lower for IRS2101, which improves immunity against the Miller effect.
- The typical values for I_{o+} and I_{o-} are increased, which allows faster switching.

Figures

This figures shown in this section compare figures shown in the IR2101 (left column) and IRS2101 (right column) datasheets. Illustrations that have not changed between the two datasheets have not been included in this section.



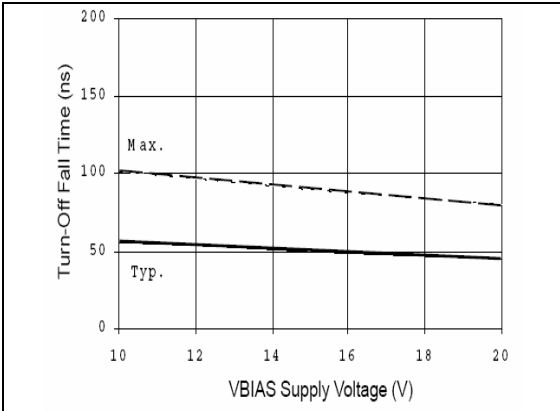


Figure 10B. Turn-Off Fall Time vs Voltage

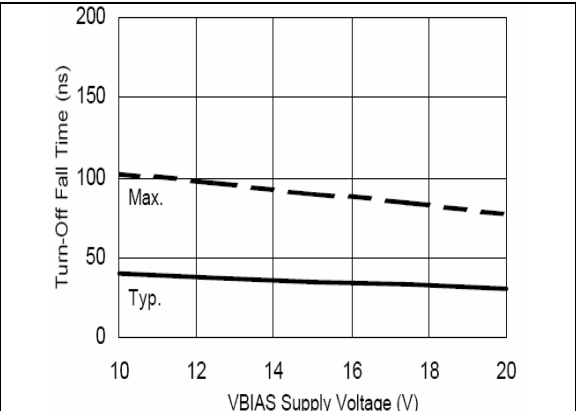
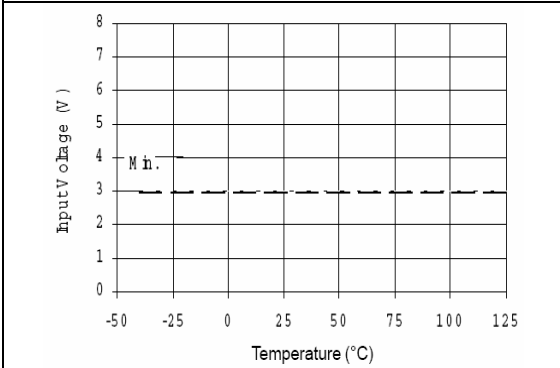
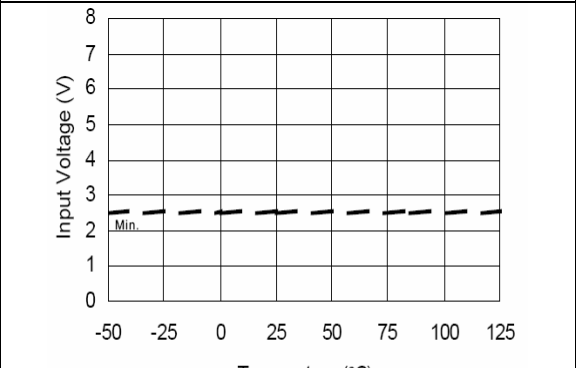


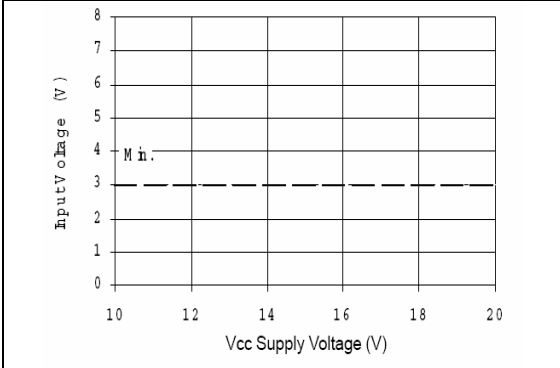
Figure 10B. Turn-Off Fall Time vs. Voltage



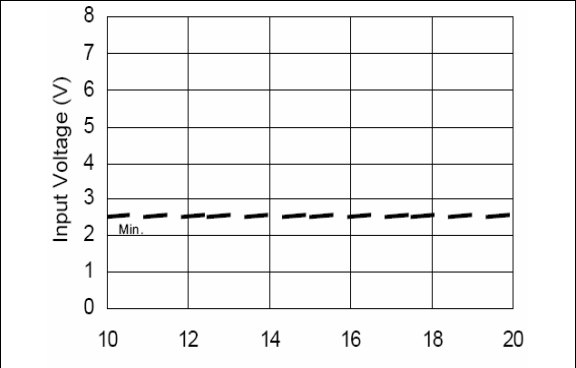
**Figure 12A. Logic "1" Input Voltage (IR2101)
Logic "0" Input Voltage (IR2102)
vs Temperature**



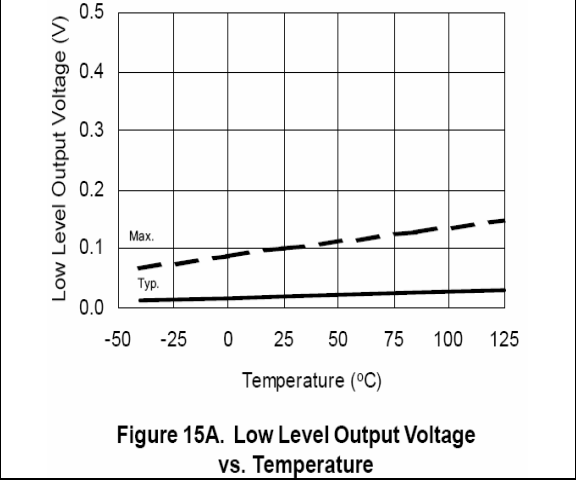
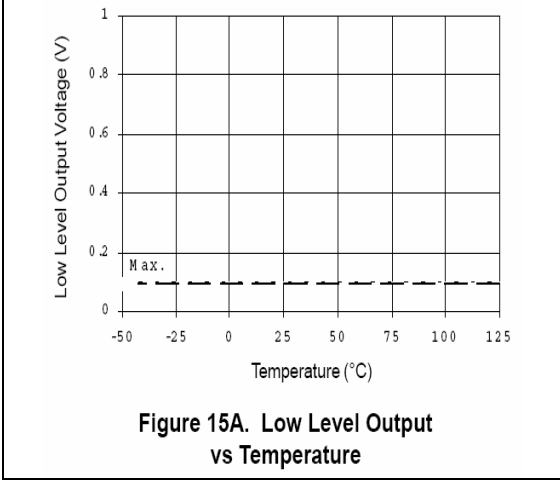
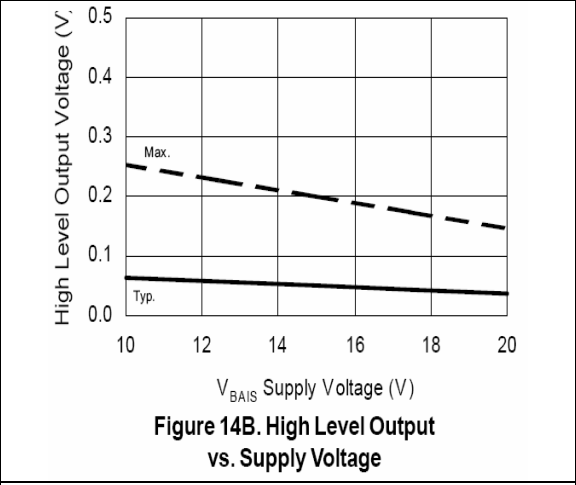
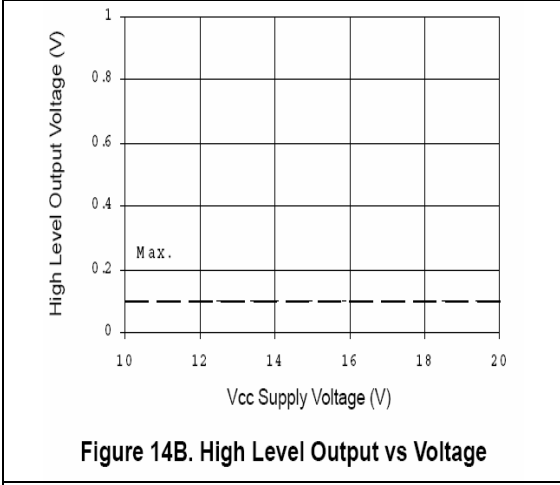
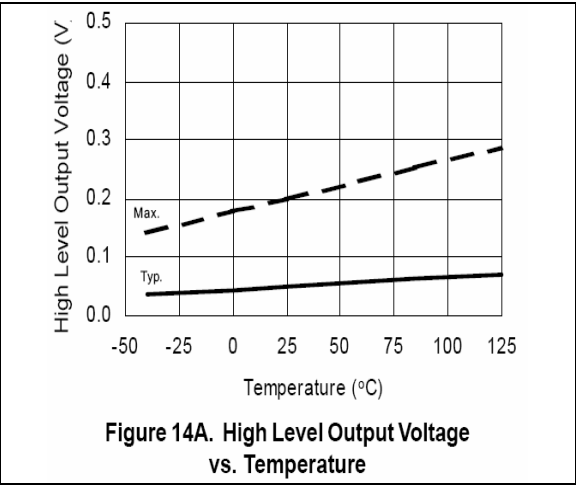
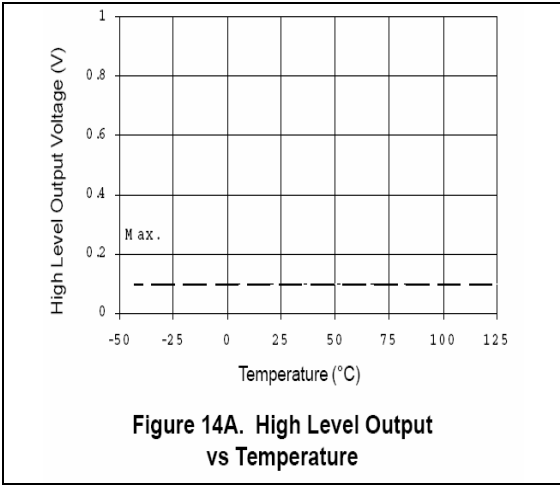
**Figure 12A. Logic "1" Input Voltage
vs. Temperature**

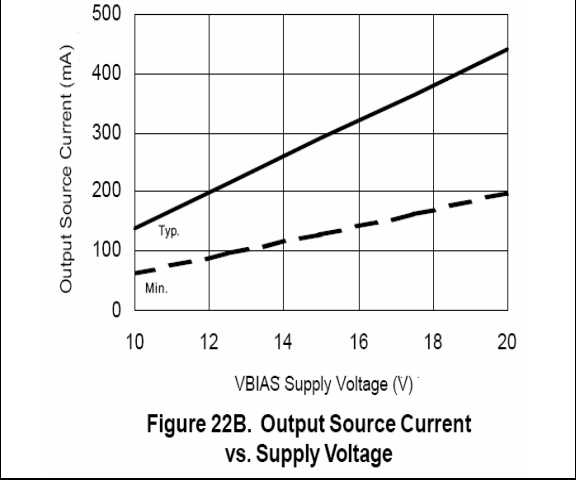
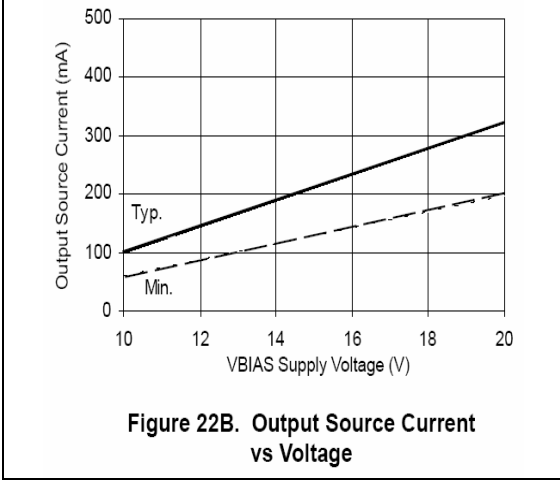
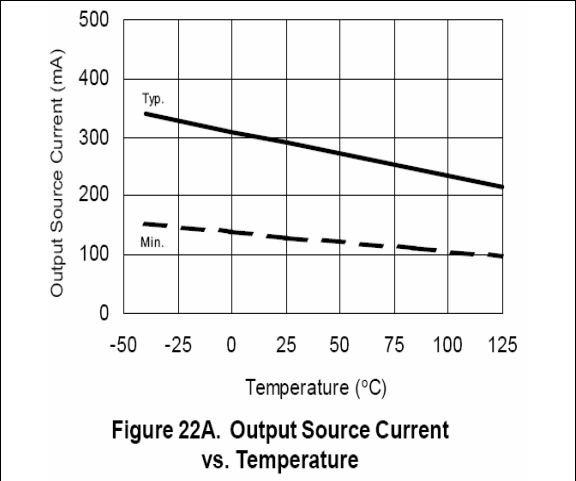
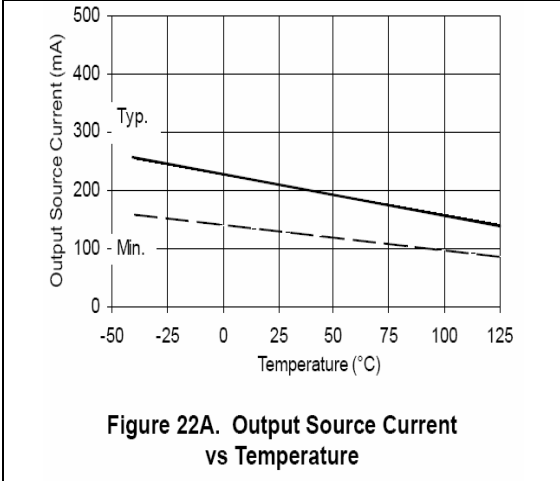
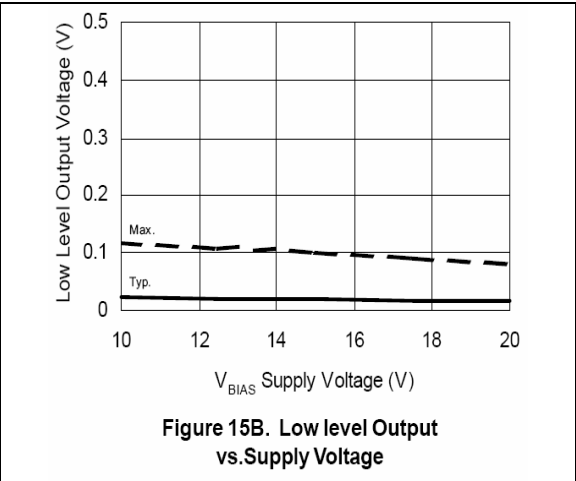
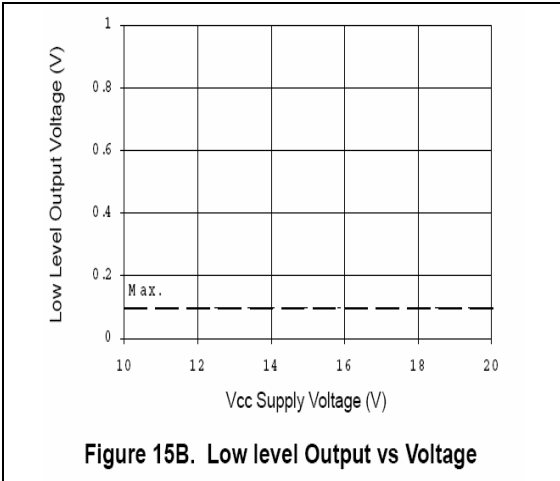


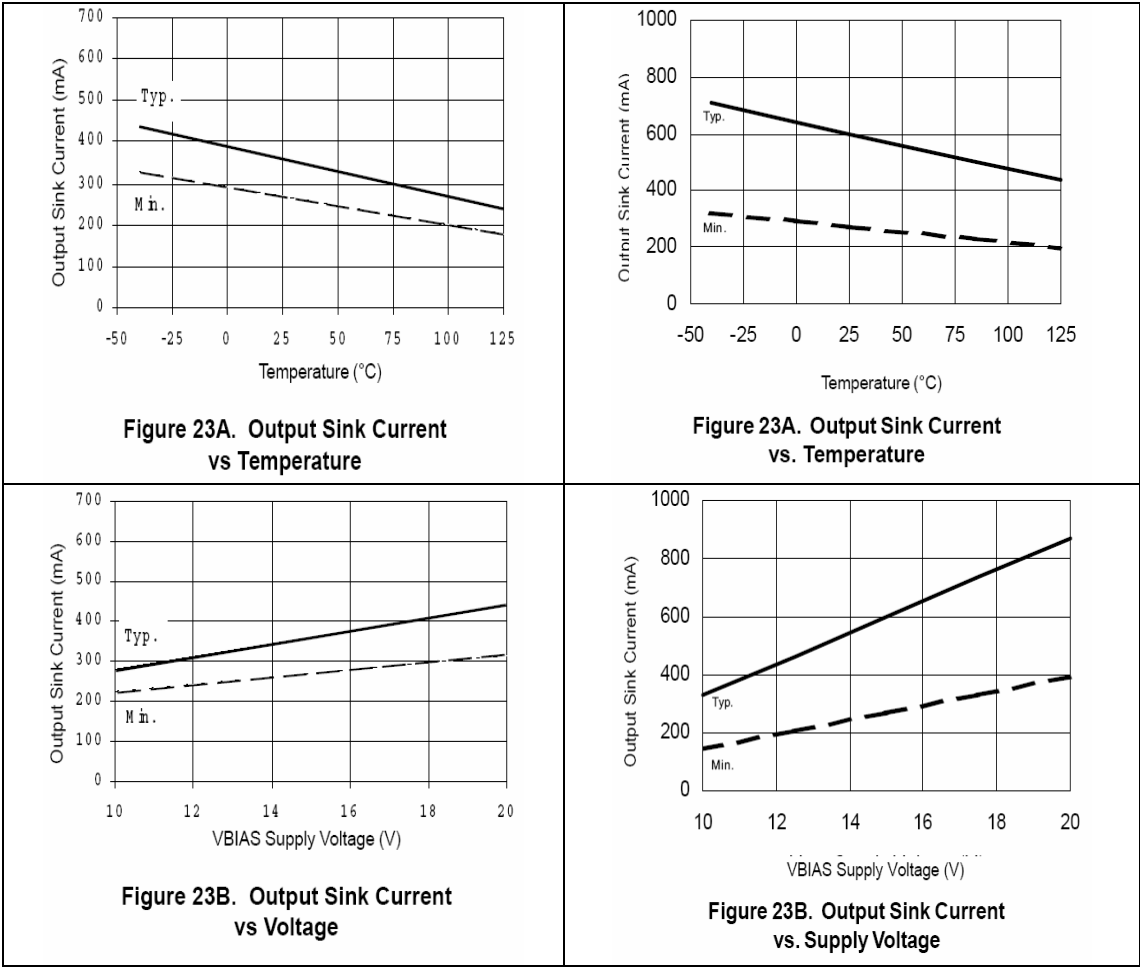
**Figure 12B. Logic "1" Input Voltage (IR2101)
Logic "0" Input Voltage (IR2102)
vs Voltage**



**Figure 12B. Logic "1" Input Voltage
vs. Voltage**







Summary

As shown by this document, the IRS2101 and the IR2101 are very similar with only a few negligible parametric differences.