XMC1000, XMC4000

Microcontroller Series
for Industrial Applications

Migration from Timer2 to CCU4/CCU8

Migration Guide

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Microcontrollers

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Revision History

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V1.0, 2013-06	

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1 Overview

The Timer2 peripheral, a 16-bit general purpose timer found in the Infineon XC800 8-bit family of microcontrollers, is often used for signal monitoring/conditioning, and for Pulse Width Modulation (PWM) signal generation. The CCU4 and CCU8 peripherals found in the XMC1000 and XMC4000 family of products from Infineon, provide all the functionality of the Timer2 peripheral, but also offer many new feature and performance enhancements.

1.1 Intended audience

This document is intended for users who are already familiar with the Timer2 module, and want to know how to implement similar functionality in XMC1000 or XMC4000 products.

1.2 Scope and purpose

Although the CCU4 and CCU8 support all of the Timer2 peripheral functionality, it is not possible to create a simple formula or table for a one-to-one conversion between peripherals. For example, it is not possible to say: “If bit x is set in the Timer2, set bit y in the CCU4 to achieve the same functionality”.

Instead we focus on a feature comparison between the Timer2 and the CCU4 and CCU8, with references to specific User Guides that describe the detail of how to implement the required feature.

We focus specifically on the following Timer2 features:

- **Using software to start and stop the timer**
- **Using hardware to start the timer**
- **Up/Down count enabled**
- **Captured Mode**
- **Counter Mode**
- **Reloading the register on overflow**
- **Prescaler Option**

1.3 References

For the complete list of available documents, please refer to the XMC web pages:

- **XMC1000** (See the ‘User Guide’ section for peripheral Device Guides)
- **XMC4000** (See the ‘User Guide’ section for peripheral Device Guides)
- **www.infineon.com/xmc**

2 Using software to start and stop the timer

Timer2

- T2_T2CON.TR2 = 1
  - Set the run bit of the timer to start the timer
- T2_T2CON.TR2 = 0
  - Clear the run bit of the timer to stop the timer

CCU4 and CCU8

For the CCU4 (and CCU8) module, the timer start and stop is controlled from a different register bit:

- CC4yTCSET.TRBS = 1
  - Set the run bit of the timer to start the timer
- CC4yTCCLR.TCC = 1
  - Clear the run bit of the timer to stop the timer

Note: For CCU8, replace '4' in the name with '8' to derive the register name; e.g. CC4yTCSET becomes CC8yTCSET.

3 Using hardware to start the timer

Timer2

Timer2 is started by hardware when T2_T2MOD.T2RHEN is set.

T2_T2MOD.T2REGS defines the event on pin T2EX, falling or rising edge, which can set the run bit TR2 by hardware.

- T2RHEN = 0
  - Timer2 external start is disabled
- T2RHEN = 1
  - Timer2 external start is enabled
- T2REGS = 0
  - The falling edge external start at pin T2EX is selected
- T2REGS = 1
  - The rising edge external start at pin T2EX is selected

CCU4 and CCU8

To select an external start function, map one of the events (output of the input selector) to a specific input signal, by:

- Setting the required value in the CC4yINS.EVxIS field
- Indicating the active edge of the signal on the CC4yINS.EVxEM field

This event is then mapped to the start functionality by setting CC4yCMC.STRTS.

Note: For CCU8, replace '4' in the name with '8' to derive the register name; e.g. CC4yINS becomes CC8yINS.

Document references

For more information please refer to the following chapters of the respective User Guides (See the 'User Guide' section of the **XMC1000** or **XMC4000** Infineon web pages):

- **Device Guide – CCU4**
  - External Events Control
- **Device Guide – CCU8**
  - External Events Control

4 Up/Down count enabled

Timer2

- The up-down count selection is enabled when T2_T2MOD.DCEN = 1.
- The direction of count is determined by the level at input pin T2EX.
- The logic 1 at pin T2EX sets the Timer2 to ‘up’ counting mode.
- The logic 0 at pin T2EX sets the Timer2 to ‘down’ counting mode.

CCU4 and CCU8

To select as ‘up’ or ‘down’ count functionality, map one of the events (output of the input selector) to a specific input signal, by:

- Setting the required value in the CC4yINS.EVxIS field
- Indicating the active edge of the signal on the CC4yINS.EVxEM field.

This event is then mapped to the up/down functionality by setting the CC4yCMC.UDS.

Note: For CCU8, replace ‘4’ in the name with ‘8’ to derive the register name; e.g. CC4yINS becomes CC8yINS.

5 Captured Mode

Timer2

When T2_T2MOD.CP =1 and T2_T2MOD.EXEN2 = 1, Timer2 functions as a 16-bit timer and always counts up to 0xFFFF.

After that point an overflow condition occurs and Timer2 reloads its register with 0x0000.

With a falling/rising edge on pin T2EX, the contents of the timer register (THL2) are captured into the RC2 register.

CCU4 and CCU8

To implement capture mode functionality, map one of the events (output of the input selector) to a specific input signal, by:

- Setting the required value in the CC4yINS.EVxIS field
- Indicating the active edge of the signal on the CC4yINS.EVxEM field

This event is then mapped to the capture functionality by setting CC4yCMC.CAP0S.

The contents of the timer register will be captured into Capture Register 1 (CC4yC1V.CAPTV).

Note: For CCU8, replace '4' in the name with '8' to derive the register name; e.g. CC4yINS becomes CC8yINS.

Document references

For more information please refer to the following chapters of the respective User Guides (See the 'User Guide' section of the **XMC1000** or **XMC4000** Infineon web pages):

- **Device Guide – CCU4**
  - Advanced Signal Measurement using the Capture Mode
- **Device Guide – CCU8**
  - Advanced Signal Measurement using the Capture Mode

6 Counter Mode

Timer2

When C/T2 = 1, Timer2 behaves as a counter that counts 1-to-0 transition at the input pin T2.

CCU4 and CCU8

To implement counting event functionality, map one of the events (output of the input selector) to a specific input signal, by:

- Setting the required value in the CC4yINS.EVxIS field
- Indicating the active edge of the signal on the CC4yINS.EVxEM field

This event must then mapped to the external count functionality by setting CC4yCMC.CNTS.

Note: For CCU8, replace '4' in the name with '8' to derive the register name; e.g. CC4yINS becomes CC8yINS.

7 Reloading the register on overflow

Timer2

The Timer2 auto-reload mode is selected with the bit CP/RL2 in register T2CON=0.

The timer will count to an overflow value (0xFFFF) and then reload the count register contents with a 16-bit start value for a fresh counting sequence.

CCU4 and CCU8

In the edge aligned mode (CC4yTC.TCM = 0), the CCU4 timer will count up from 0x0000 until it matches the value programmed in the period register (CC4yPR).

When the period match is detected the timer is cleared to 0x0000 and then it begins incrementing again.

Note: For CCU8, replace '4' in the name with '8' to derive the register name; e.g. CC4yTC becomes CC8yTC.

8 Prescaler Option

Timer2

T2_T2MOD.PREN allows the user to select whether to use the default divider setting (1/12) or follow the configuration of T2PRE bits.

- When PREN =0, the default 12 divider takes effect.
- When PREN = 1, it will follow the T2PRE bits for the configuration of the prescaler divider.

000	$f_{T2} = f_{T2CCU}$
001	$f_{T2} = f_{T2CCU}/2$
010	$f_{T2} = f_{T2CCU}/4$
011	$f_{T2} = f_{T2CCU}/8$
100	$f_{T2} = f_{T2CCU}/16$
101	$f_{T2} = f_{T2CCU}/32$
110	$f_{T2} = f_{T2CCU}/64$
111	$f_{T2} = f_{T2CCU}/128$

Figure 1 Prescaler divider option for Timer2

CCU4 and CCU8

CC4yPSC.PSIV is used to configure the different possible division values for the timer clock prescaler.

CC4yPSC.PSIV	Resulting clock
0000_B	f_{ccu4}
0001_B	$f_{ccu4}/2$
0010_B	$f_{ccu4}/4$
0011_B	$f_{ccu4}/8$
0100_B	$f_{ccu4}/16$
0101_B	$f_{ccu4}/32$
0110_B	$f_{ccu4}/64$
0111_B	$f_{ccu4}/128$
1000_B	$f_{ccu4}/256$
1001_B	$f_{ccu4}/512$
1010_B	$f_{ccu4}/1024$
1011_B	$f_{ccu4}/2048$
1100_B	$f_{ccu4}/4096$
1101_B	$f_{ccu4}/8192$
1110_B	$f_{ccu4}/16384$
1111_B	$f_{ccu4}/32768$

Figure 2 Prescaler divider option for CCU4

Note: For CCU8, replace '4' in the name with '8' to derive the register name; e.g. CC4yPSC becomes CC8yPSC.

9 Conclusion

The CCU4 and CCU8 peripherals offer the same functionality as the Timer2 peripheral, but with significant additional features. These peripherals are designed with repetitive structures that make automatic code generation easier.

The DAVE™ 3 tool with DAVE™ Apps can be used to handle the most common use cases for the CCU4 and CCU8. Example projects and Users Guides are available for free download from the Infineon website, to ease the learning curve and help speed-up development and implementation.

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