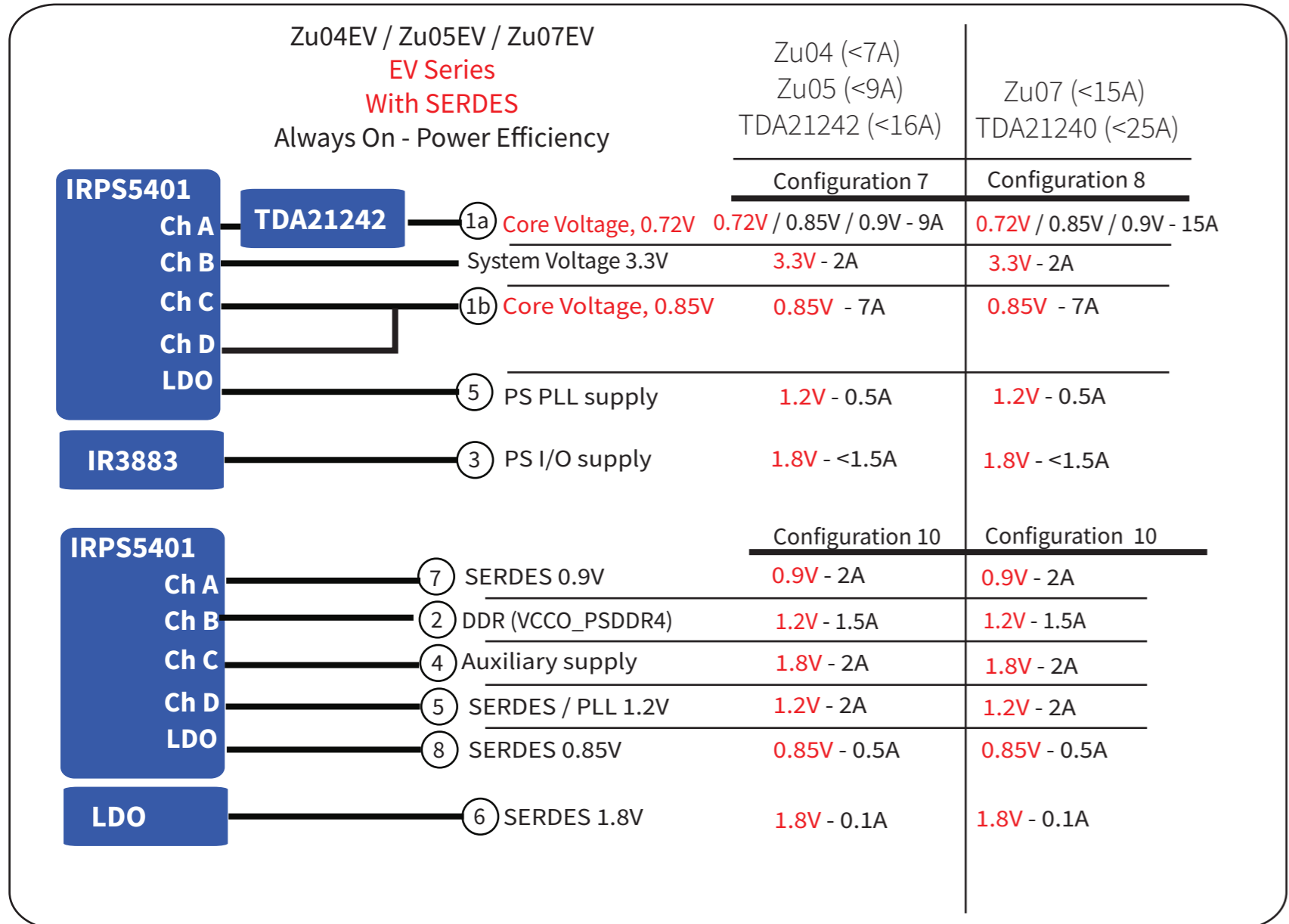
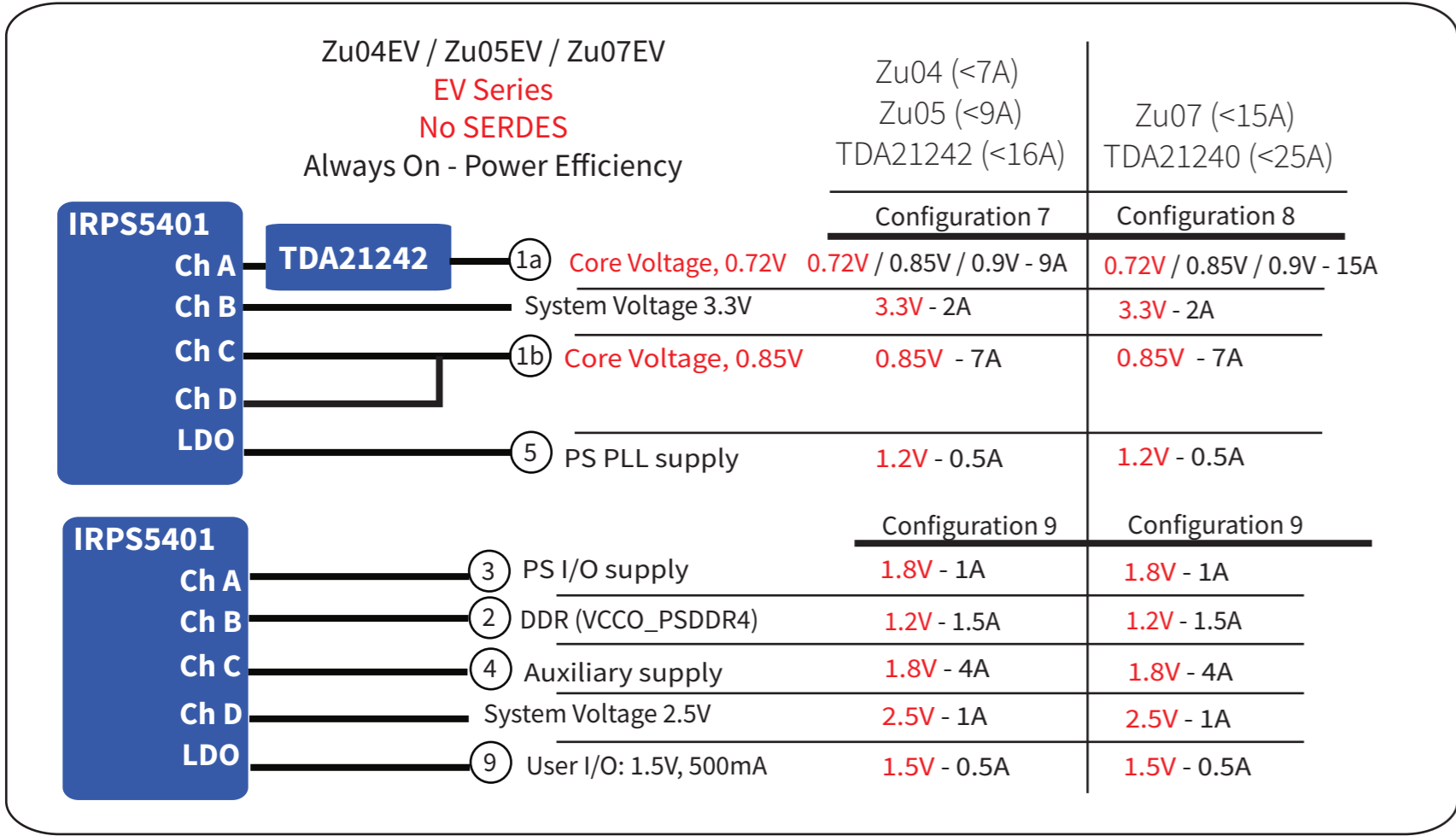


Xilinx
Zynq UltraScale+ MPSoC
Power Macros for
Zu02 to Zu09
CG / EG / EV SERIES

Part Number: 12 Configurations
IRPS5401MXI03TRP



Infineon DC/DC PMIC	MTP Resistor Address	Application – Voltage Outputs	PowerStage	EXT_A INDUCTOR	Zynq US+ config #
IRPS5401MXI03TRP	8870	1.8/1.8/.85/.85/1.2	N/A		1
	10000	1.2/.85/3.3/.85/1.8	N/A		2
	11000	1.8/5/3.3/.85/1.8	N/A		3
	2320	.85/1.2/1.8/1.8/1.2	TDA21242	VLB10050HT-R20M	4
	2870	.85/1.2/1.8/1.8/1.2	TDA21240	VLB10050HT-R12M	5
	3480	3.3/1.8/.9/1.2/.85	N/A		6
	4120	.72/3.3/.85/1.2	TDA21242	VLB10050HT-R30M	7
	4750	.72/3.3/.85/1.2	TDA21240	VLB10050HT-R20M	8
	5490	1.8/1.2/1.8/2.5/1.5	N/A		9
	6190	.9/1.2/1.8/1.2/.85	N/A		10
	6980	.72/1.2/.85/1.8/1.2	TDA21242	VLB10050HT-R30M	11
	7870	.72/1.2/.85/1.8/1.2	TDA21240	VLB10050HT-R20M	12

For Zu11 to Zu19:

For Zu15 use Zu09 Configuration 5 & 6

For Zu11 use Zu09 Configuration 5 & 6 (for SERDES <2A)
if SERDES < 6A, use IR38060 for MGTAVCC/VTT

For Zu17/Zu19 use Zu09 Configuration 5 & 6 (for SERDES <2A)
if SERDES < 6A, use IR38060 for MGTAVCC/VTT
if SERDES <10A, use IR38062 for MGTAVTT

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