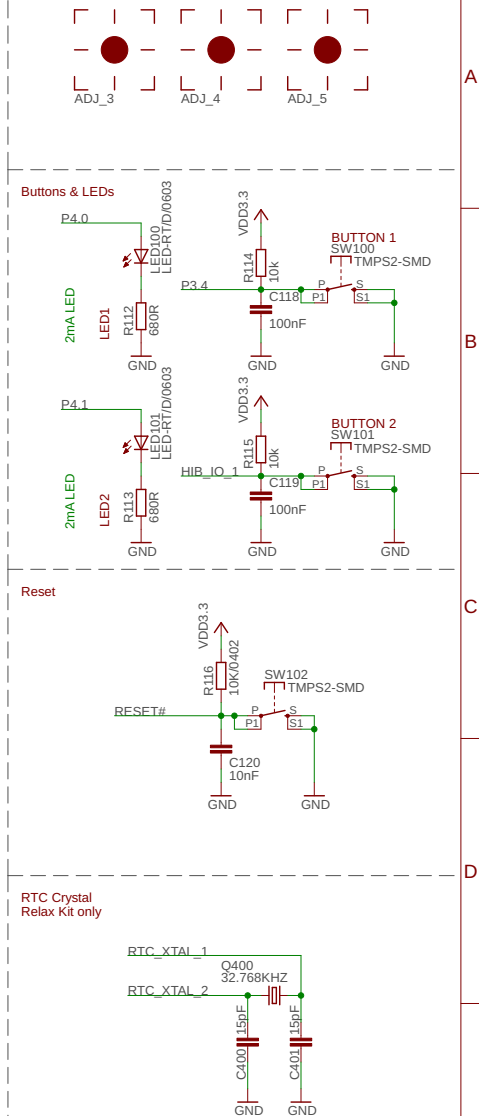
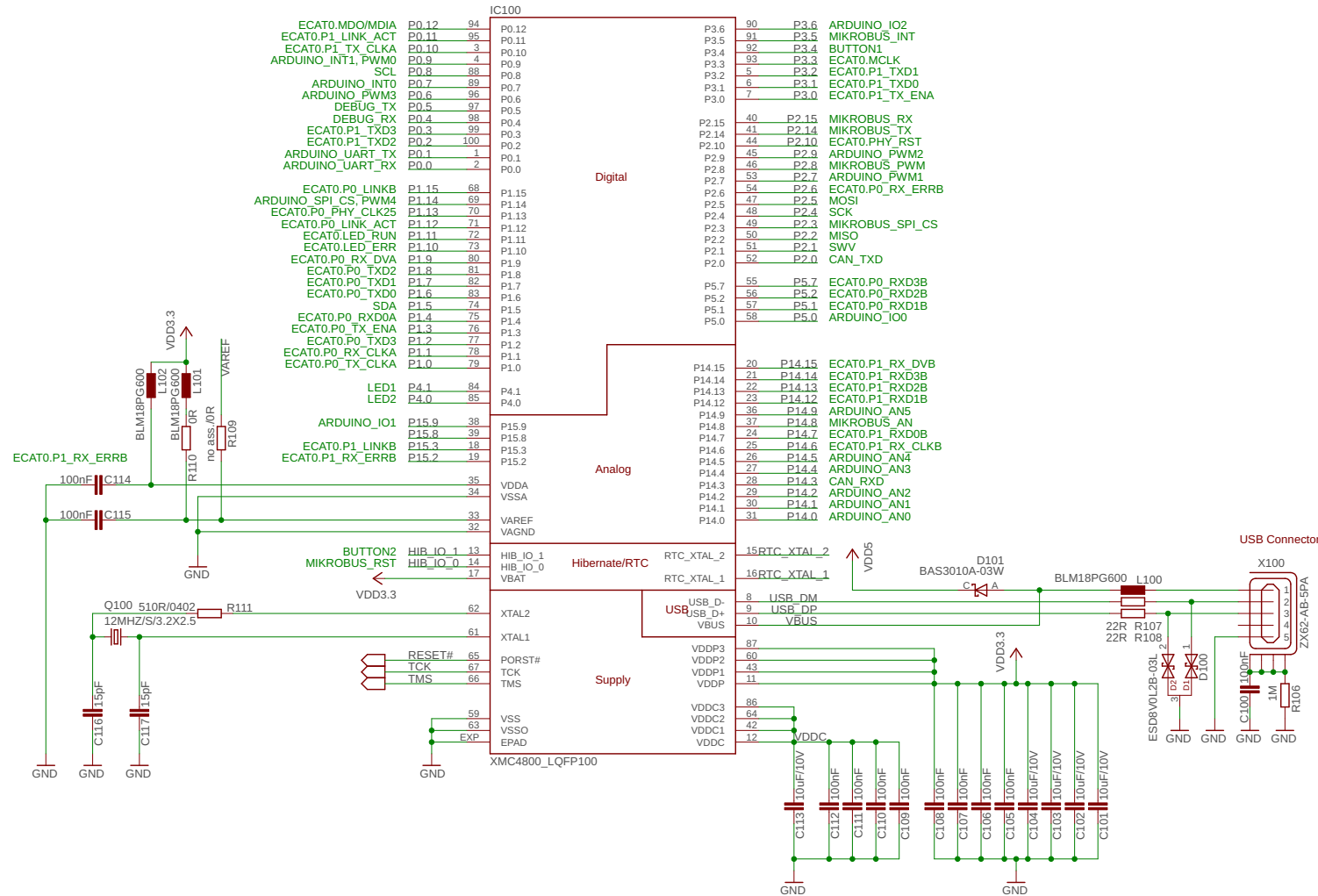


Target Device



V1 / 01.09.2015

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XMC4700 Relax Kit Series
XMC4800_IoT_Connectivity_Kit_v1.0
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The diagram illustrates the ECAT Port0 IN circuit. It features a BCM5241XA1KMLG transceiver (U251) connected to an ECAT module. The transceiver's TX and RX pins are connected to the PHY0_TD_P, PHY0_TD_N, PHY0_RD_P, and PHY0_RD_N signals. The ECAT module's P1.15, P1.12, P1.13, P1.7, P1.6, P1.8, P5.7, P5.2, P5.1, P1.4, P1.9, P2.6, P0.12, P3.3, and P1.1 pins are connected to the transceiver's TXEN, TXD0, TXD1, TXD2, TXD3, RXEN, RXD0, RXD1, RXD2, RXD3, RXD4, RXD5, RXD6, RXD7, RXD8, RXD9, RXD10, RXD11, RXD12, RXD13, RXD14, RXD15, RXD16, RXD17, RXD18, RXD19, RXD20, RXD21, RXD22, RXD23, RXD24, RXD25, RXD26, RXD27, RXD28, RXD29, RXD30, RXD31, RXD32, RXD33, RXD34, RXD35, RXD36, RXD37, RXD38, RXD39, RXD40, RXD41, RXD42, RXD43, RXD44, RXD45, RXD46, RXD47, RXD48, RXD49, RXD50, RXD51, RXD52, RXD53, RXD54, RXD55, RXD56, RXD57, RXD58, RXD59, RXD60, RXD61, RXD62, RXD63, RXD64, RXD65, RXD66, RXD67, RXD68, RXD69, RXD70, RXD71, RXD72, RXD73, RXD74, RXD75, RXD76, RXD77, RXD78, RXD79, RXD80, RXD81, RXD82, RXD83, RXD84, RXD85, RXD86, RXD87, RXD88, RXD89, RXD90, RXD91, RXD92, RXD93, RXD94, RXD95, RXD96, RXD97, RXD98, RXD99, RXD100, RXD101, RXD102, RXD103, RXD104, RXD105, RXD106, RXD107, RXD108, RXD109, RXD110, RXD111, RXD112, RXD113, RXD114, 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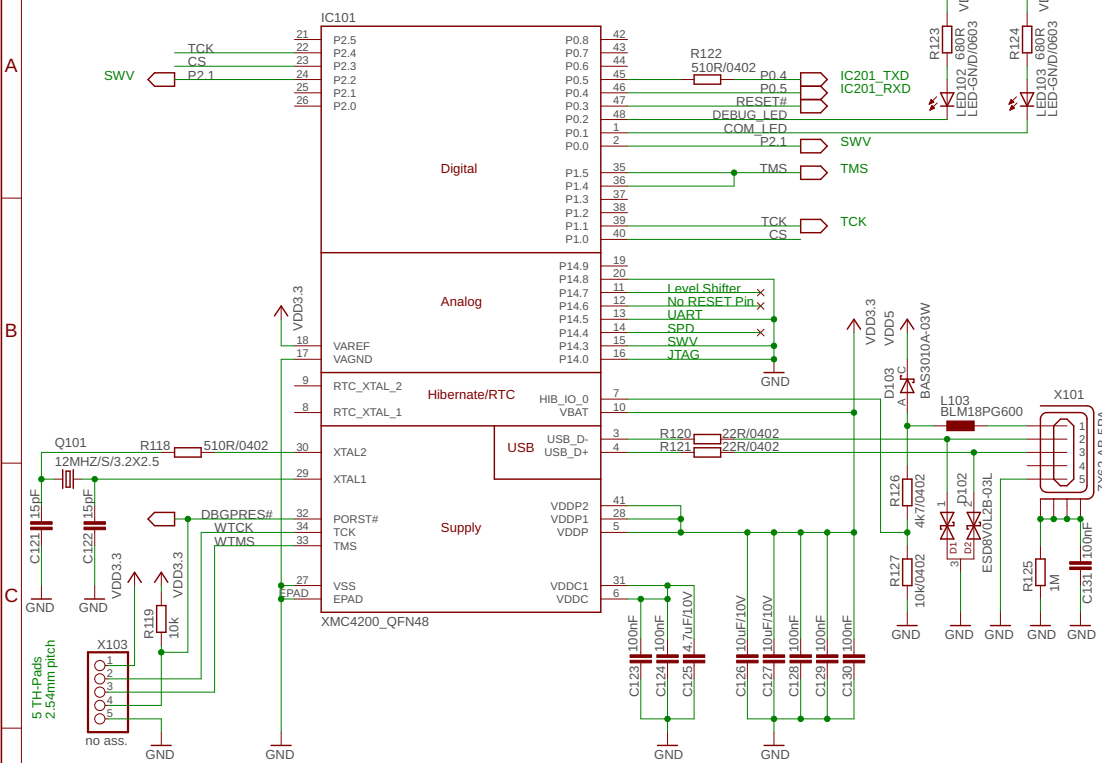
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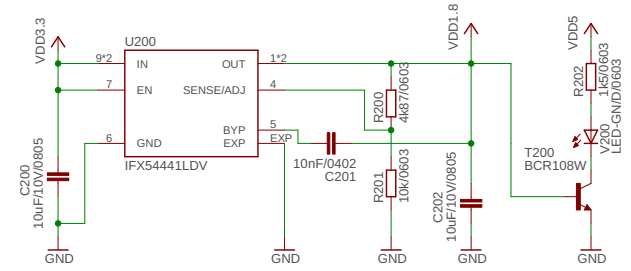
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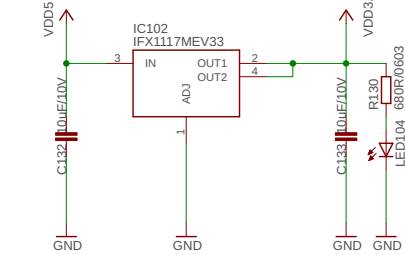
On-board Debugger



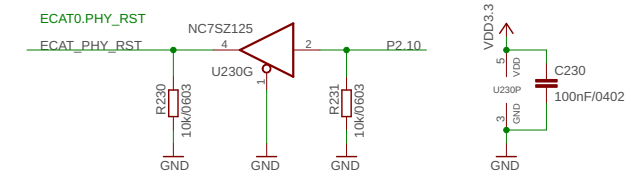
Power 1,8V



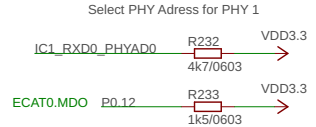
Power Target Device



Phy Reset Buffer



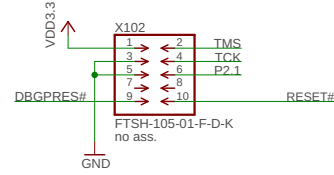
Phy Pull Resistors



On-board Debugger Concept

Cortex Debug

SPI Master	U0C0
MISO	DX0B-P1.4
MOSI	DOUT0-P1.5
CLK_OUT	SCLKOUT-P1.1
CS_OUT	SELO0-P1.0
SPI Slave	U0C1
CS_IN	DX2A-P2.3
CLK_IN	DX1A-P2.4
MISO	DOUT0-P2.5
MOSI	DX0A-P2.2
UART	U1C1
RXD	DX0D-P0.0
TXD	DX0A-P0.4
TXACTIVE#	DOUT0-P0.5
GPIO-P0.6	
RESET#	GPIO-P0.3
DEBUG_LED#	GPIO-P0.2
AUX_LED#	GPIO-P0.1



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