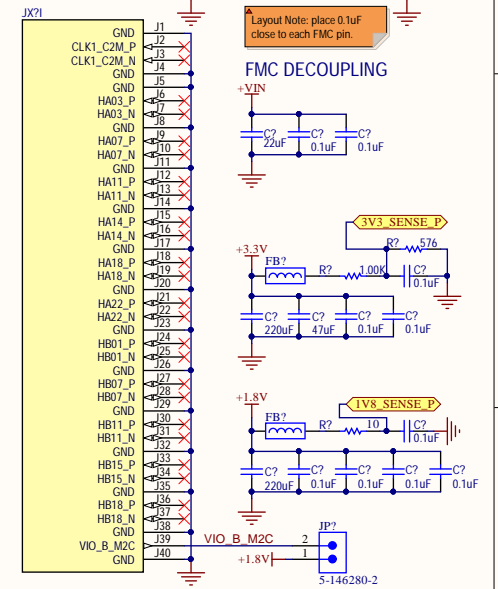
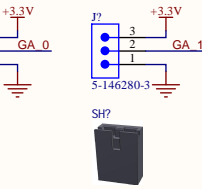
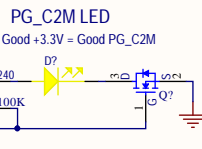
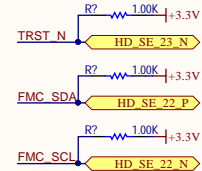
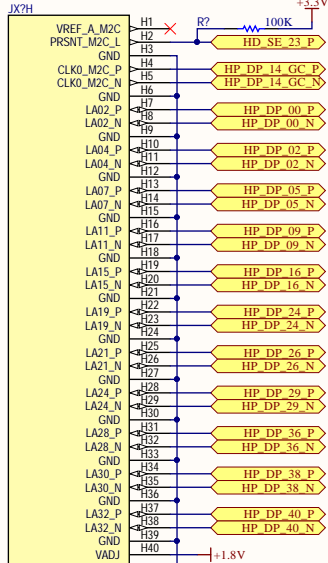
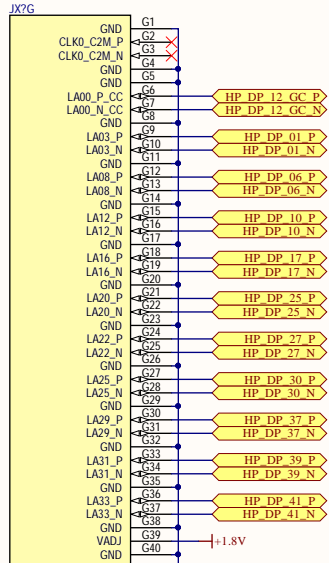
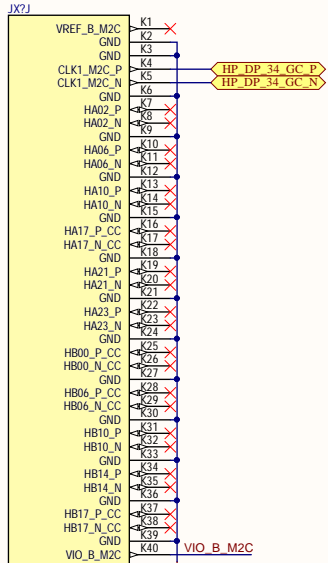
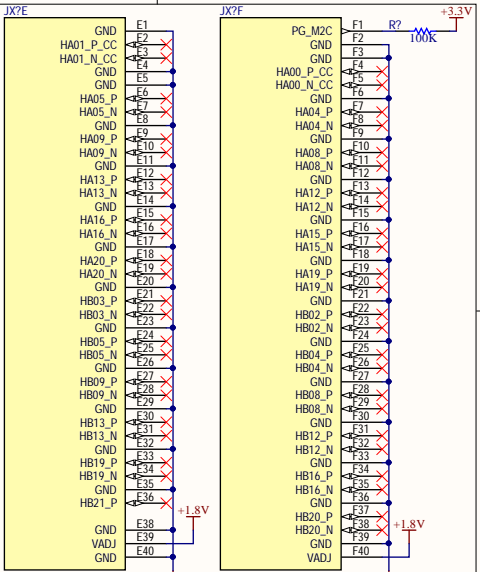
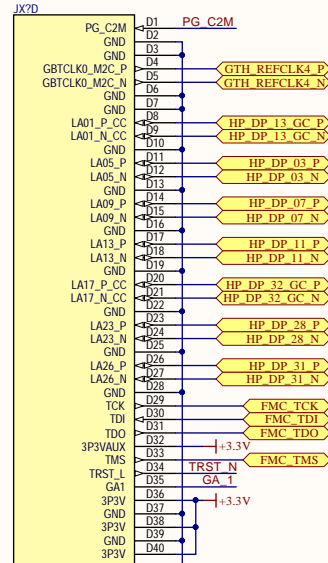
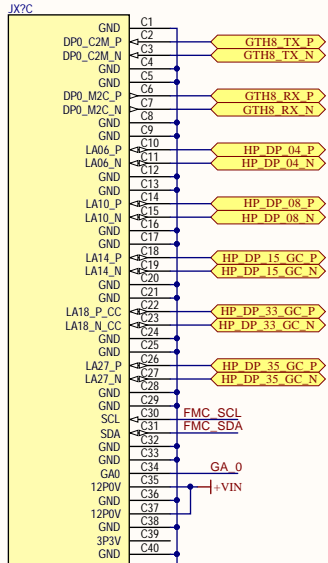
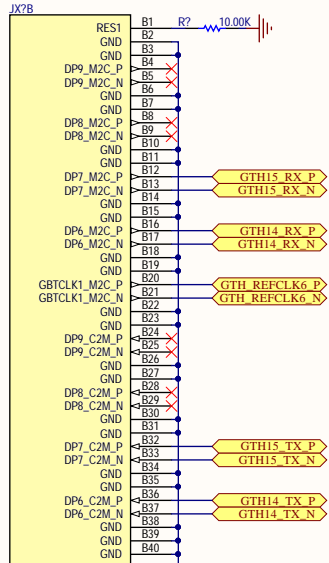
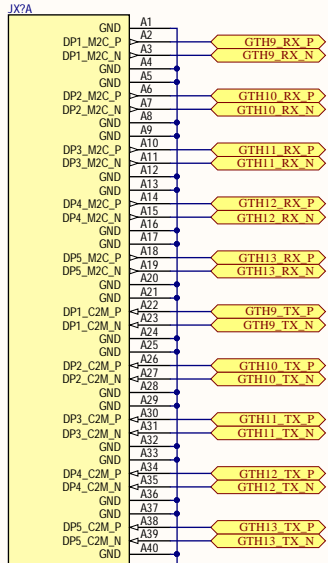


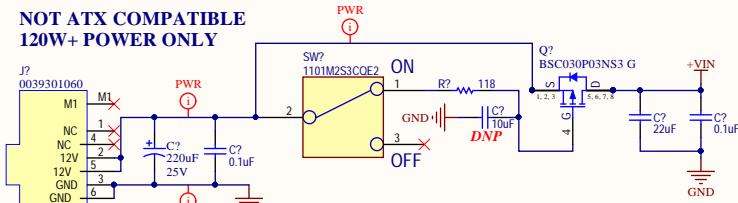
FMC HPC INTERFACE



POWER 1

POWER INPUT

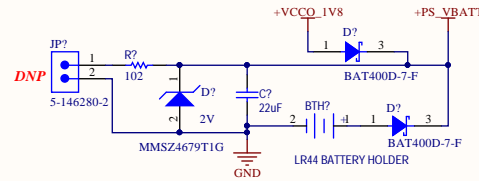
NOT ATX COMPATIBLE
120W+ POWER ONLY



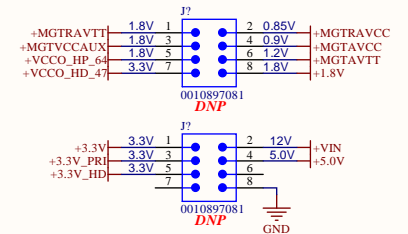
NOTE: NEW PINOUT FOR 120W+ PSU



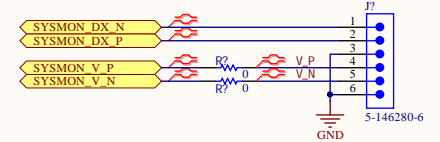
+PS_VBATT_TEST



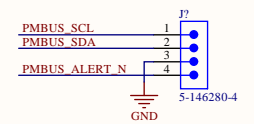
VOLTAGE MONITOR HEADERS



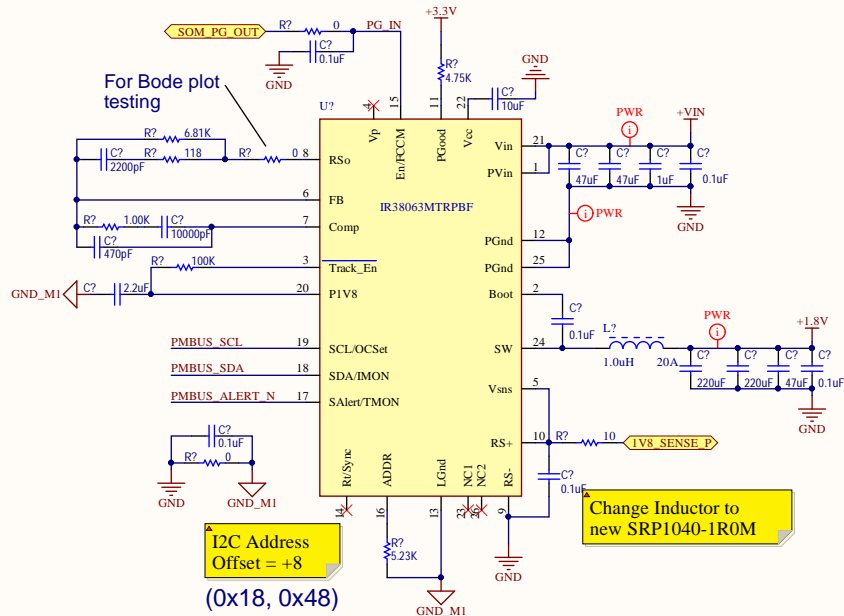
SYSMON HEADER



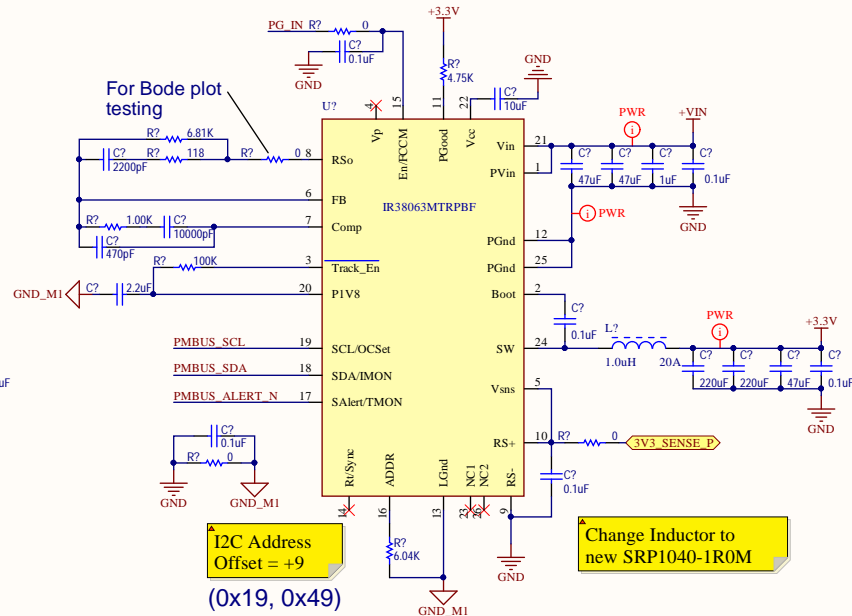
PMBus HEADER



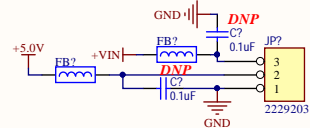
1.8V MAIN (HIGH CURRENT)



3.3V MAIN (HIGH CURRENT)

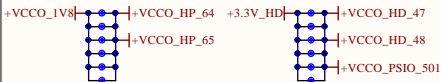


FAN HEADER



+1.8V NET TIES

+3.3V NET TIES

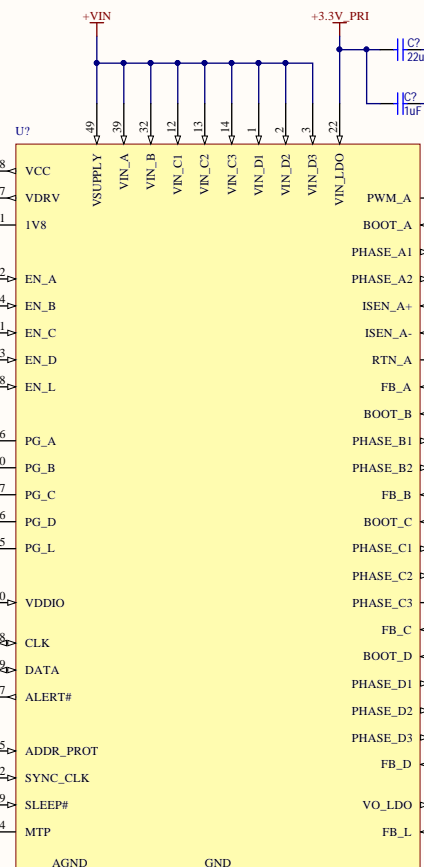
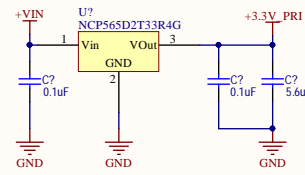


Layout Note: Net Ties at 0.5mm each. Tie as close as possible to each JX pin.

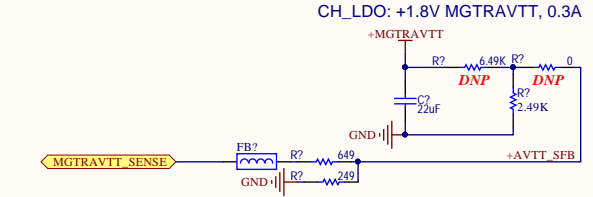
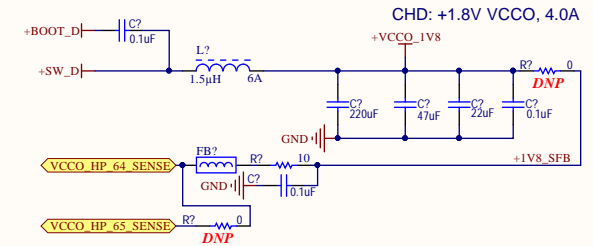
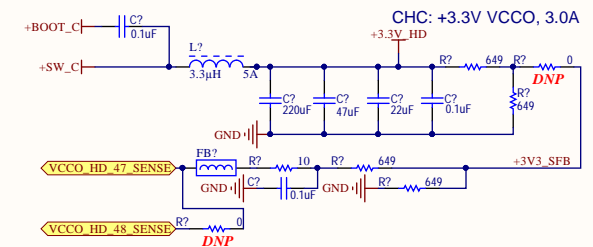
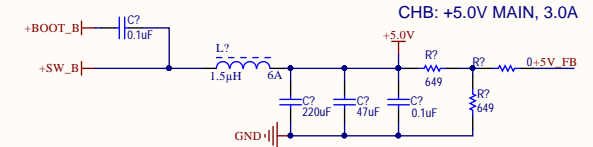
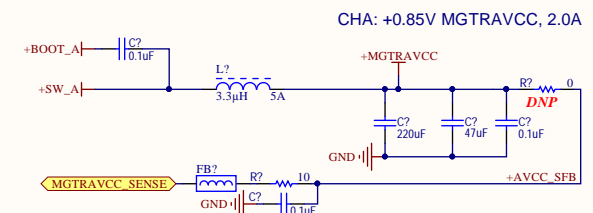
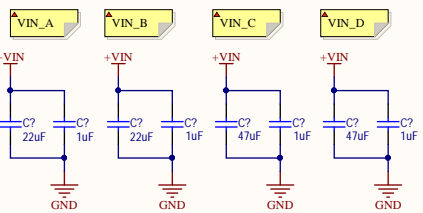
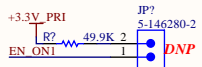
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Doc Num:	SCH-US3CAR	Date:	3/23/2017
Sheet Title:	17 - POWER1.SchDoc	Time:	10:22:59 AM
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POWER 2

+3.3V PRIMARY REG

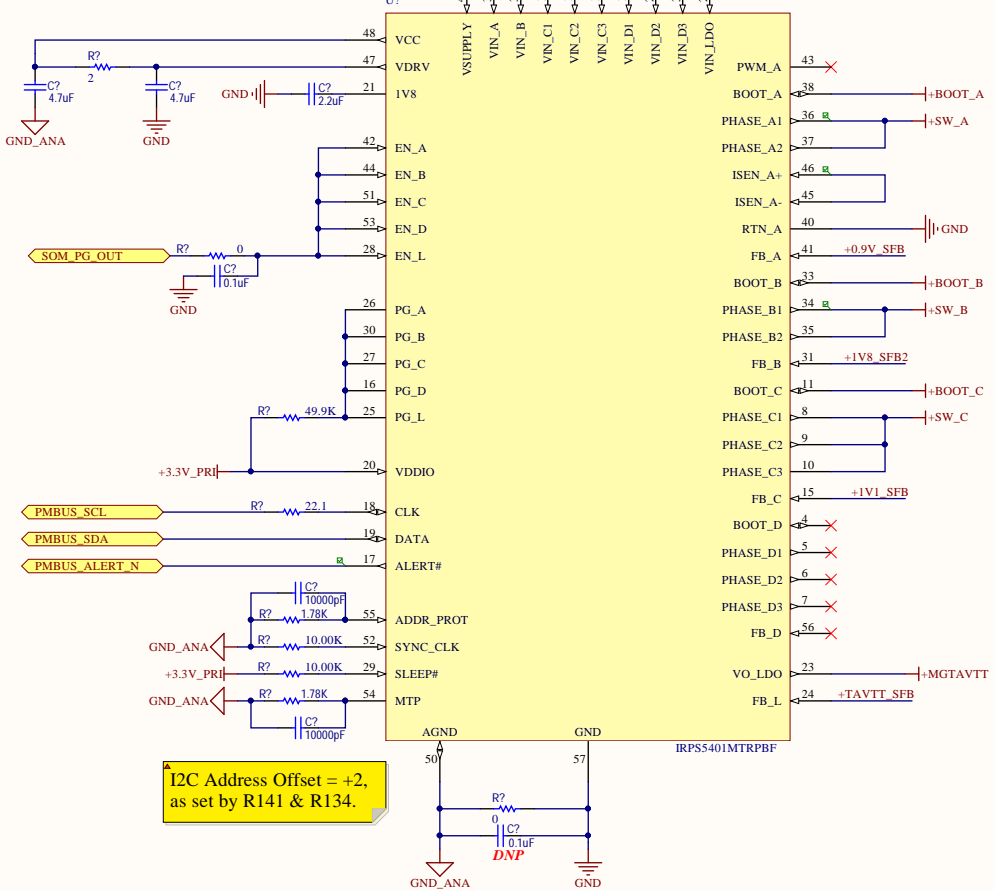
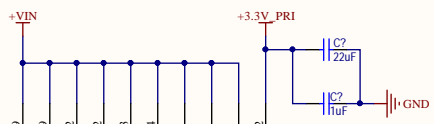
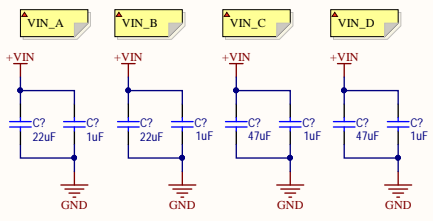


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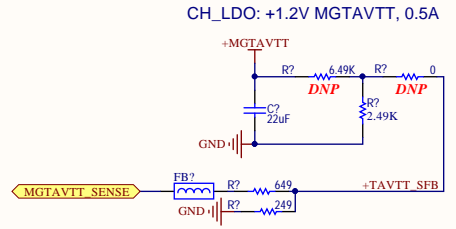
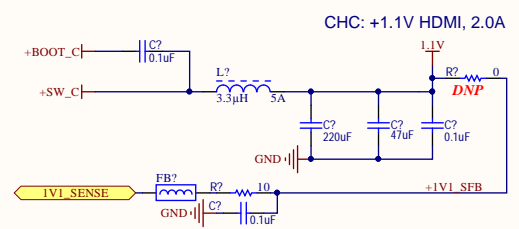
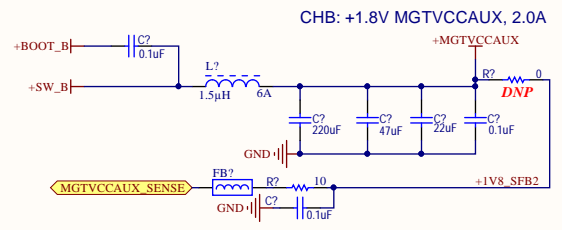
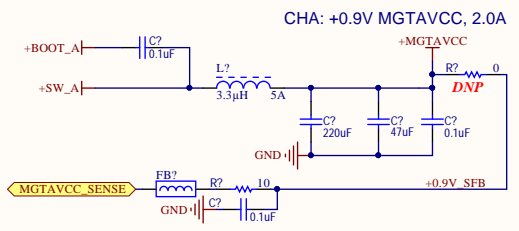


AVNET Avnet Engineering Services		PCB Rev: A	BOM: 01	Variant: 00
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POWER 3



I2C Address Offset = +2, as set by R141 & R134.



Avnet Engineering Services			
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