

TRAVEO™ T2G: TVII-B-H-8M registers

Body controller high technical reference manual

About this document

Scope and purpose

The TRAVEO™ T2G body controller high registers TRM provides reference information for registers and contains respective programming details.

Intended audience

This manual is for system designer and programmers who are designing or programming using TVII-B-H-8M parts.

Instructions

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This document contains instructions for the location of

- TVIIBH8M_RegistersTRM.html
- TVIIBH8M_RegistersTRM_RegistersTRM_Diff_Rev0F_Rev0G.pdf and
- TVIIBH8M_RegistersTRM.pdf files.

The above mentioned files are located in the 002-24402 source folder. To use TVIIBH8M_RegistersTRM.html file, unzip the 002-24402.zip file and open the .html file in the web browser.

Revision history

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Document version	Date of release	Description of changes
**	2018-07-25	Specification for new silicon.
*A	2018-09-26	Minor issues fixed in generation of HTML.
*B	2019-05-16	<p>Instance table is created for each IP and the Instance number is picked from the parameter section of the XML.</p> <p>Links are created for the REGISTER table with offset from the base.</p> <p>All registers are displayed (same instance of the same IP).</p> <p>All bit-fields per register are rendered based on parameterization</p> <p>Logs captures the following three details:</p> <ol style="list-style-type: none"> a. IP and its respective instances b. IP wise Hidden registers c. IP wise Populated registers <p>Optimization of HTML:</p> <ol style="list-style-type: none"> a. Stable "File Header" which was earlier moving along with the contents b. Color codes for the navigation pane c. Links to the REGISTER definition from the register table
*C	2019-09-17	<ol style="list-style-type: none"> 1. All the instances of all the IPs are supported now. 2. Look and feel of the UI has been changed to make it more user friendly: <ol style="list-style-type: none"> a. Distributed into 3 panes: <ol style="list-style-type: none"> i. Sections ii. Registers iii. Content b. Default page with right fragment opens up with the references, notes, and registers structure with respective links on the Contents pane. c. Registers pane provide global search operation after feeding first 3 characters when ONLY at home. d. On the Sections pane, user must select the MMIO > IP > Instance etc. e. Registers pane provides Search options for the selected instance. f. Once a particular instance is chosen, the Registers pane provides a list of registers. User can select one of these registers and observe the register structure on the Contents pane. 3. Instance table, register name and offsets have been removed, as they are no longer needed. 4. All hidden registers and sections are not visible (similar to earlier versions). 5. There is no PDF associated with HTML as earlier. <p>Known Issues:</p> <ol style="list-style-type: none"> 1. There is no way to print any content as of now. 2. There could be a slight misalignment on the content pane based on the screen size.

Revision history

Document version	Date of release	Description of changes
*D	2020-03-05	<ol style="list-style-type: none"> 1. Extensive search options using address or register or bit fields. Search option is supported for "Registers", register "Addresses" and register "Bit Fields" using prefixes "reg:" or "add:" or "bit:" respectively. 2. Buttons "Home", and "Up" included. 3. Register Structure with links, all homepage notes are moved down. 4. Some fixes which were present in the previous revision in spec system. Missing contents due to parser errors (CDT#343334). 5. Print options for the selected registers. 6. HTML is supported only on Chrome, Mozilla Firefox, Opera, UC Browser and Microsoft Edge browsers. (No support for any other OS/Mobiles etc.) 7. Issue of content overlap while re-sizing after "restore down" option is fixed. <p>Known Issues:</p> <ol style="list-style-type: none"> 1. Whole HTML print option not available.
*E	2020-08-07	<ol style="list-style-type: none"> 1. Based on Rev_D of Silicon tape out. 2. SW Access note enhanced. 3. Added HW access note for registers behavior.
*F	2020-09-23	<ol style="list-style-type: none"> 1. ES100 Release 2. Sflash additions 3. eFuse corrections etc.
*G	2021-10-01	<ol style="list-style-type: none"> 1. Refer to Registers TRM difference log with respect to previous revision for details 2. Addition of PASS/ADC registers for Temperature sensor control and trimming 3. Sflash TOC2 updates for flash boot 4. Minor description updates to some registers 5. Addition of marker for security enhancement 6. Removal of not needed registers

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