

5-V Voltage Regulator

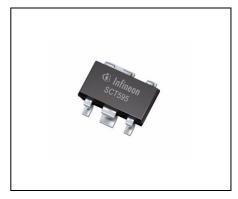
TLE 4286 G





Features

- 15 mA output current capability
- 1 μA current consumption in standby mode
- Low quiescent current consumption 60 μA in ON mode
- Inhibit input
- Very small SMD-Package PG-SCT-595-5
- Wide operation range: 6.2 V to 42 V
- Wide temperature range: -40 °C to 150 °C
- Output protected against short circuit
- Overtemperature protection
- Green product (RoHS compliant)
- AEC qualified.



PG-SCT-595-5

Functional Description

The **TLE 4286 G** is a 5-V low-drop fixed voltage regulator in the very small SMD package PG-SCT-595-5. The maximum input voltage is 42 V. The output is able to drive a load of more than 10 mA while it regulates the output voltage within a 4% accuracy.

The device can be switched in stand-by mode via an inhibit input which causes the current consumption to drop below 1 μ A.

A temperature protection disables the IC at over temperature.

Туре	Package	Marking
TLE 4286 G	PG-SCT-595-5	A1

Data Sheet 1 Rev. 2.3, 2008-04-21



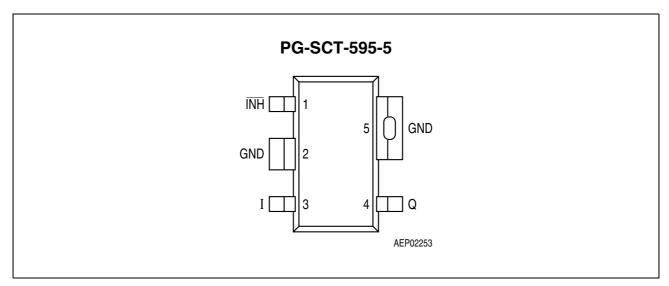


Figure 1 Pin Configuration (top view)

Table 1 Pin Definitions and Functions

Pin No.	Symbol	Function
1	INH	Inhibit input; H for active ($V_Q = 5 \text{ V}$) and L for stand-by
2	GND	Ground; internally connected to pin 5
3	I	Input voltage
4	Q	Output voltage; must be blocked by a capacitor $C_{\rm Q} \ge 1~\mu \rm F,~ESR \le 10~\Omega$ to GND
5	GND	Ground; internally connected to pin 2

Data Sheet 2 Rev. 2.3, 2008-04-21



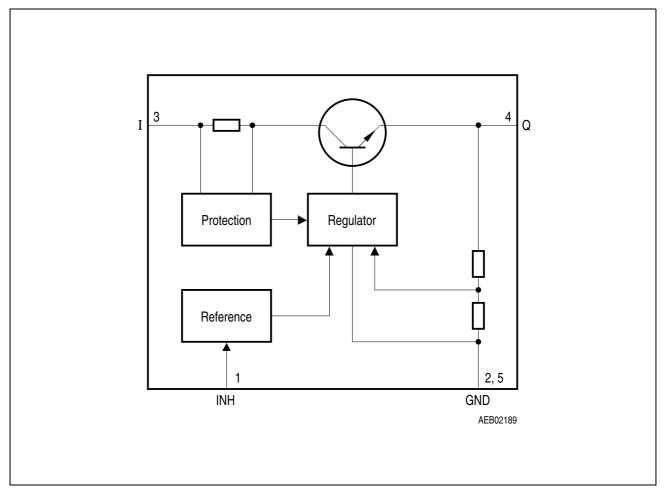


Figure 2 Block Diagram



 Table 2
 Absolute Maximum Ratings

 $-40 \, ^{\circ}\text{C} < T_{i} < 150 \, ^{\circ}\text{C}$

Parameter	Symbol	Limit Values		Unit	Remarks
		Min.	Max.		
Input				•	
Voltage	V_{I}	-0.3	45	V	_
Current	I_{I}	-20	*	mA	* internally limited
Output					
Voltage	V_{Q}	-0.3	16	V	_
Current	I_{Q}	-20	*	mA	* internally limited
Inhibit		•	•	•	
Voltage	V_{INH}	-40	45	V	_
Current	I_{INH}	-500	*	μΑ	* internally limited
Current	I_{INH}	-5	5	mA	-0.3 V < $V_{\rm I}$ < 45 V; t < 1 ms
Temperatures					
Junction temperature	T_{j}	-40	150	°C	_
Storage temperature	$T_{ m stg}$	-50	150	°C	_
Thermal Resistances		•		•	•
Junction pin	$R_{ m thj\text{-pin}}$	-	30	K/W	measured to pin 5
Junction ambient ¹⁾	R_{thja}	_	179	K/W	zero airflow zero heat sink area

¹⁾ Worst case regarding peak temperature.

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.

Table 3 Operating Range

Parameter	Symbol	Limit Values		Unit	Remarks
		Min.	Max.		
Input voltage	V_{I}	6.0	42	V	_
Inhibit input voltage	V_{INH}	-0.3	40	V	-
Junction temperature	$T_{\rm j}$	-40	150	°C	-

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 Table 4
 Electrical Characteristics

6.2 V < $V_{\rm I}$ < 36 V; $V_{\rm INH}$ > $V_{\rm INH,~ON}$; -40 °C < $T_{\rm j}$ < 150 °C; unless otherwise specified

V_{Q}	Min. 4.85	Typ. 5.0	Max.		
V_{Q}	4.85	5.0	•		1
V_{Q}	4.85	5.0			
	1		5.15	V	$T_{\rm j}$ = 25 °C; 1 mA < $I_{\rm Q}$ < 10 mA
	4.8	5.0	5.20	V	$1 \text{ mA} < I_{Q} < 10 \text{ mA}$
V_{dr}	0.6	8.0	1.1	V	$I_{\rm Q}$ = 10 mA
C_{Q}	1	_	_	μF	ESR ≤ 10 Ω at 10 kHz
I_{Q}	15	_	70	mA	_
I_{q}	_	60	100	μΑ	$I_{\rm Q}$ < 10 mA; $V_{\rm I}$ = 13.5 V
I_{q}	_	_	1	μΑ	$V_{\text{INH}} < V_{\text{INH, OFF}};$ $T_{\text{j}} < 85 ^{\circ}\text{C}$
I_{q}	_	_	5	μΑ	$V_{INH} < V_{INH, OFF}$
	•	•	•		
$\Delta V_{ m Q}$	_	5	10	mV	0 mA < $I_{\rm Q}$ <10 mA; $V_{\rm I}$ = 6.2 V; $T_{\rm j}$ ≤ 85 °C
ΔV_{Q}	_	5	10	mV	$I_{\rm Q}$ = 5 mA; $T_{\rm j}$ ≤ 85 °C
PSRR	_	60	_	dB	$f_{\rm r}$ = 100 Hz; $V_{\rm r}$ = 0.5 Vpp
	•	•	•		
$V_{INH,ON}$	_	_	3.5	V	$V_{\rm Q} \ge 4.8 \ { m V}$
	0.3	_	_	V	$V_{\rm Q} \le 0.8 \ { m V}$
				1	1
$I_{INH,ON}$		10	15	μΑ	$V_{INH} = 5 \; V$
	$I_{ m q}$ $\Delta V_{ m Q}$ $\Delta V_{ m Q}$	$I_{ m q}$ - $\Delta V_{ m Q}$ - $\Delta V_{ m Q}$ - $\Delta V_{ m NH,ON}$ -	$I_{ m q}$ $ \Delta V_{ m Q}$ $ 5$ $\Delta V_{ m Q}$ $ 5$ $ 60$ $ -$	$I_{ m q}$ 5 $I_{ m q}$ - 60 - $I_{ m q}$ - 60 - $I_{ m q}$ - 7 $I_{ m q}$ -	$I_{ m q}$ 5 $\mu { m A}$ $\Delta V_{ m Q}$ - 5 10 $m { m V}$ $\Delta V_{ m Q}$ - 5 10 $m { m V}$ $PSRR$ - 60 - $d { m B}$ $V_{ m INH, ONF}$ $V_{ m INH, OFF}$ 0.3 $V_{ m INH, OFF}$

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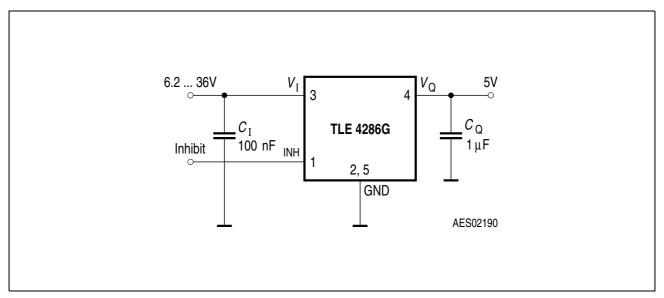


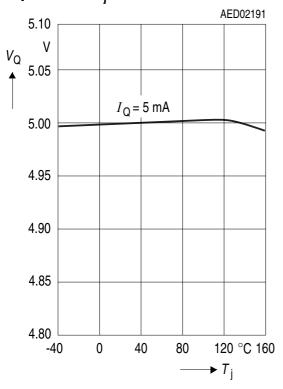
Figure 3 Application Circuit

Data Sheet 6 Rev. 2.3, 2008-04-21

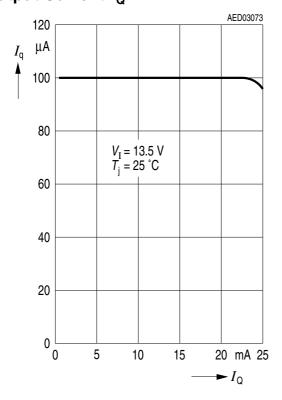


Typical Performance Characteristics

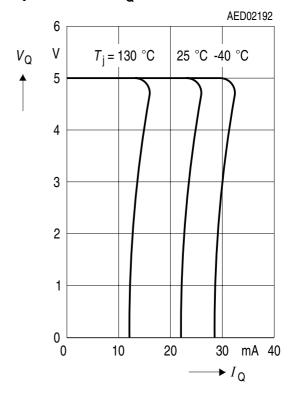
Output Voltage $V_{\rm Q}$ versus Temperature $T_{\rm i}$



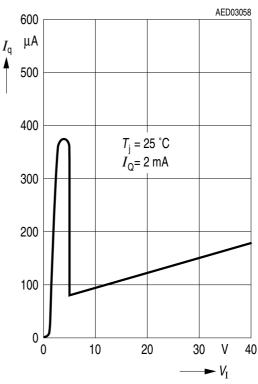
Current Consumption $I_{\rm q}$ versus Output Current $I_{\rm Q}$



Output Voltage $V_{\rm Q}$ versus Output Current $I_{\rm Q}$

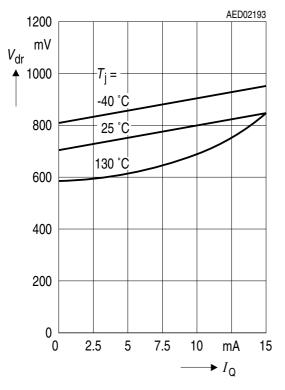


Current Consumption $I_{\rm q}$ versus Input Voltage $V_{\rm I}$

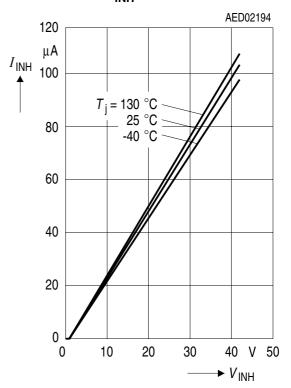




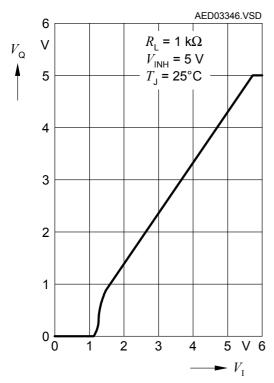
Drop Voltage V_{dr} versus Output Current I_{Q}



Inhibit Voltage V_{INH} versus Inhibit Current I_{INH}



Output Voltage $V_{\rm Q}$ versus Input Voltage $V_{\rm I}$





Package Outlines

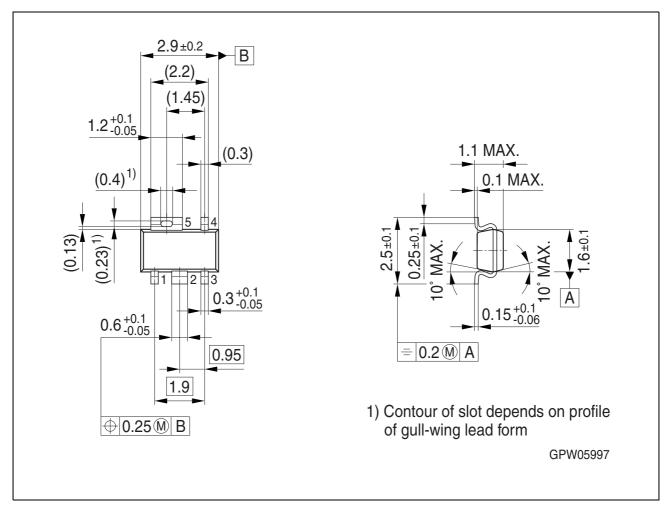


Figure 4 Outline PG-SCT-595-5

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

You can find all of our packages, sorts of packing and others in our Infineon Internet Page "Products": http://www.infineon.com/packages.

SMD = Surface Mounted Device

Dimensions in mm



Revision History

Version	Date	Changes
Rev. 2.3	2008-04-21	Initial version of RoHS-compliant derivate of TLE 4286 G. Page 1: AEC certified statement added. Page 1 and Page 9: RoHS compliance statement and Green product feature added. Page 1 and Page 9: Package changed to RoHS compliant version. Page 1: Marking information added. Page 1: Adapted description to values given on Page 5. Not a change of electrical characteristics. Legal Disclaimer updated
Rev. 2.2	2004-01-01	Final datasheet

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