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CE223693 –Sine Wave Generation Using PSoC 4

Objective

This code example demonstrates how to generate a sine wave using the VDAC Component of PSoC 4100PS.

Overview

This code example demonstrates how to generate a sine wave using the VDAC Component of the PSoC 4100PS device. A 1000-point look-up table (LUT) is computed and written to flash. A DMA Component is used to transfer the LUT values from flash to VDAC input data register to generate a sine wave of 125 Hz frequency.

Requirements

Tools: PSoC® Creator™ 4.2 or later versions

Programming Language: C (Arm® GCC 5.4)

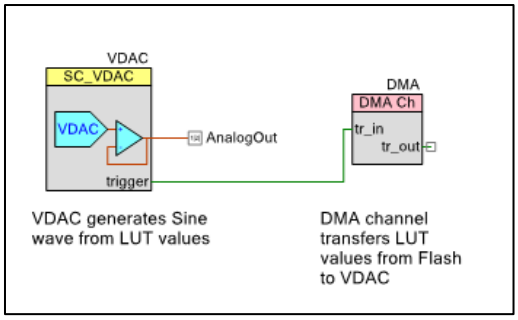
Associated Parts: PSoC 4100PS

Related Hardware: CY8CKIT-147 PSoC 4100PS Prototyping Kit

Design

Figure 1 shows the PSoC Creator schematic for generating a sine wave using PSoC 4100PS.

Figure 1. PSoC Creator Schematic



This code example calculates the sine LUT using trigonometric identities. The number of LUT points used in this code example is 1000. The frequency of the generated sine wave is shown in the following equation.

$$\text{Output Frequency} = \frac{\text{VDAC update Rate}}{\text{Number of LUT points}}$$

In this code example, the trigger output of VDAC triggers the DMA channel. The frequency of signal that appears on trigger outputof VDAC is equal to refresh rate of VDAC. In this case, the refresh rate of VDAC is 125-kHz. Therefore, the frequency of the generated sine wave for a 1000-point LUT is 125 Hz. The LUT is generated using two initial values, sin ΔΦ and cos ΔΦ, where ΔΦ = (2*π) / Number of points. The calculated LUT values are written to flash using the flash write API.

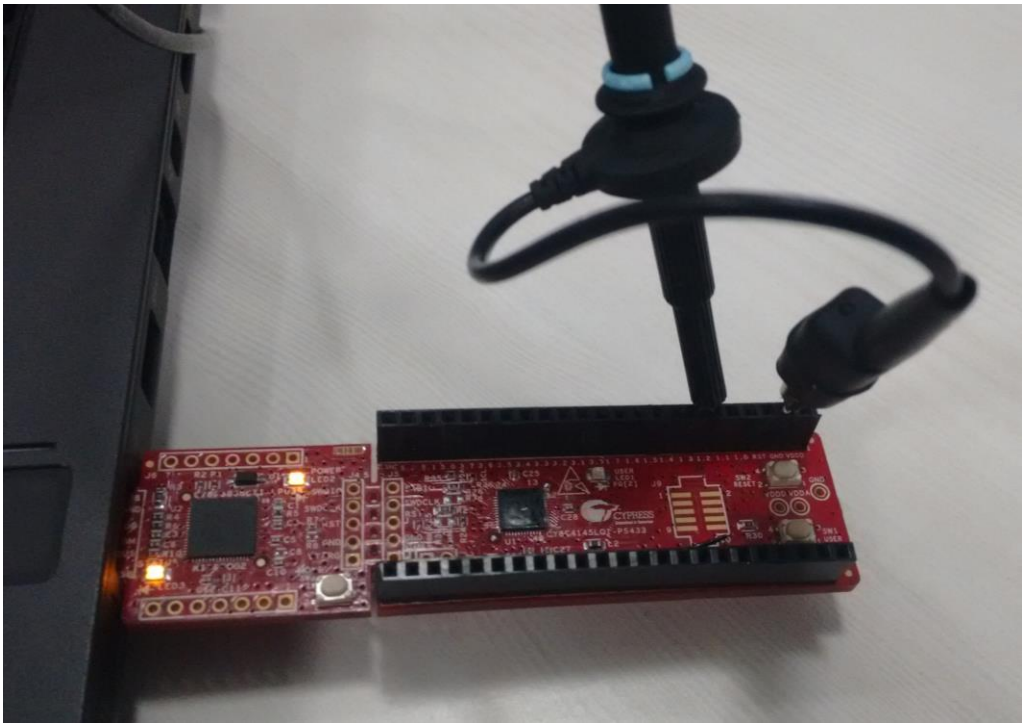
Design Considerations

This code example is designed for the PSoC 4100PS Prototyping Kit. The design is easily portable to other kits and PCBs.

Hardware Setup

Connect the CY8CKIT-147 PSoC 4100PS Prototyping Kit to your computer's USB port as Figure 2 shows.

Figure 2. Hardware Connection



Software Setup

No software setup is required to test this code example.

Components

Table 1 lists the PSoC Creator Components used in this example, and the hardware resources used by each Component.

Table 1 List of PSoC Creator Components

Component	Instance Name	Version	Hardware Resources
SC_VDAC	VDAC	v1.40	VDAC
Analog Pin	AnalogOut	v2.20	I/O
DMA Ch	DMA	v1.0	DMA

Parameter Settings

Table 2 lists the non-default settings of all the Components used in the design.

Table 2. Component Parameters

Component Instance Name	Settings (Non-Default)
DMA	**Channel:** ▪ Channel Priority: 0 (Highest) **Descriptor 0:** ▪ Data Element size: Halfword (2 Bytes) ▪ Number of Data Elements to transfer: 1000 ▪ Source and Destination transfer width: Halfword to Word ▪ Increment source address by two: Yes
VDAC	**Basic:** ▪ Clock Source: **Internal>** Freq = 1500 KHz

Design-Wide Resources

Table 3 lists the physical pin used.

Table 3. Pin Names and Locations

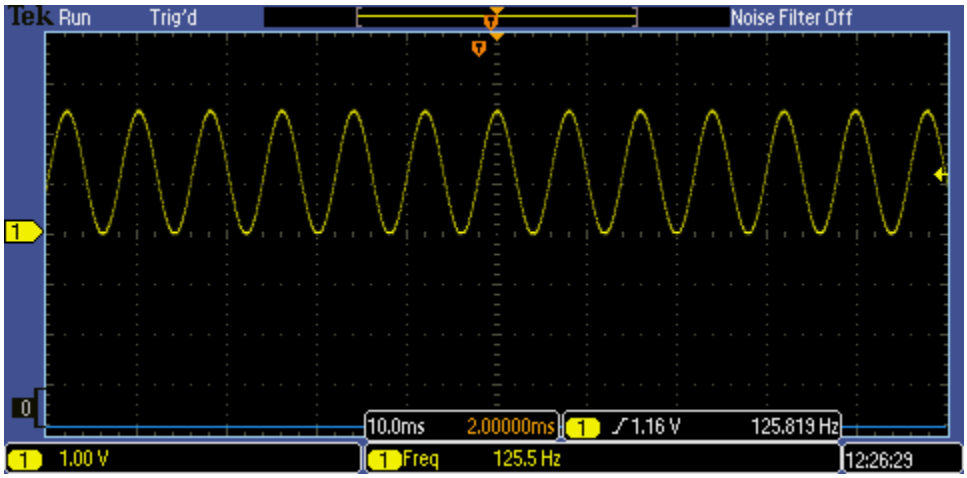
Pin Name	Location
Analog_Out	P1[2]

Operation

Follow these steps:

1. Open the project attached with this code example in PSoC Creator.
2. Build the project; select **Build** > **Build CE223693_Sine_Wave_Generation**.
3. Connect the PSoC 4100PS Prototyping Kit to your computer's USB port as described in the section Hardware Setup.
4. Program the PSoC 4100PS device; select **Debug** > **Program**.
5. Connect the oscilloscope probe between P1_2 and Vss to observe the generated sine wave as Figure 3 shows.

Figure 3. Generated Sine Wave



Related Documents

Table 4 lists all relevant application notes, device datasheets, technical reference manuals, component datasheets, and development kits.

Table 4. Related Documents

Application Notes		
AN79953	Getting Started with PSoC 4	Describes the PSoC 4100PS
AN223616	AFE Implementation Using PSoC 4	Discusses the AFE implementation of different types of sensors
PSoC Creator Component Datasheets		
SC_VDAC	13-bit Voltage output Digital to Analog Convertor	
DMA_Ch	Transfers data to and from memory, components, and registers	
Pins	Supports the connection of hardware resources to physical pins	
Device Documentation		
PSoC 4100PS Datasheet		
PSoC 4100PS Architecture Technical Reference Manual		
PSoC 4100PS Register Technical Reference Manual		
Development Kit (DVK) Documentation		
CY8CKIT-147 PSoC 4100PS Prototyping Kit		

Document History

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Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	6155957	DIMA	4/26/2018	New code example.

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