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Title	EVAL-XMC4800PSOC6M5		
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Revision History

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Rev.	Rel.	Date	Author	Description	Page(s)
1.0		2023-09-04		- Initial design	

EVAL-XMC4800PSOC6M5

Default_Assembly

V 1.0

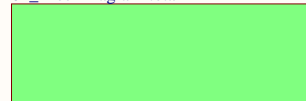


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2023

Link to Top Level Document

02_BlockDiagram-total



03_BlockDiagram-Powersupply



04_Top level diagram



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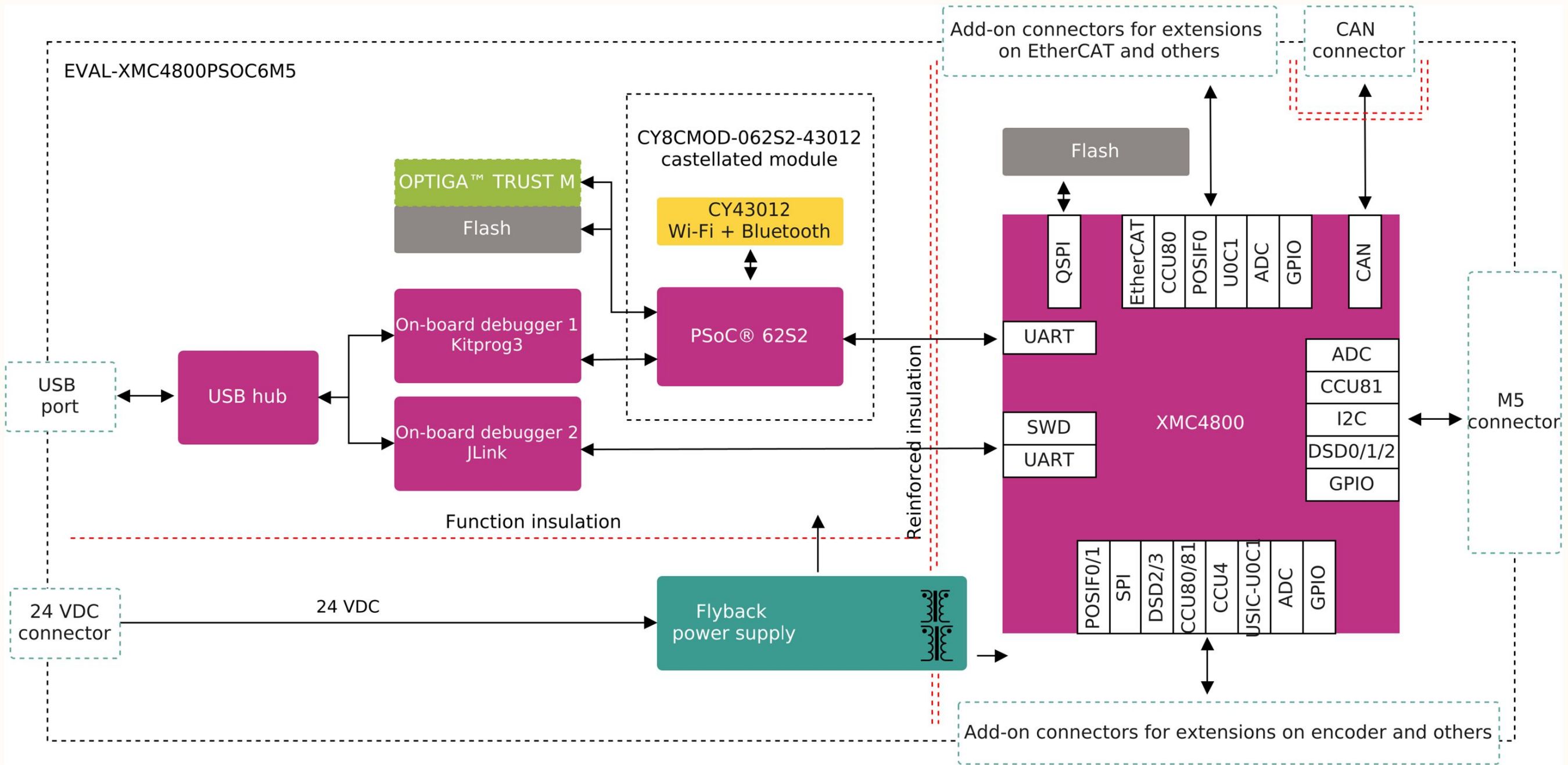


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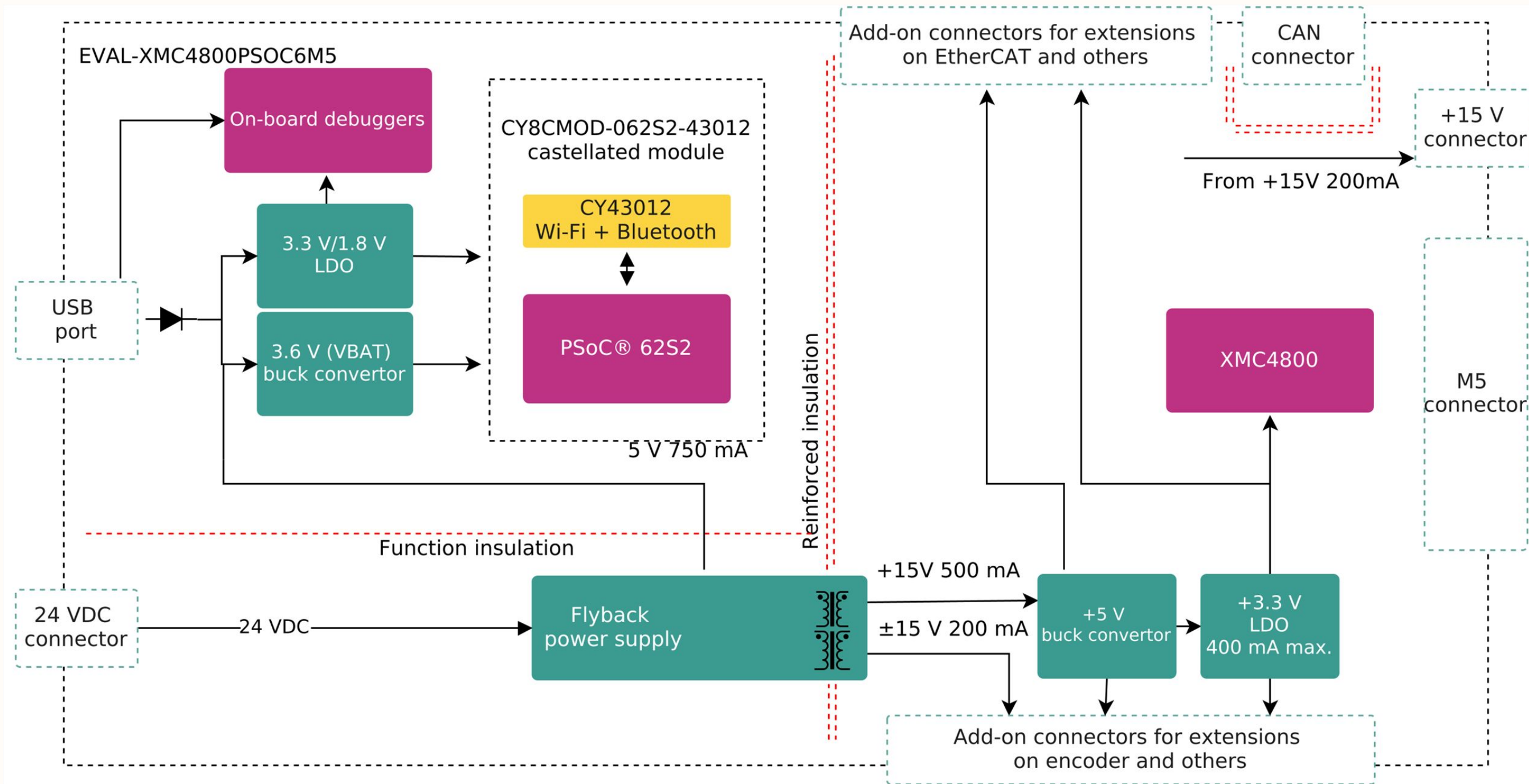
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02_BlockDiagram-total.SchDoc

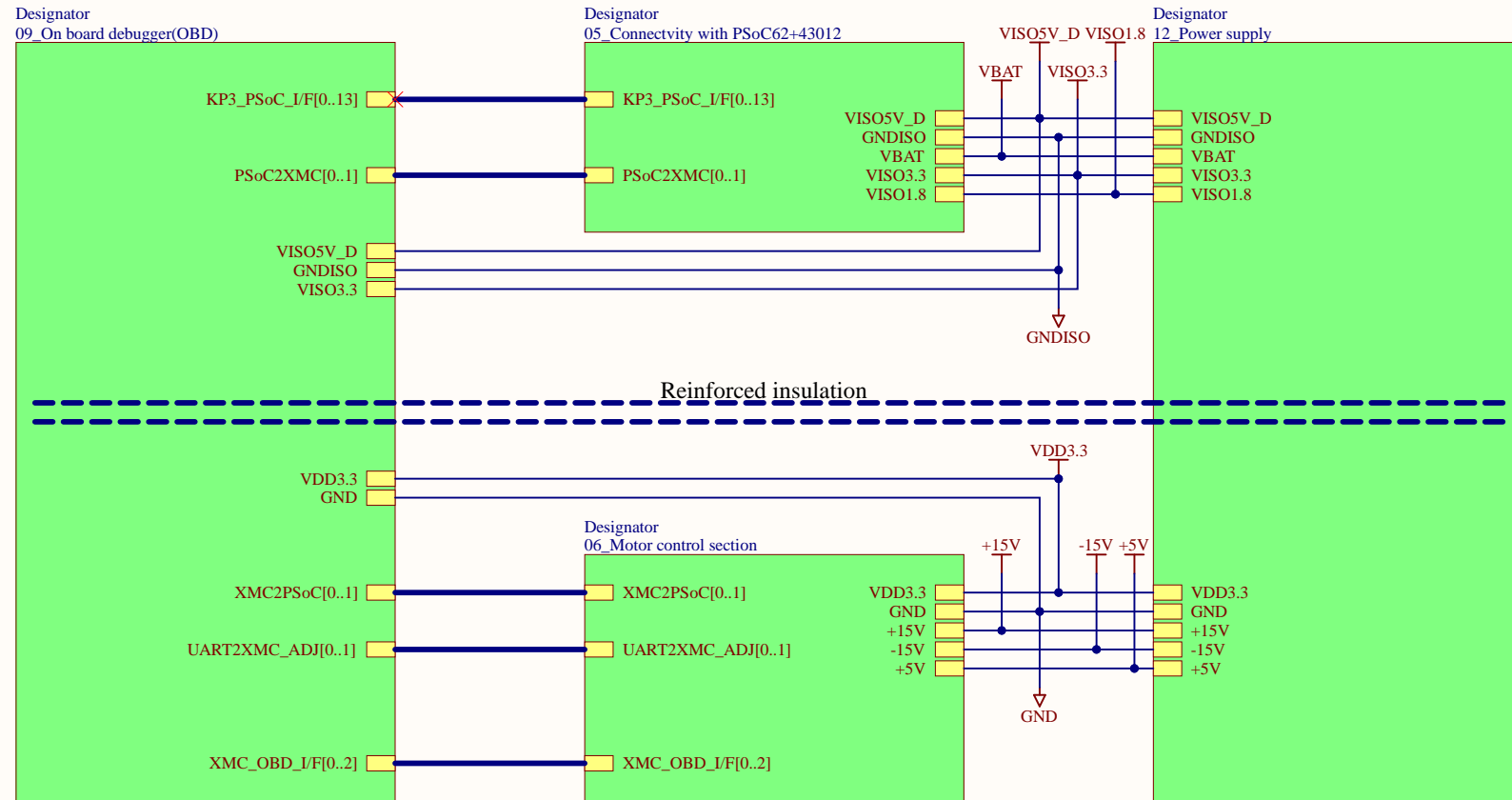



03_BlockDiagram-Powersupply.SchDoc



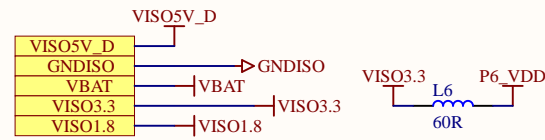
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04_Top level diagram.SchDoc

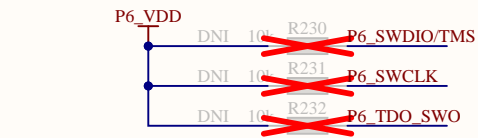
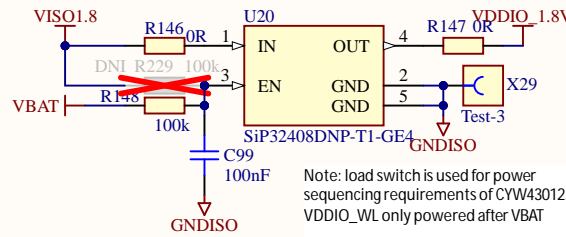


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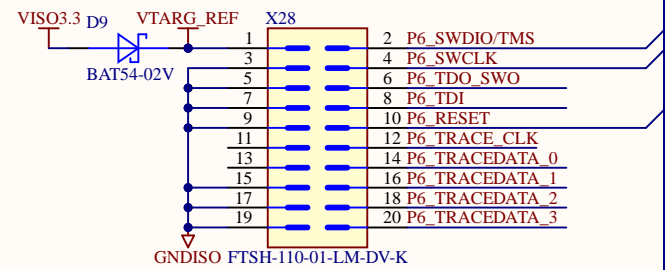
05_Connectivity with PSoC62+43012.SchDoc



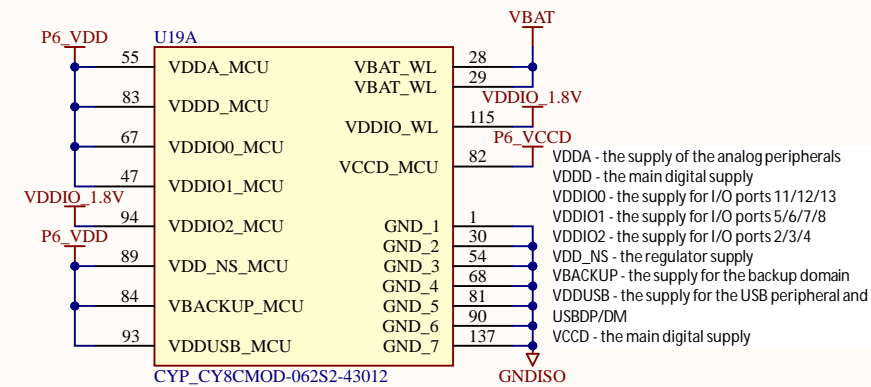
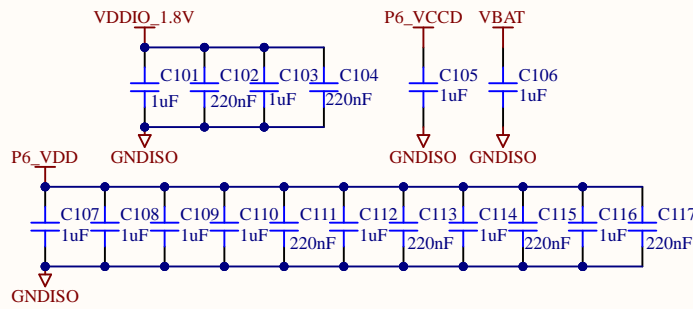
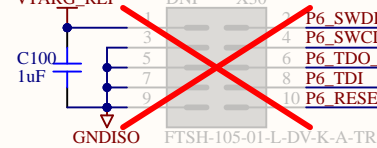
1.8V load switch



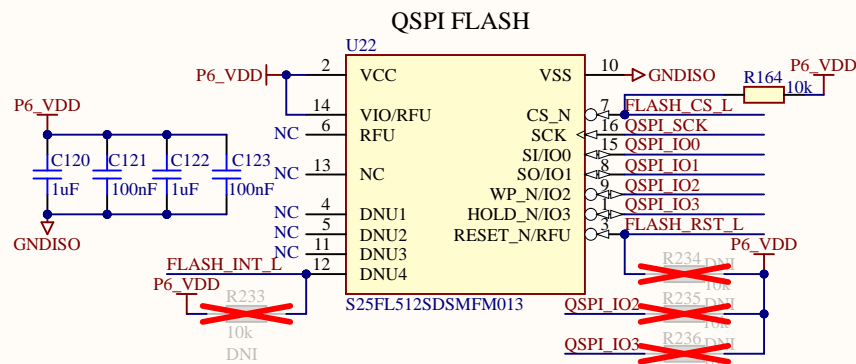
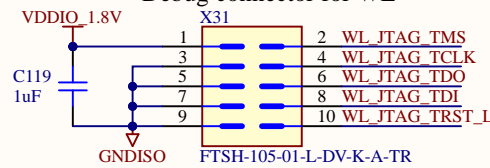
ETM connector for PSoC62



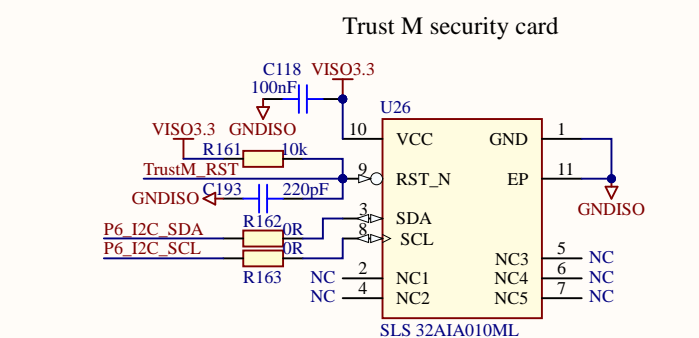
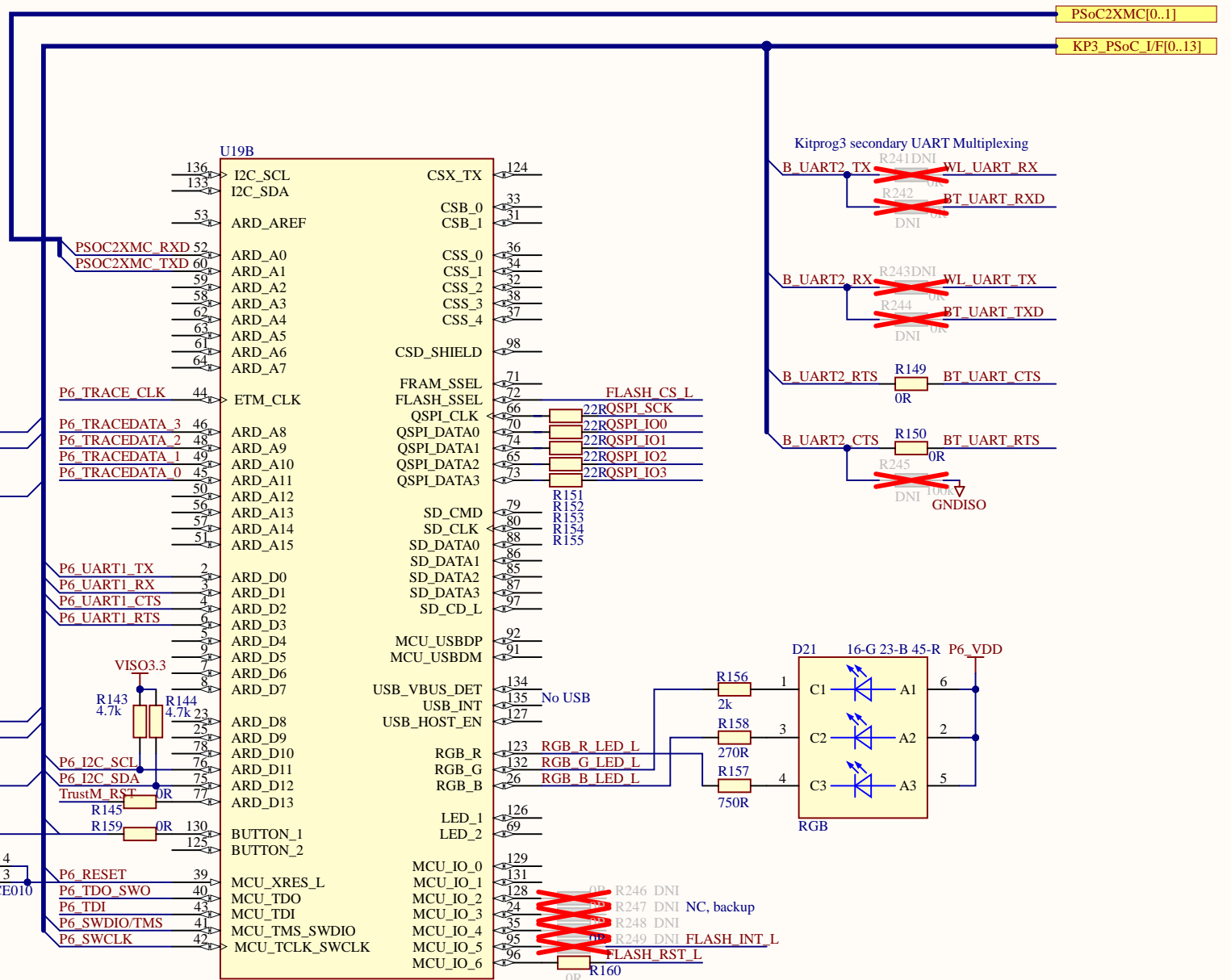
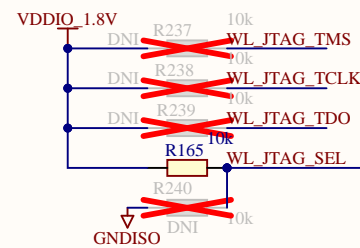
PSoC62 10pin SWD/JTAG header



Debug connector for WL



QSPI FLASH



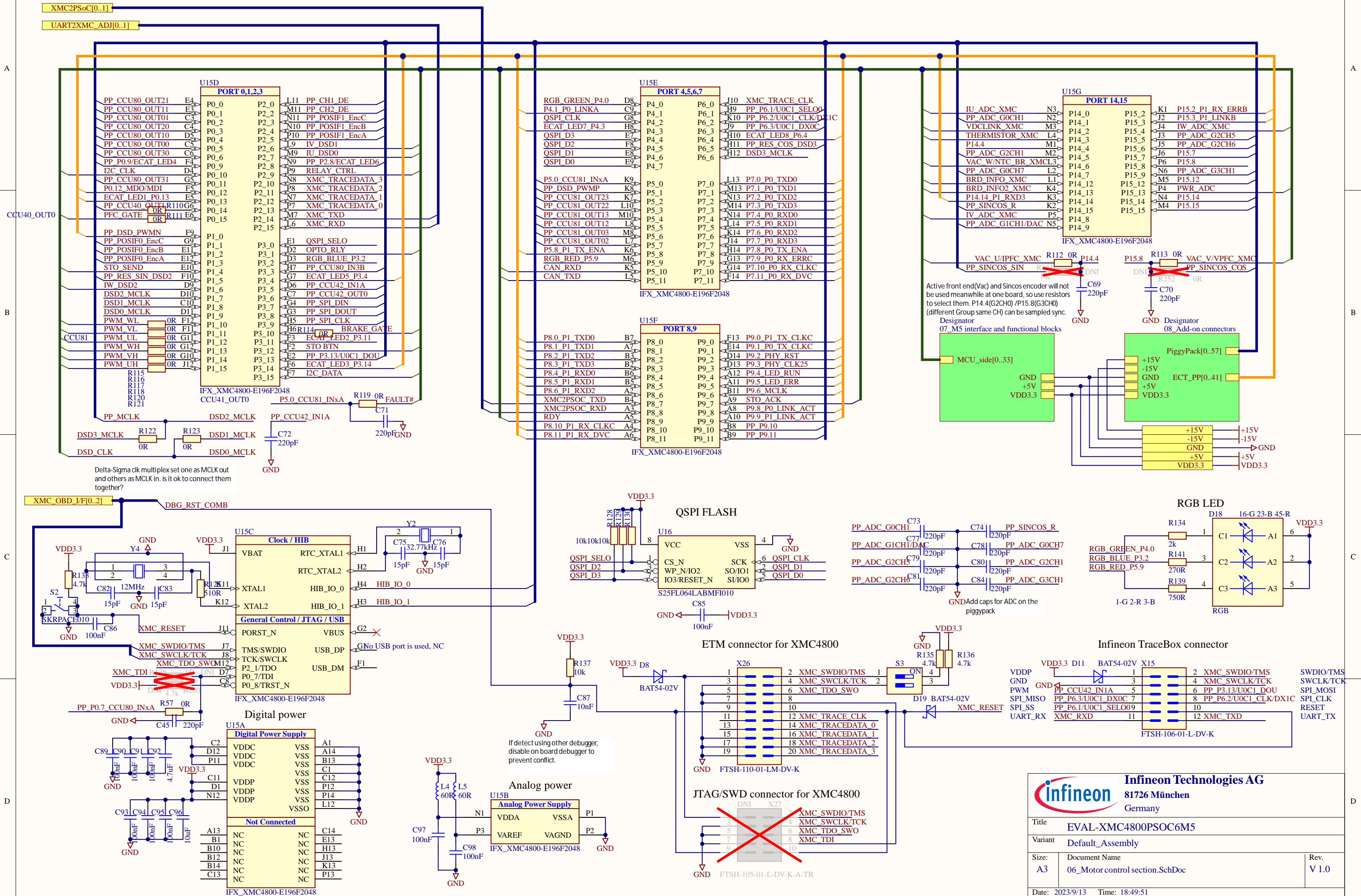
Trust M security card

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06_Motor control section.SchDoc



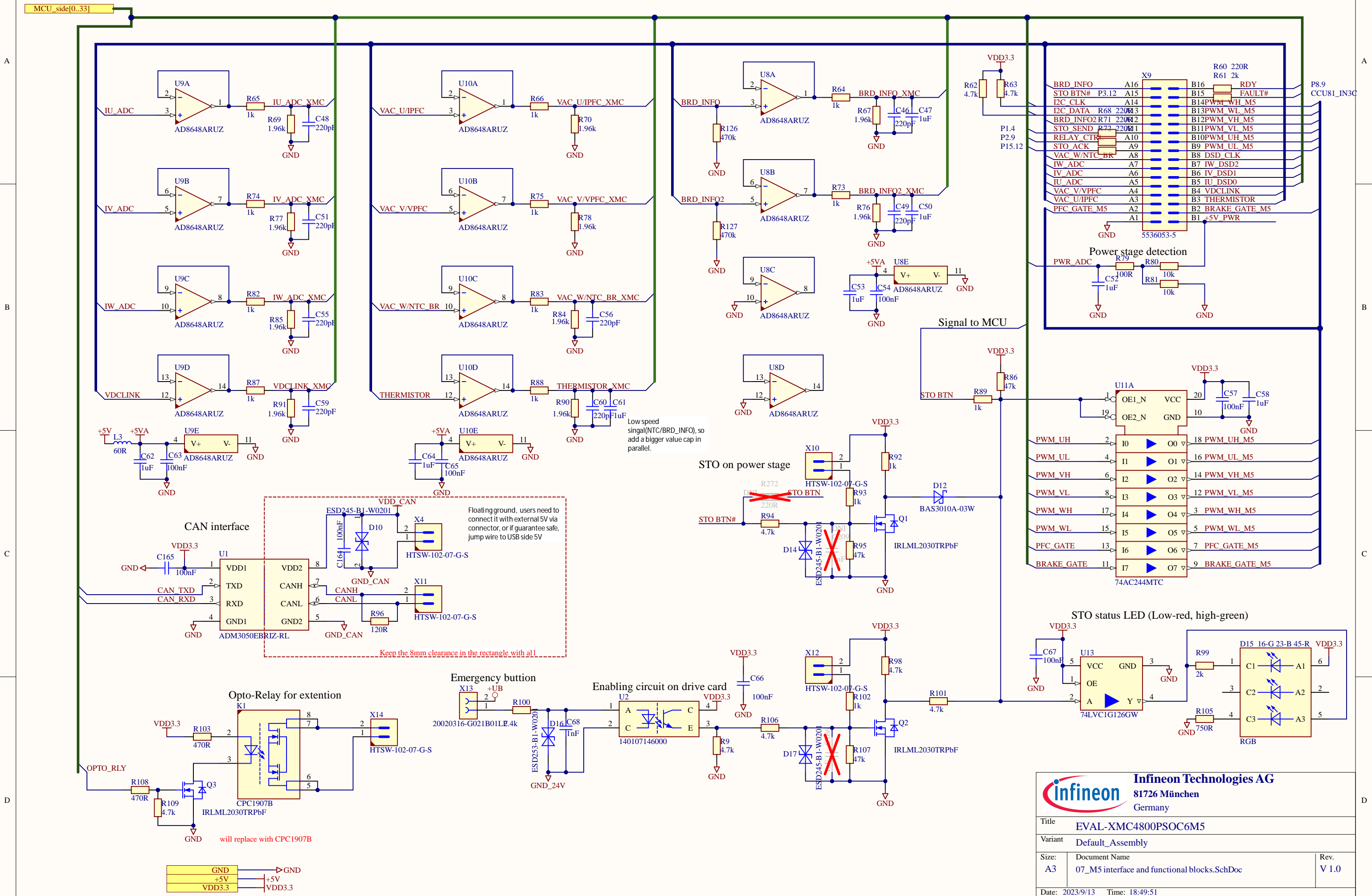
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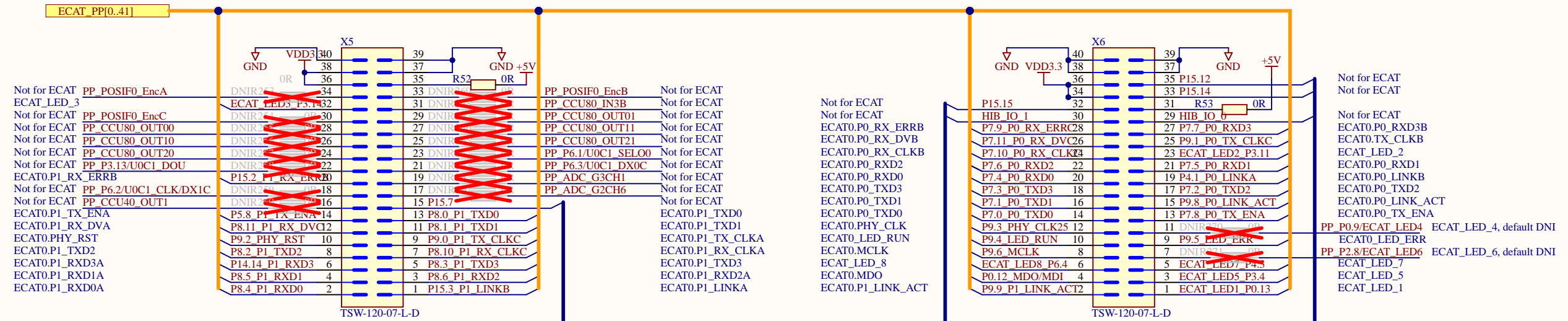
07_M5 interface and functional blocks.SchDoc



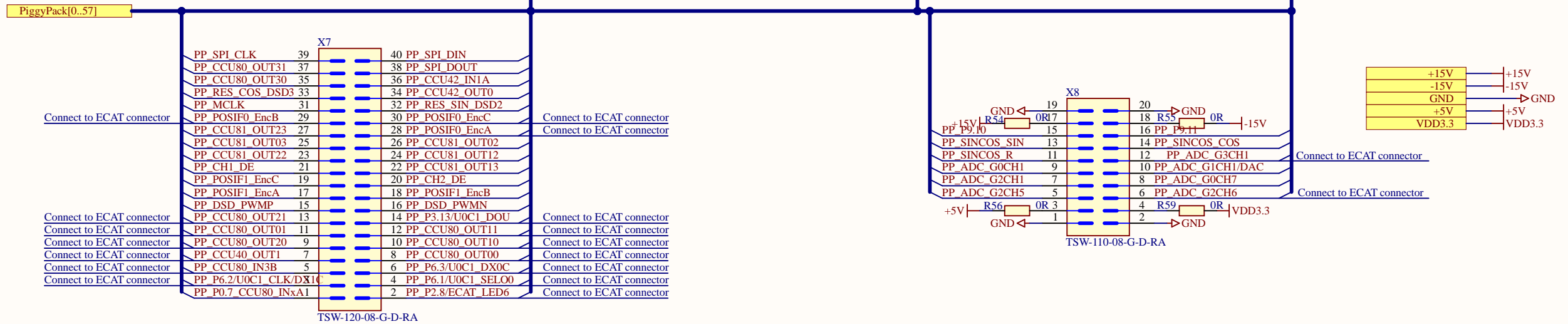
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08_Add-on connectors.SchDoc

Add-on connectors for extensions on EtherCAT and others



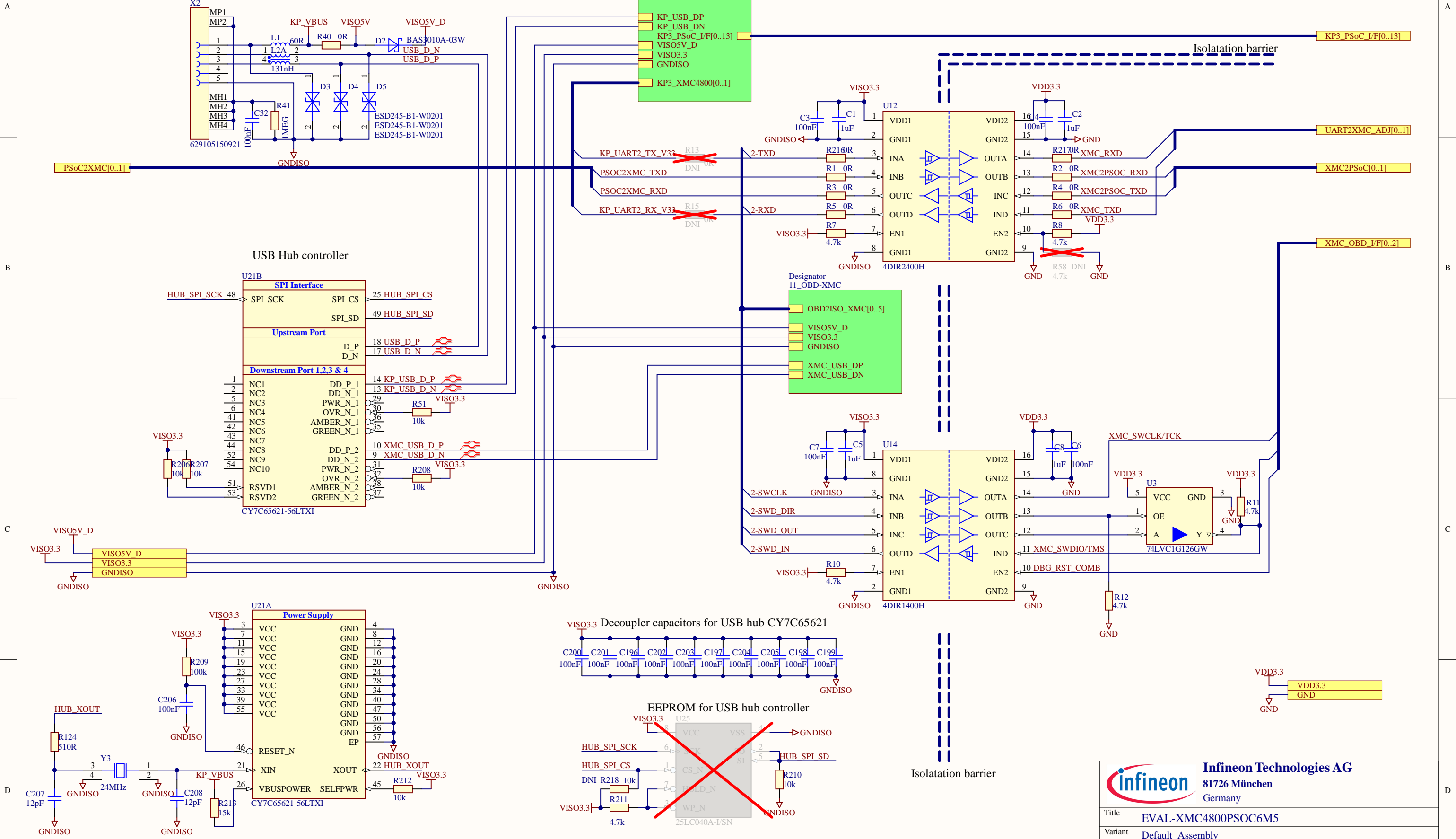
Add-on connectors for extensions on encoder and others



TSS-120-04-T-D-RA/TSS-110-04-T-D-RA or DC3-2.54-64PL is an alternative choice

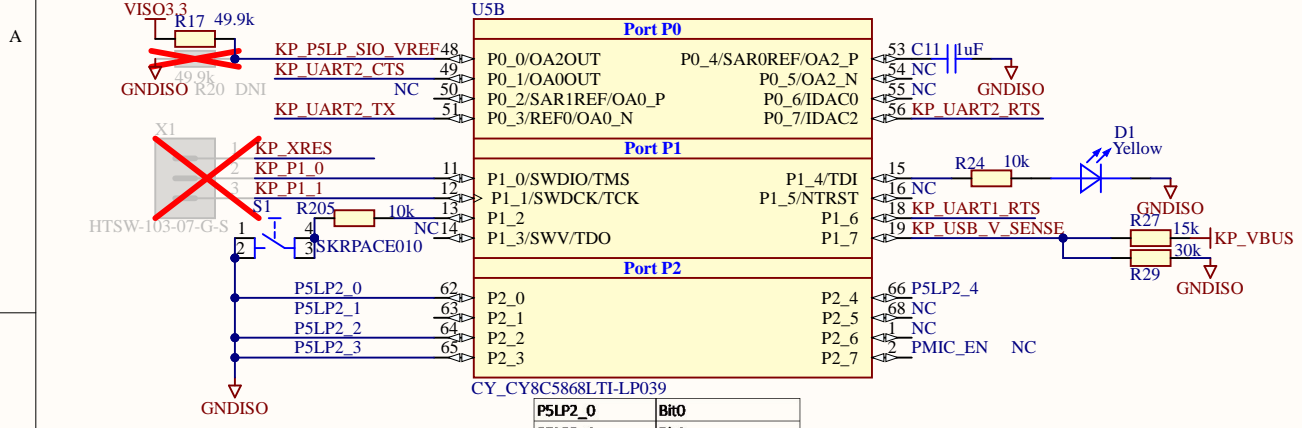
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09_On board debugger(OBD).SchDoc



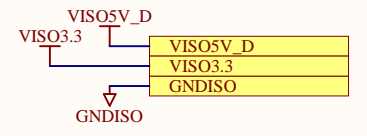
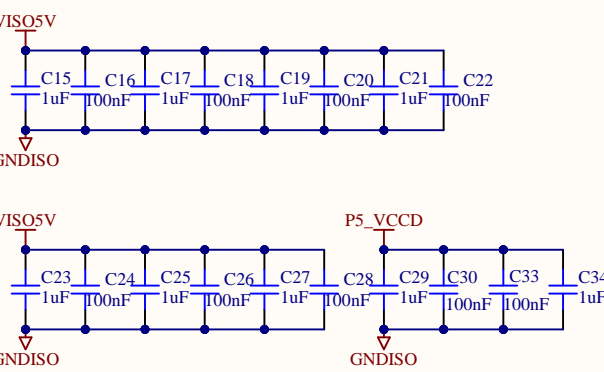
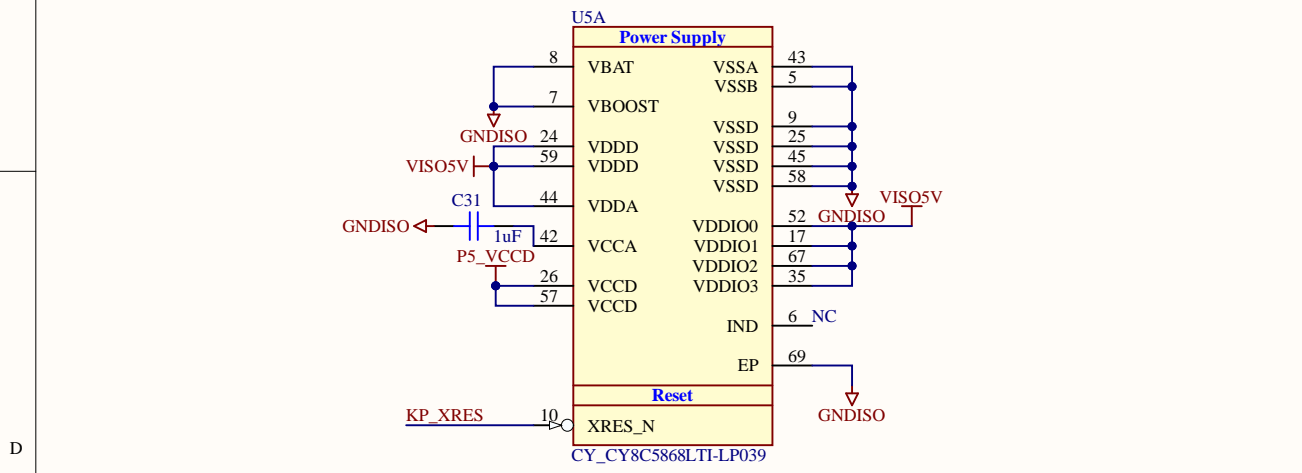
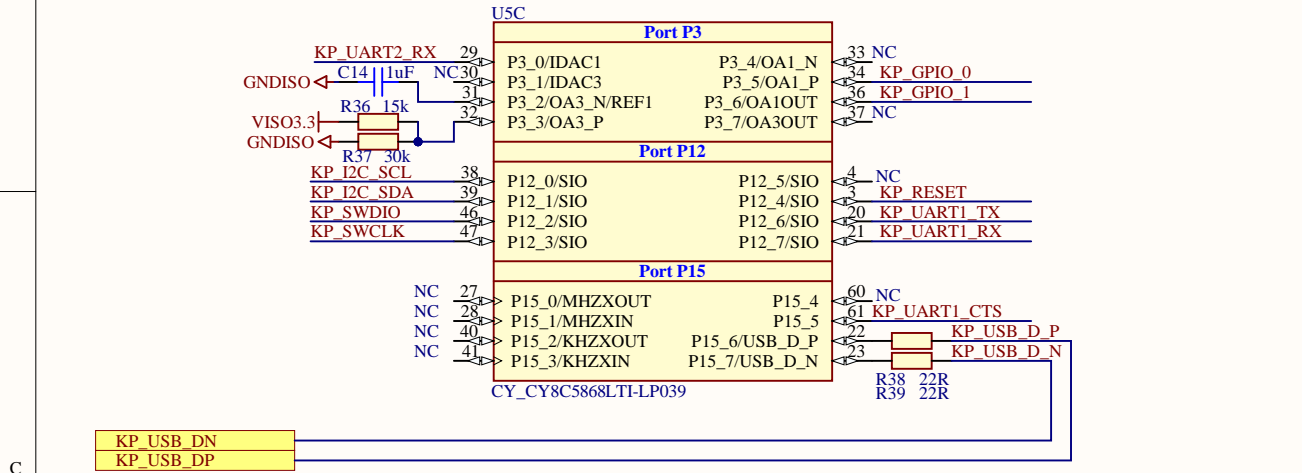
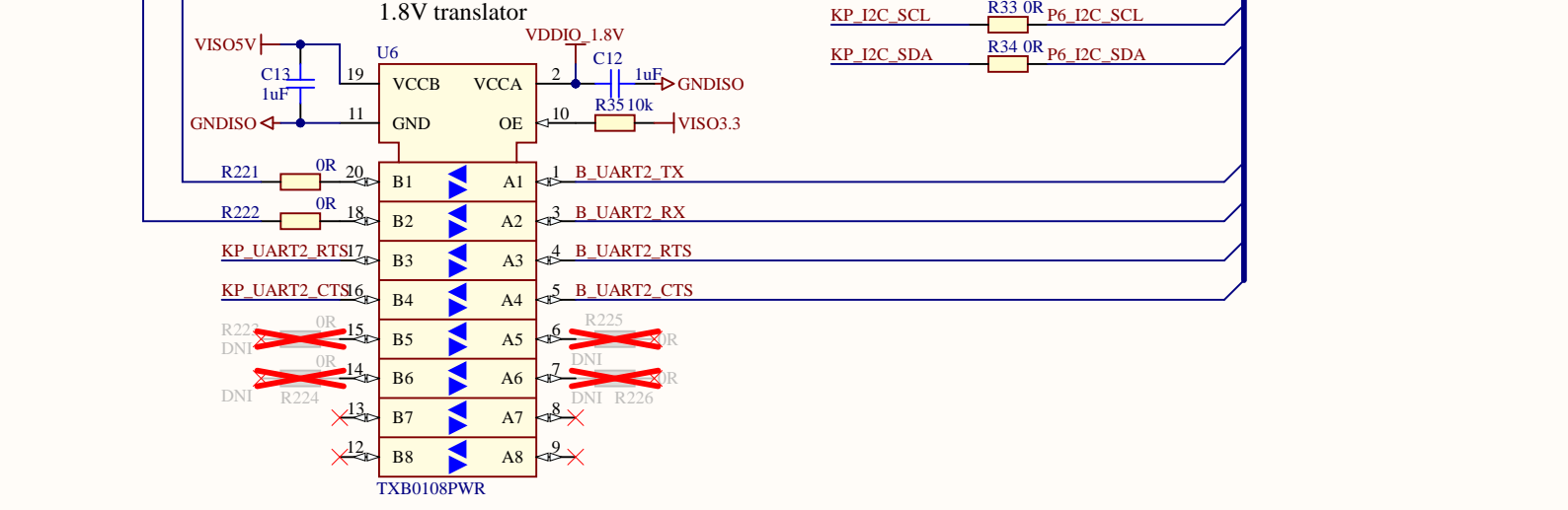
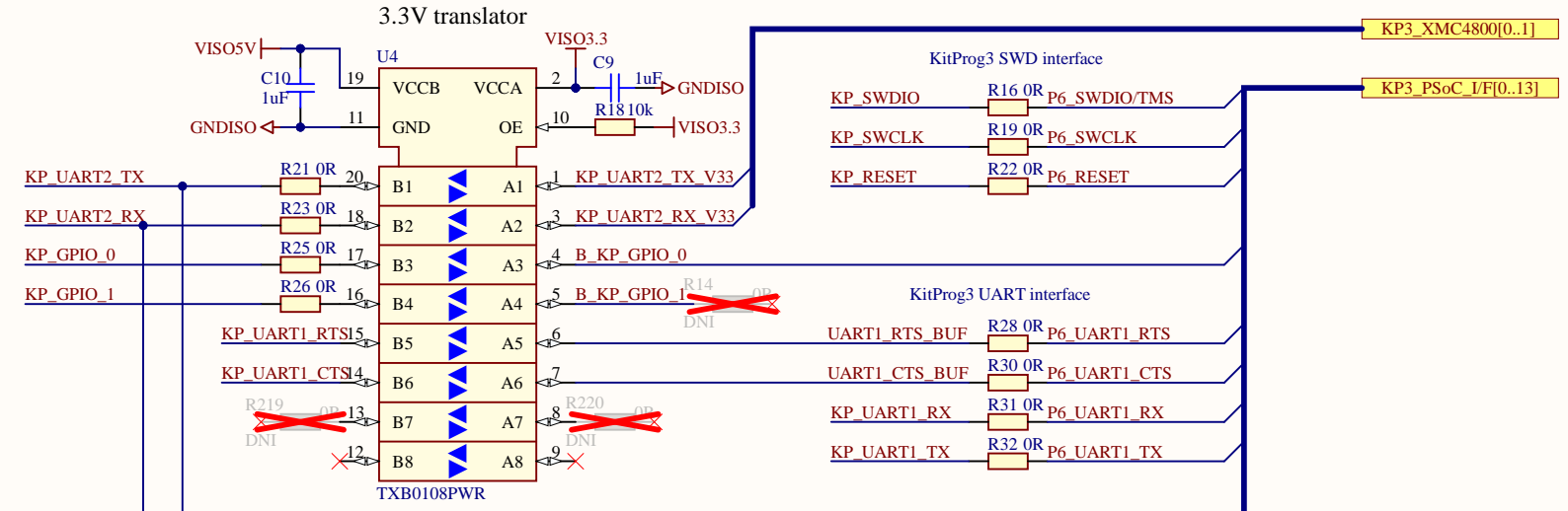
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10_OBD-PSOC.SchDoc



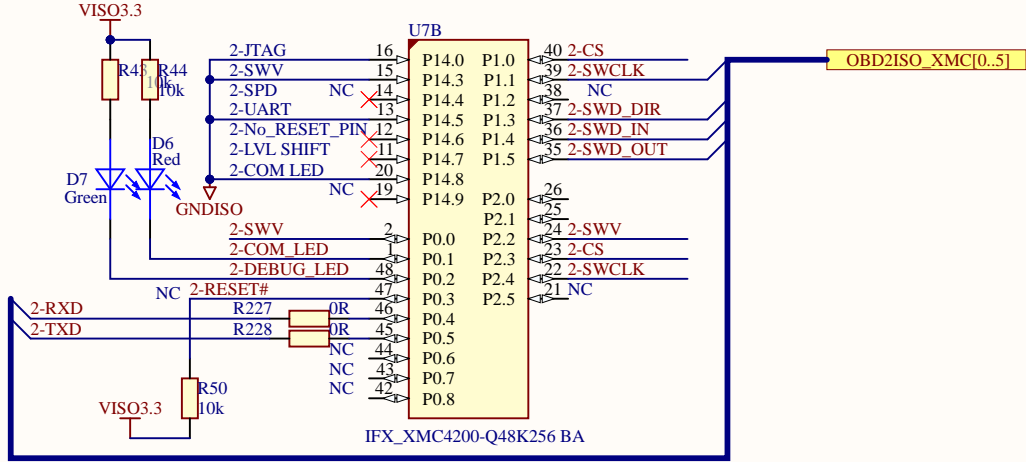
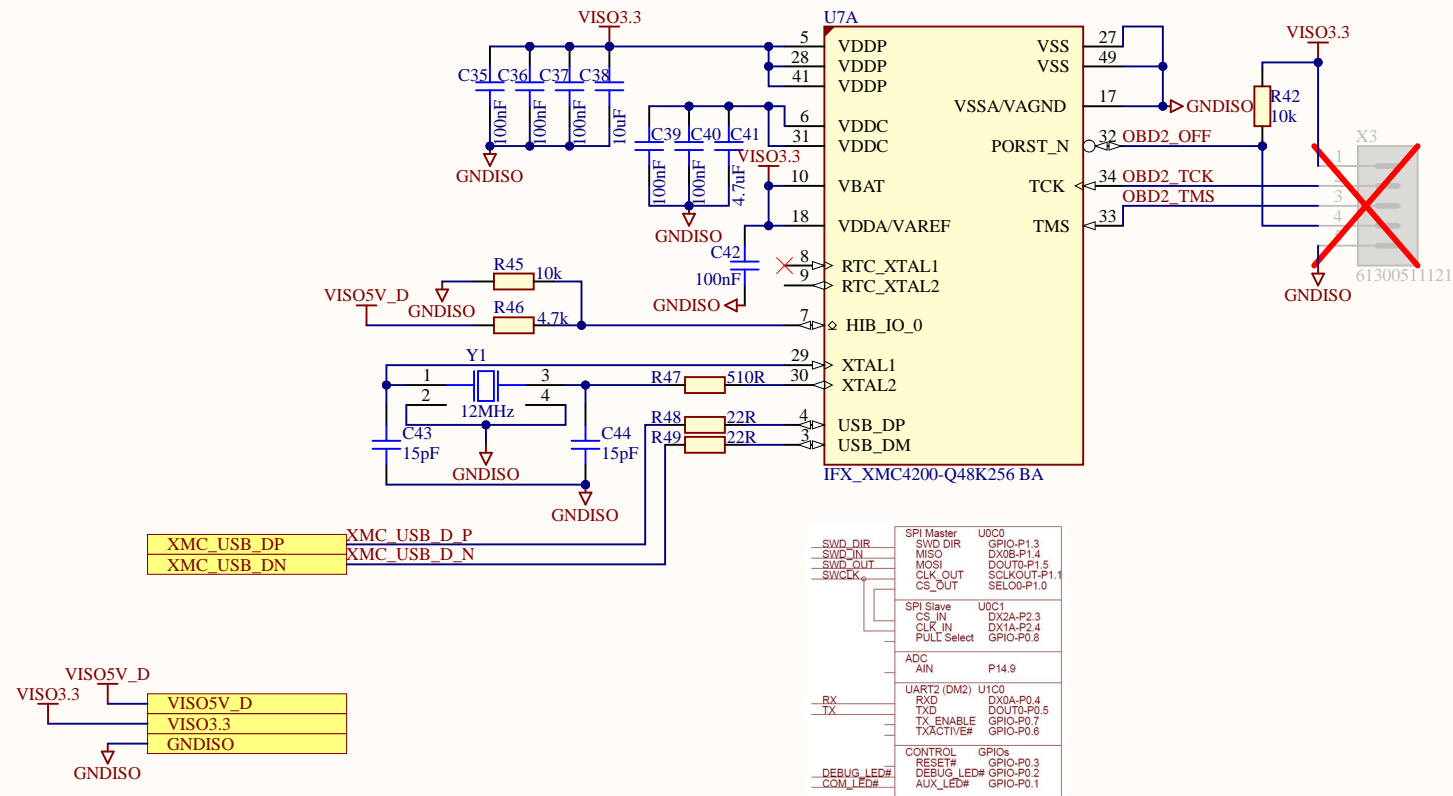
P5LP2_0	Bit0
P5LP2_1	Bit1
P5LP2_2	Bit2
P5LP2_3	Bit3
P5LP2_4	Bit4

Note: ID=0x0D, GND is read as binary "1" and floating as "0".



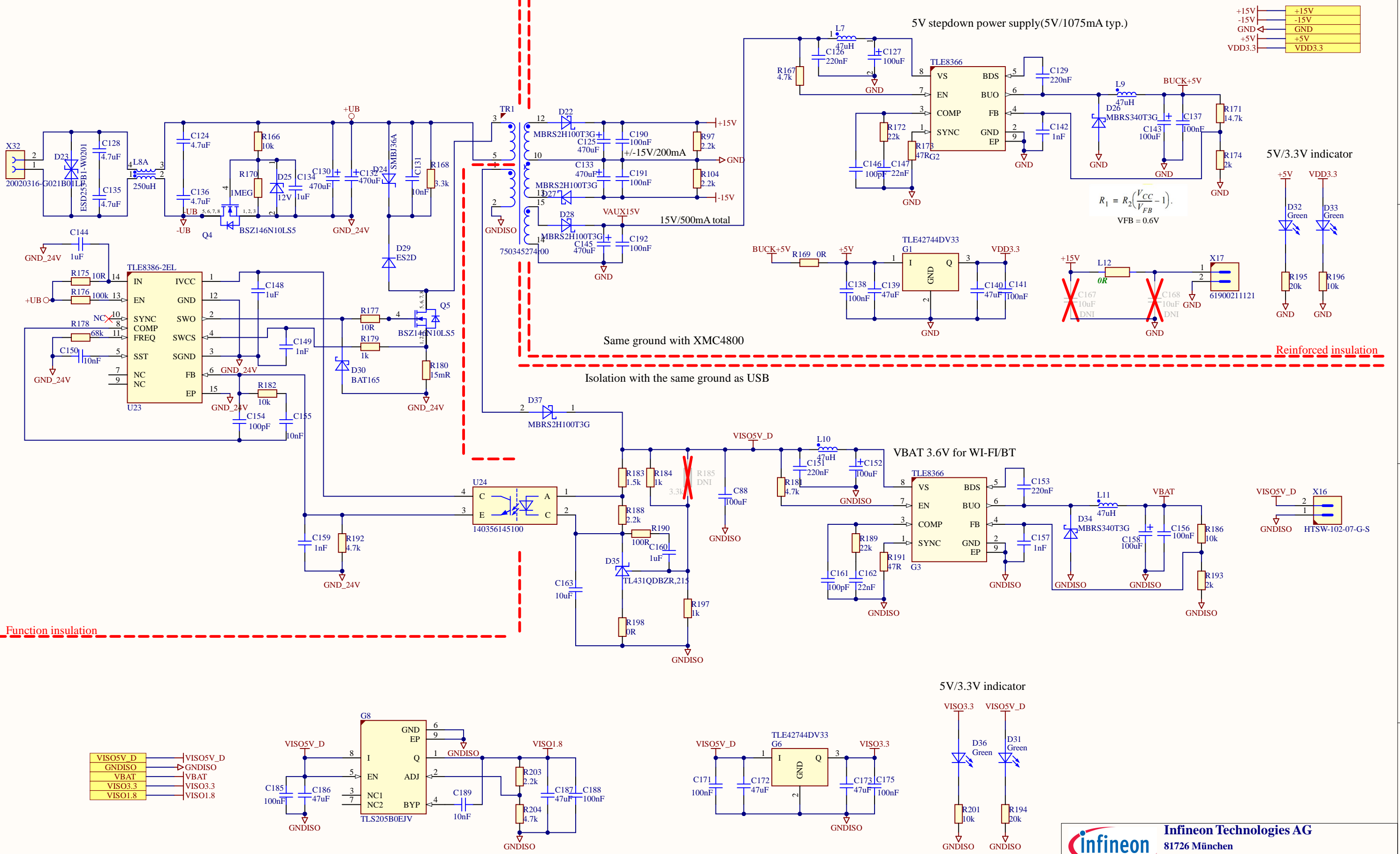
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11_OBD-XMC.SchDoc



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12_Power supply.SchDoc



+15V	+15V
-15V	-15V
GND	GND
+5V	+5V
VDD3.3	VDD3.3

$$R_1 = R_2 \left(\frac{V_{CC}}{V_{FB}} - 1 \right)$$

VFB = 0.6V

VISO5V_D	VISO5V_D
GNDISO	GNDISO
VBAT	VBAT
VISO3.3	VISO3.3
VISO1.8	VISO1.8

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