

IGBT Chip in NPT-technology

FEATURES:

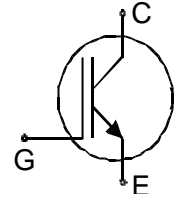
- 1200V NPT technology
- 180µm chip
- short circuit prove
- positive temperature coefficient
- easy paralleling

This chip is used for:

- chip only

Applications:

- drives



| Chip Type | V _{CE} | I _{Cn} | Die Size | Package | Ordering Code |
|--------------|-----------------|-----------------|--------------------------|--------------|-------------------|
| SIGC16T120CL | 1200V | 8A | 4.04 x 4 mm ² | sawn on foil | Q67041-A4703-A003 |

MECHANICAL PARAMETER:

| | | |
|---------------------------------|--|-----------------|
| Raster size | 4.04 x 4 | mm ² |
| Area total / active | 16.16 / 10.4 | |
| Emitter pad size | 1.88 x 2.18 | |
| Gate pad size | 0.71x1.08 | |
| Thickness | 180 | µm |
| Wafer size | 150 | mm |
| Flat position | 0 | deg |
| Max.possible chips per wafer | 898 pcs | |
| Passivation frontside | Photoimide | |
| Emitter metalization | 3200 nm Al Si 1% | |
| Collector metalization | 1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding | |
| Die bond | electrically conductive glue or solder | |
| Wire bond | Al, ≤500µm | |
| Reject Ink Dot Size | Ø 0.65mm ; max 1.2mm | |
| Recommended Storage Environment | store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C | |

MAXIMUM RATINGS:

| Parameter | Symbol | Value | Unit |
|---|----------------|---------------|--------------------|
| Collector-emitter voltage, $T_j=25\text{ °C}$ | V_{CE} | 1200 | V |
| DC collector current, limited by T_{jmax} | I_C | ¹⁾ | A |
| Pulsed collector current, t_p limited by T_{jmax} | I_{Cpuls} | 24 | A |
| Gate emitter voltage | V_{GE} | ± 20 | V |
| Operating junction and storage temperature | T_j, T_{stg} | -55 ... +150 | $^{\circ}\text{C}$ |

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), $T_j=25\text{ °C}$, unless otherwise specified:

| Parameter | Symbol | Conditions | Value | | | Unit |
|--------------------------------------|---------------|-------------------------------|-------|------|------|---------|
| | | | min. | typ. | max. | |
| Collector-emitter breakdown voltage | $V_{(BR)CES}$ | $V_{GE}=0V, I_C=500\mu A$ | 1200 | | | V |
| Collector-emitter saturation voltage | $V_{CE(sat)}$ | $V_{GE}=15V, I_C=8A$ | 1.8 | 2.2 | 2.6 | |
| Gate-emitter threshold voltage | $V_{GE(th)}$ | $I_C=350\mu A, V_{GE}=V_{CE}$ | 4.5 | 5.5 | 6.5 | |
| Zero gate voltage collector current | I_{CES} | $V_{CE}=1200V, V_{GE}=0V$ | | | 1.1 | μA |
| Gate-emitter leakage current | I_{GES} | $V_{CE}=0V, V_{GE}=20V$ | | | 120 | nA |

DYNAMIC CHARACTERISTICS (tested at component):

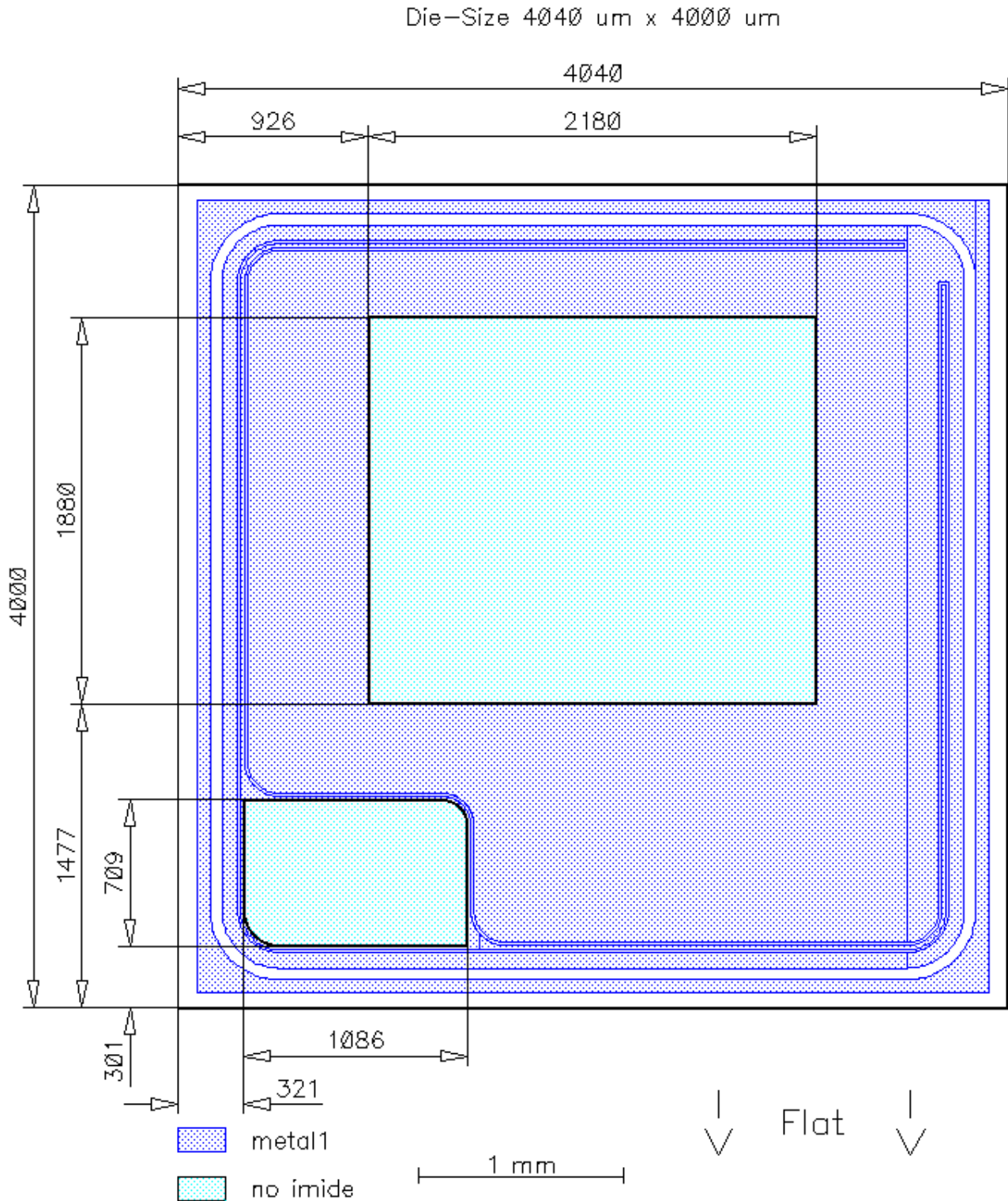
| Parameter | Symbol | Conditions | Value | | | Unit |
|------------------------------|------------|-----------------|-------|------|------|------|
| | | | min. | typ. | max. | |
| Input capacitance | C_{iss} | $V_{CE}=25V,$ | - | 556 | - | pF |
| Output capacitance | C_{oss} | $V_{GE}=0V,$ | - | - | - | |
| Reverse transfer capacitance | C_{riss} | $f=1\text{MHz}$ | - | 38 | - | |

SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

| Parameter | Symbol | Conditions ¹⁾ | Value | | | Unit |
|---------------------|--------------|-------------------------------------|-------|------|------|------|
| | | | min. | typ. | max. | |
| Turn-on delay time | $t_{d(on)}$ | $T_j=125\text{ °C}$ | - | 45 | - | ns |
| Rise time | t_r | $V_{CC}=600V,$ $I_C=10A$ | - | 40 | - | |
| Turn-off delay time | $t_{d(off)}$ | $V_{GE}=\pm 15V,$ $R_G=82\Omega$ | - | 285 | - | |
| Fall time | t_f | | - | 60 | - | |

¹⁾ values also influenced by parasitic L- and C- in measurement and package.

CHIP DRAWING:



FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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