

Product Brief

SAK-TC212S-8F133SC AC

Enabling safety applications

AURIX™ is Infineon's brand new family of microcontrollers serving the needs of all safety critical automotive applications. It is based on a new generation TriCore™ cores, ranging from single core devices, up to microcontrollers with 3 independent CPUs.

Additional lockstep cores provide excellent fault detection and fast reaction times for ASIL-D safety systems.

The scalability in terms of performance, memory and packages within the AURIX™ family allows for a common safety case across the different devices, allowing single applications to be hosted on the smaller devices, but also allows multiple applications to be hosted in parallel on the larger devices without the need to modify software architecture or safety strategies.

Features

- › Diverse lockstep architecture to reduce development effort for ASIL-D systems
- › High integration for reduced complexity and significant cost savings
- › Innovative single supply concept for best-in-class power consumption and cost savings in external supply
- › Scalability in terms of performance, packages, memory and peripherals for flexibility across platform concepts
- › Available as single and lockstep core
- › Latest connectivity CAN FD (flexible data rate)
- › Scalable safety from QM to ASIL-D
- › Hot package options for extended temperature range
- › Including wireless charging SW IP and support

Main features

Features SAK-TC212S-8F133SC AC

- › TriCore™ with 133 MHz
- › TriCore™ DSP functionality
- › Up to 0.5 MB flash w/ECC protection
- › 64 KB EEPROM at 125 k cycles
- › Up to 56 KB RAM w/ECC protection
- › 16x DMA channels
- › 24x 12-bit SAR ADC converter
- › Powerful Generic Timer Module (GTM)
- › 4x SENT sensor interfaces

- › State of the art connectivity: 2x LIN, 4x QSPI, 3x CAN including data rate enhanced CAN FD
- › Wake-up timer
- › Including wireless charging SW IP and support
- › Single voltage supply 3.3 V
- › TQFP-80 package

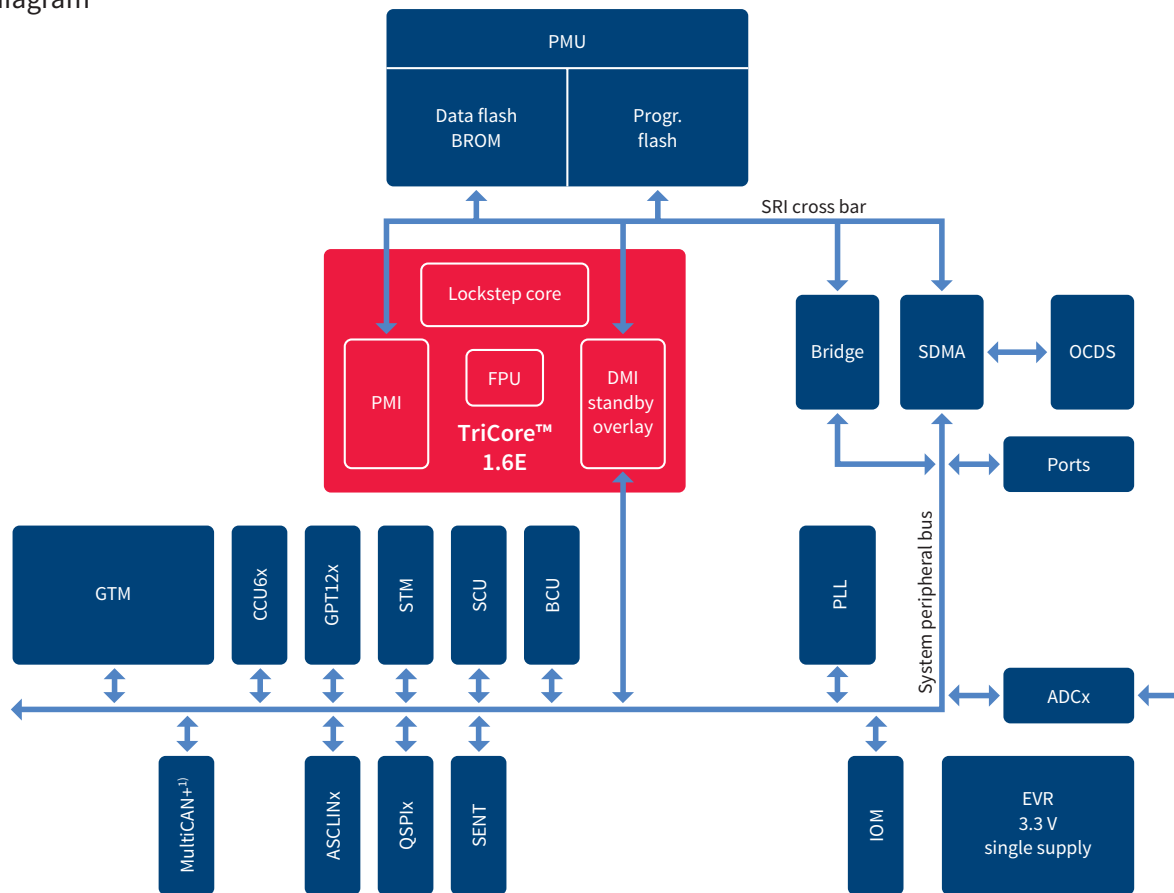
Most innovative safety

- › Diverse lockstep core with clock delay
- › Redundant and diverse timer modules (GTM, CCU6, GPT12)
- › Access permission system
- › Safety management unit
- › Safe DMA
- › I/O, clock, voltage monitor
- › ISO 26262 compliance to support safety requirements up to ASIL-D
- › AUTOSAR V3.2 and V4.x

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Block diagram



1) MultiCAN+ including data rate enhanced CAN FD

Product summary

Type	eFlash [KB]	Data flash [KB]	Frequency [MHz]	SRAM [KB]	Package	Temp. range [°C]
SAK-TC212S-8F133SC AC	512	64 ¹⁾	133	56	TQFP-80	-40 ... +125 ²⁾

1) EEPROM emulation (up to 125 k w/e cycles)

2) Hot package options with $T_s = 150^\circ\text{C}$ are available on request

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