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# Cypress Semiconductor Automotive Product Qualification Report

QTP# 150408 VERSION \*C  
January 2017

<b>Automotive 4-MBIT and 2-MBIT Asynchronous SRAM Family ULL65 (LL65UP-25ODR) Technology, UMC Fab12A</b>	
<b>CY62146G* CY62147G* CY621472G*</b>	<b>MOBL® AUTOMOTIVE, 4-MBIT (256K WORDS X 16 BIT) STATIC RAM WITH ERROR-CORRECTING CODE (ECC)</b>
<b>CY7C1041G*</b>	<b>FAST AUTOMOTIVE, 4-MBIT (256K WORDS X 16 BIT) STATIC RAM WITH ERROR-CORRECTING CODE (ECC)</b>
<b>CY7C1011G*</b>	<b>FAST AUTOMOTIVE, 2-MBIT (128K WORDS X 16 BIT) STATIC RAM WITH ERROR-CORRECTING CODE (ECC)</b>

**FOR ANY QUESTIONS ON THIS REPORT, PLEASE CONTACT**  
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## PRODUCT QUALIFICATION HISTORY

Qual Report	Description of Qualification Purpose	Date Comp
133601	Qualification of Automotive 16-MBIT Asynchronous SRAM Family, ULL65nm (LL65UP-250DR) Technology, UMC Fab12A	June 15
150408	Qualification of 4-MBIT and 2-MBIT Asynchronous SRAM Family, ULL65 (LL65UP-250DR) Technology at UMC Fab12A	Sept 15

PRODUCT DESCRIPTION (for qualification)	
Qualification of 4-MBIT Asynchronous SRAM Family, ULL65 (LL65UP-250DR) Technology at UMC Fab12A	
Marketing Part #:	CY62146G*, CY62146G30*, CY62147G30*, CY621472G30* CY7C1041G30*, CY7C1011G30*
Device Description:	Automotive, 4MB (256K X 16 ) / 2MB (128KX16) Static Ram With Error Correcting Code (ECC)
Cypress Division:	Cypress Semiconductor Corporation – Memory Products Division (MPD)

TECHNOLOGY/FAB PROCESS DESCRIPTION			
Number of Metal Layers:	5 Metal + 1 RDL	Metal Composition:	Metal 1: Cu 0.18um Metal 2: Cu 0.22um Metal 3: Cu 0.22um Metal 4: Cu 0.36um Metal 5: Cu 1.25um
Passivation Type and Materials:	0.4um oxide/0.5um nitride		
Generic Process Technology/Design Rule (□-drawn):	CMOS 65nm		
Gate Oxide Material/Thickness (MOS):	SiON / 19.5 A		
Name/Location of Die Fab (prime) Facility:	UMC / Taiwan		
Die Fab Line ID/Wafer Process ID:	Fab12 L65LL		

## PACKAGE AVAILABILITY

PACKAGE	ASSEMBLY FACILITY SITE	QTP NUMBER
48-Lead VFBGA (6x8x1.2mm)	ASEK-Taiwan (G)	150417
48-Lead VFBGA (6x8x1.0mm)	ASEK-Taiwan (G)	150418
44-Lead TSOP II (400mil)	JCET-China (JT)	150416

### MAJOR PACKAGE INFORMATION USED IN THIS QUALIFICATION

Package Designation:	ZW44A
Package Outline, Type, or Name:	44L TSOPII (400mils)
Mold Compound Name/Manufacturer:	KE-G6000DA / Kyocera
Mold Compound Flammability Rating:	V0 UL94
Mold Compound Alpha Emission Rate:	<0.001 CPH/cm2
Oxygen Rating Index: >28%	28%
Lead Frame Designation:	FMP with thru slots
Lead Frame Material:	Cu
Substrate Material:	N/A
Lead Finish, Composition / Thickness:	Roughened NiPdAu PPF
Die Backside Preparation Method/Metallization:	Backgrind to 7mils
Die Separation Method:	Laser Groove + Mech'l Saw
Die Attach Supplier:	Ablestik
Die Attach Material:	QMI-509
Bond Diagram Designation	001-95778
Wire Bond Method:	Thermosonic
Wire Material/Size:	Au / 0.8 mil
Thermal Resistance Theta JA °C/W:	66.82 C/W
Package Cross Section Yes/No:	Yes
Assembly Process Flow:	002-03840
Name/Location of Assembly (prime) facility:	JCET – China (JT)
MSL LEVEL	3
REFLOW PROFILE	260C

### ELECTRICAL TEST / FINISH DESCRIPTION

<b>Test Location:</b>	Chipmos Taiwan (GO)
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**Note:** Please contact a Cypress Representative for other packages availability.

MAJOR PACKAGE INFORMATION USED IN THIS QUALIFICATION	
Package Designation:	BK48M
Package Outline, Type, or Name:	48L VFBGA (6x8x1.2mm)
Mold Compound Name/Manufacturer:	KE-G2250 / Kyocera
Mold Compound Flammability Rating:	V0 UL94
Mold Compound Alpha Emission Rate:	0.001 CPH/cm2
Oxygen Rating Index: >28%	28%
Lead Frame Designation:	N/A
Lead Frame Material:	N/A
Substrate Material:	BT
Lead Finish, Composition / Thickness:	SAC105 (SnAgCu)
Die Backside Preparation Method/Metallization:	Backgrind to 7mils
Die Separation Method:	Laser Groove + Mech'l Saw
Die Attach Supplier:	Ablestik
Die Attach Material:	Ablebond 2100A
Bond Diagram Designation	001-95781
Wire Bond Method:	Thermosonic
Wire Material/Size:	Au / 0.8mil
Thermal Resistance Theta JA °C/W:	30.68 C/W
Package Cross Section Yes/No:	Y
Assembly Process Flow:	002-03863
Name/Location of Assembly (prime) facility:	ASEK-Taiwan (G)
MSL LEVEL	3
REFLOW PROFILE	260C

ELECTRICAL TEST / FINISH DESCRIPTION	
Test Location:	Chipmos Taiwan (GO)

### MAJOR PACKAGE INFORMATION USED IN THIS QUALIFICATION

Package Designation:	BZ48A
Package Outline, Type, or Name:	48L VFBGA (6x8x1.0mm)
Mold Compound Name/Manufacturer:	KE-G2250 / Kyocera
Mold Compound Flammability Rating:	V0 UL94
Mold Compound Alpha Emission Rate:	0.001 CPH/cm2
Oxygen Rating Index: >28%	28%
Lead Frame Designation:	N/A
Lead Frame Material:	N/A
Substrate Material:	BT
Lead Finish, Composition / Thickness:	SAC105 (SnAgCu)
Die Backside Preparation Method/Metallization:	Backgrind to 7mils
Die Separation Method:	Laser Groove + Mech'l Saw
Die Attach Supplier:	Ablestik
Die Attach Material:	Ablebond 2100A
Bond Diagram Designation	001-95780, 001-85194
Wire Bond Method:	Thermosonic
Wire Material/Size:	Au / 0.8mil
Thermal Resistance Theta JA °C/W:	30.68 C/W
Package Cross Section Yes/No:	Y
Assembly Process Flow:	002-03864
Name/Location of Assembly (prime) facility:	ASEK-Taiwan (G)
MSL LEVEL	3
REFLOW PROFILE	260C

### ELECTRICAL TEST / FINISH DESCRIPTION

Test Location:	Chipmos Taiwan (GO)
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### MAJOR PACKAGE INFORMATION USED IN THIS QUALIFICATION

Package Designation:	BZ48A
Package Outline, Type, or Name:	48L VFBGA 6x8x1.0mm
Mold Compound Name/Manufacturer:	GR9810 / Henkel
Mold Compound Flammability Rating:	V0 UL94
Mold Compound Alpha Emission Rate:	0.002 CPH/cm <sup>2</sup>
Oxygen Rating Index: >28%	54 (Typical) / 28 (Min. value)
Lead Frame Designation:	N/A
Lead Frame Material:	N/A
Substrate Material:	BT / KIT
Lead Finish, Composition / Thickness:	SAC-105
Die Backside Preparation Method/Metallization:	Backgrind to 7mils
Die Separation Method:	100% Saw
Die Attach Supplier:	Henkel
Die Attach Material:	QMI-506
Bond Diagram Designation	001-91668
Wire Bond Method:	Thermosonic
Wire Material/Size:	CuPd / 0.8mil
Thermal Resistance Theta JA °C/W:	32 degC /W
Package Cross Section Yes/No:	Y
Assembly Process Flow:	002-03811
Name/Location of Assembly (prime) facility:	CML-Philippines (RA)
MSL LEVEL	3
REFLOW PROFILE	260C

### ELECTRICAL TEST / FINISH DESCRIPTION

<b>Test Location:</b>	Chipmos-Taiwan (GO), CML-Philippines (RA)
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**Note:** Please contact a Cypress Representative for other packages availability.



### MAJOR PACKAGE INFORMATION USED IN THIS QUALIFICATION

Package Designation:	ZT48A
Package Outline, Type, or Name:	48L TSOP I
Mold Compound Name/Manufacturer:	EME-G631H / Sumitomo
Mold Compound Flammability Rating:	V-0 / UL94
Mold Compound Alpha Emission Rate:	<0.0010 CPH/cm2
Oxygen Rating Index: >28%	>28%
Lead Frame Designation:	FMP
Lead Frame Material:	Copper
Substrate Material:	N/A
Lead Finish, Composition / Thickness:	Pure Sn
Die Backside Preparation Method/Metallization:	Backgrind
Die Separation Method:	Saw
Die Attach Supplier:	Sumitomo
Die Attach Material:	CRM-1076WA
Bond Diagram Designation	001-88666
Wire Bond Method:	Thermosonic
Wire Material/Size:	CuPd / 0.8mil
Thermal Resistance Theta JA °C/W:	56.94 degC/W
Package Cross Section Yes/No:	Y
Assembly Process Flow:	49-41001
Name/Location of Assembly (prime) facility:	ASEK-Taiwan (G)
MSL LEVEL	3
REFLOW PROFILE	260C

### ELECTRICAL TEST / FINISH DESCRIPTION

Test Location:	Chipmos Taiwan (GO)
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**Note:** Please contact a Cypress Representative for other packages availability.

### RELIABILITY TESTS PERFORMED PER SPECIFICATION REQUIREMENT

Stress/Test	Test Condition (Temp/Bias)	Result P/F
High Temperature Operating Life Early Failure Rate	AEC-Q100-008 and JESD22-A108, 125°C Dynamic Operating Condition, Vcc Max = 1.44V	P
High Temperature Operating Life Latent Failure Rate	JESD22-A108, 125°C Dynamic Operating Condition, Vcc Max = 1.44V	P
High Accelerated Saturation Test (HAST)	JESD22-A110, 130C, 3.65V, 85%RH Precondition: JESD22-A113 Moisture Sensitivity MSL 3 192 Hrs, 30C/60%RH+3IR-Reflow, 260°C+0, -5°C	P
Temperature Cycle	JESD22-A104, -65°C to 150°C Precondition: JESD22-A113 Moisture Sensitivity MSL 3 192 Hrs, 30C/60%RH+3IR-Reflow, 260°C+0, -5°C	P
Pressure Cooker	JESD22-A102, 121C, 100%RH, 15 Psig Precondition: JESD22-A113 Moisture Sensitivity MSL 3 192 Hrs, 30C/60%RH+3IR-Reflow, 260°C+0, -5°C	P
Electrostatic Discharge Human Body Model (ESD-HBM)	AEC-Q100-002 500V/1000V/1500V/2000V/4000V/6000V	P
Electrostatic Discharge Charge Device Model (ESD-CDM)	AEC-Q100-011 250V/500V/750V (corner pins)	P
Electrostatic Discharge Machine Model (ESD-MM)	200V, JESD22-A115-A	P
Wire Ball Shear	AEC-Q100-001	P
Wire Bond Pull	Mil-Std 883, Method 2011	P
Electrical Distribution	AEC-Q100-009	P
Soft Error (Alpha Particle)	JESD89	P
Final Visual	JESD22-B101B	P
Physical Dimensions	JESD22-B100/108	P
Solderability	JESD22-B102	P
Post Temperature Cycle Wire Bond Pull	Mil-Std 883, Method 2011	P
High Temperature Storage Life Test	JESD22-A103, 150 C	P
Dye Penetrant Test	Criteria: No Package Crack	P
Static Latch-up	AEC-Q100-004, +/-140mA, 85C/125C	P
Constructional Analysis	Criteria: Meet external and internal characteristics of Cypress package	P
Acoustic	J-STD-020 Precondition: JESD22-A113 Moisture Sensitivity MSL 3 192 Hrs, 30C/60%RH+3IR-Reflow, 260C+0, -5C	P

### RELIABILITY FAILURE RATE SUMMARY

Stress/Test	Device Tested/ Device Hours	# Fails	Activation Energy	Thermal AF <sup>3</sup>	Failure Rate
High Temperature Operating Life Early Failure Rate	10,489 Devices	0	N/A	N/A	0 PPM
High Temperature Operating Life Long Term Failure Rate	1,445,000	0	0.7	55	11 FIT

<sup>1</sup> Assuming an ambient temperature of 55°C and a junction temperature rise of 15°C.

<sup>2</sup> Chi-squared 60% estimations used to calculate the failure rate..

<sup>3</sup> Thermal Acceleration Factor is calculated from the Arrhenius equation

$$AF = \exp \left[ \frac{E_A}{k} \left[ \frac{1}{T_2} - \frac{1}{T_1} \right] \right]$$

where:

E<sub>A</sub> =The Activation Energy of the defect mechanism.

K = Boltzmann's constant = 8.62x10<sup>-5</sup> eV/Kelvin.

T<sub>1</sub> is the junction temperature of the device under stress and T<sub>2</sub> is the junction temperature of the device at use conditions.



## Reliability Test Data

### QTP #: 133601

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
<b>STRESS: ACOUSTIC, MSL3</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	22	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	22	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	22	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	COMP	22	0	
CY7C1061G30 (7AP171061AO)	9313001	611422548	ASE-G	COMP	22	0	
<b>STRESS: BALL SHEAR</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	100	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	100	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	100	0	
<b>STRESS: BOND PULL</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	100	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	100	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	100	0	
<b>STRESS: CONSTRUCTIONAL ANALYSIS</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	5	0	
<b>STRESS: DYE PENETRANT TEST</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	15	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	15	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	15	0	



## Reliability Test Data

**QTP #: 133601**

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
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**STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-EARLY FAILURE RATE, 125C, 1.44V, Vcc Max**

CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	96	275	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	96	264	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	96	107	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	96	560	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	96	556	0	
CY7C1061G30 (7AP171061AO)	9313001	611508409	ASE-G	96	271	0	
CY7C1061G30 (7AP171061AO)	9313001	611511127	ASE-G	96	361	0	
CY62167G30 (7CC1721673AO)	9423005	611500929	CML-RA	96	927	0	
CY62167G30 (7CC1721673AO)	9438003	611506601	CML-RA	96	3609	0	
CY62167G30 (7CC1721673AO)	9423006	611440524	CML-RA	96	1600	0	
CY7C1061G30 (7AP1710612AO)	9423006	611422550	ASE-G	96	761	0	
CY7C1061G30 (7CC171061AO)	9324001	611342911	ASE-G	96	1627	0	
CY7C1061G30 (7AP1710612AO)	9341020	611422551	ASE-G	96	1091	0	

**STRESS: ELECTRICAL DISTRIBUTION**

CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	30	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	30	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	30	0	

**STRESS: ESD-CHARGE DEVICE MODEL**

CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	250	3	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	500	3	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	750	3	0	
CY62167G30 (7AP172167AO)	9438001	611503292	ASE-G	250	3	0	
CY62167G30 (7AP172167AO)	9438001	611503292	ASE-G	500	3	0	
CY62167G30 (7AP172167AO)	9438001	611503292	ASE-G	750	3	0	

## Reliability Test Data

### QTP #: 133601

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
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#### STRESS: ESD-HUMAN BODY MODEL

CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	500	3	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	1000	3	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	1500	3	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	2000	3	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	4000	3	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	6000	3	0	
CY62167G30 (7AP172167AO)	9438001	611503292	ASE-G	500	3	0	
CY62167G30 (7AP172167AO)	9438001	611503292	ASE-G	1000	3	0	
CY62167G30 (7AP172167AO)	9438001	611503292	ASE-G	2000	3	0	

#### STRESS: ESD-MACHINE MODEL

CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	200	5	0	
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#### STRESS: FINAL VISUAL INSPECTION

CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	986	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	749	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	748	0	

#### STRESS: HI-ACCEL SATURATION TEST, 130C, 3.65V, 85%RH, PRE COND 192 HR 30C/60%RH, MSL3

CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	96	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	192	79	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	96	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	192	79	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	96	74	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	128	74	0	
CY7C1061G30 (7AP171061AO)	9313001	611422548	ASE-G	96	77	0	
CY7C1061G30 (7AP171061AO)	9313001	611422548	ASE-G	128	77	0	



## Reliability Test Data

**QTP #: 133601**

<i>Device</i>	<i>Fab Lot #</i>	<i>Assy Lot #</i>	<i>Assy Loc</i>	<i>Duration</i>	<i>Samp</i>	<i>Rej</i>	<i>Failure Mechanism</i>
<b>STRESS: HIGH TEMPERATURE STORAGE</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	1000	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	2000	80	0	
<b>STRESS: LEAD INTEGRITY</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	5	0	
<b>STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-LATENT FAILURE RATE, 125C, 1.44V, Vcc Max</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	1000	84	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	1000	84	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	1000	84	0	
CY7C1069G30 (7AP171069AO)	9313001	611404705	CML-RA	1000	80	0	
CY7C1069G30 (7AP171069AO)	9313001	611404699	CML-RA	1000	77	0	
CY7C1069G30 (7AP171069AO)	9313001	611404743	CML-RA	1000	80	0	
CY7C1061G30 (7AP1710612AO)	9324001	611404528	ASE-G	1000	80	0	
CY7C1061G30 (7AP1710612AO)	9324001	611404529	ASE-G	1000	80	0	
CY7C1061G30 (7AP1710612AO)	9324001	611404527	ASE-G	1000	80	0	
<b>STRESS: PRESSURE COOKER TEST</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	96	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	168	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	192	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	96	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	168	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	192	78	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	96	79	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	168	79	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	192	78	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	96	77	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	168	77	0	
CY7C1061G30 (7AP171061AO)	9313001	611422548	ASE-G	96	77	0	
CY7C1061G30 (7AP171061AO)	9313001	611422548	ASE-G	168	77	0	

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## Reliability Test Data

### QTP #: 133601

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
<b>STRESS: PHYSICAL DIMENSION</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	30	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	30	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	30	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	COMP	30	0	
CY7C1061G30 (7AP171061AO)	9313001	611422548	ASE-G	COMP	30	0	
<b>STRESS: POST TEMPERATURE CYCLE WIRE BOND PULL</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	500	5	0	
<b>STRESS: PRE/POST LFR CRITICAL PARAMETER</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	30+2	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	30+2	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	30+2	0	
<b>STRESS: STATIC LATCH-UP (+/-140mA 85C)</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	6	0	
CY62167G30 (7AP172167AO)	9438001	611503292	ASE-G	COMP	6	0	
<b>STRESS: STATIC LATCH-UP (+/-180mA 85C)</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	2	0	
<b>STRESS: STATIC LATCH-UP (+/-140mA 125C)</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	2	0	
<b>STRESS: SER – ALPHA PARTICLE SEL, 25C/85C/120C, 1.65V/3.3V/5.5V</b>							
7C1710614GE	0	0	UMC	COMP	3	0	
<b>STRESS: SER – NEUTRON SEL, 85C/125C, 5.25V</b>							
7C17165A	0	0	UMC	COMP	3	0	
<b>STRESS: SOLDERABILITY</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	COMP	15	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	COMP	15	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	COMP	15	0	



## Reliability Test Data

### QTP #: 133601

<i>Device</i>	<i>Fab Lot #</i>	<i>Assy Lot #</i>	<i>Assy Loc</i>	<i>Duration</i>	<i>Samp</i>	<i>Rej</i>	<i>Failure Mechanism</i>
<b>STRESS: TC COND. C -65C TO 150C, PRECONDITION 192 HRS 30C/60%RH</b>							
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	500	80	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	1000	75	0	
CY7C1061G30 (7AP171061AO)	9308001	611340601	ASE-G	2000	75	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	500	79	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	1000	79	0	
CY7C1061G30 (7AP171061AO)	9308001	611336557	ASE-G	2000	79	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	500	79	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	1000	79	0	
CY7C1061G30 (7AP171061AO)	9313001	611336551	ASE-G	2000	79	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	500	77	0	
CY7C1061G30 (7AP171061AO)	9313001	611422547	ASE-G	1000	77	0	
CY7C1061G30 (7AP171061AO)	9313001	611422548	ASE-G	500	77	0	
CY7C1061G30 (7AP171061AO)	9313001	611422548	ASE-G	1000	77	0	



## Reliability Test Data

QTP #: 150408

Device	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
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### STRESS: ELECTRICAL TEST DISTRIBUTION

CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	COMP	30	0	
CY7C1041G30 (7A171041AO)	9507003	611519245	JCET-JT	COMP	30	0	
CY62147G30 (7A172147AO)	9508002	611520074	JCET-JT	COMP	30	0	

### STRESS: ESD-CHARGE DEVICE MODEL

CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	250	3	0	
CY621472G30 (7A1721472AO)	9507003	611519244	JCET-JT	250	3	0	
CY62147G30 (7A172147AO)	9507001	611516597	ASE-G	250	3	0	
CY7C1011G30 (7A1710311AO)	9507003	611518455	ASE-G	250	3	0	
CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	500	3	0	
CY621472G30 (7A1721472AO)	9507003	611519244	JCET-JT	500	3	0	
CY62147G30 (7A172147AO)	9507001	611516597	ASE-G	500	3	0	
CY7C1011G30 (7A1710311AO)	9507003	611518455	ASE-G	500	3	0	
CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	750	3	0	
CY621472G30 (7A1721472AO)	9507003	611519244	JCET-JT	750	3	0	
CY62147G30 (7A172147AO)	9507001	611516597	ASE-G	750	3	0	
CY7C1011G30 (7A1710311AO)	9507003	611518455	ASE-G	750	3	0	

### STRESS: ESD-HUMAN BODY MODEL

CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	500	3	0	
CY621472G30 (7A1721472AO)	9507003	611519244	JCET-JT	500	3	0	
CY62147G30 (7A172147AO)	9507001	611516597	ASE-G	500	3	0	
CY7C1011G30 (7A1710311AO)	9507003	611518455	ASE-G	500	3	0	
CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	1000	3	0	
CY621472G30 (7A1721472AO)	9507003	611519244	JCET-JT	1000	3	0	
CY62147G30 (7A172147AO)	9507001	611516597	ASE-G	1000	3	0	
CY7C1011G30 (7A1710311AO)	9507003	611518455	ASE-G	1000	3	0	
CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	2000	3	0	
CY621472G30 (7A1721472AO)	9507003	611519244	JCET-JT	2000	3	0	
CY62147G30 (7A172147AO)	9507001	611516597	ASE-G	2000	3	0	
CY7C1011G30 (7A1710311AO)	9507003	611518455	ASE-G	2000	3	0	

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## Reliability Test Data

**QTP #: 150408**

<b>Device</b>	<b>Fab Lot #</b>	<b>Assy Lot #</b>	<b>Assy Loc</b>	<b>Duration</b>	<b>Samp</b>	<b>Rej</b>	<b>Failure Mechanism</b>
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**HIGH TEMP DYNAMIC OPERATING LIFE-EARLY FAILURE RATE, 125C, 1.44V, Vcc Max**

CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	COMP	3495	0	
CY7C1041G30 (7A171041AO)	9507003	611519245	JCET-JT	COMP	3497	0	
CY62147G30 (7A172147AO)	9508002	611520074	JCET-JT	COMP	3497	0	

**STRESS: STATIC LATCH-UP (+/-140mA 125C)**

CY7C1041G30 (7A171041AO)	9507001	611517301	JCET-JT	COMP	6	0	
CY621472G30 (7A1721472AO)	9507003	611519244	JCET-JT	COMP	6	0	
CY62147G30 (7A172147AO)	9507001	611516597	ASE-G	COMP	6	0	
CY7C1011G30 (7A1710311AO)	9507003	611518455	ASE-G	COMP	6	0	



## Reliability Test Data

**QTP #: 150416**

<i>Device</i>	<i>Package</i>	<i>Fab Lot #</i>	<i>Assy Lot #</i>	<i>Assy Loc</i>	<i>Duration</i>	<i>Samp</i>	<i>Rej</i>	<i>Failure Mechanism</i>
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**STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-LATENT FAILURE RATE, 125C, 1.44V, Vcc Max**

CY7C1041G30 (7A171041AO)	ZW44A	9507001	611517301	JCET-JT	1000	80	0	
CY7C1041G30 (7A171041AO)	ZW44A	9507001	611517304	JCET-JT	1000	80	0	
CY62147G30 (7A172147AO)	ZW44A	9507001	611517303	JCET-JT	1000	80	0	



## Reliability Test Data

**QTP #: 150417**

<i>Device</i>	<i>Package</i>	<i>Fab Lot #</i>	<i>Assy Lot #</i>	<i>Assy Loc</i>	<i>Duration</i>	<i>Samp</i>	<i>Rej</i>	<i>Failure Mechanism</i>
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**STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-LATENT FAILURE RATE, 125C, 1.44V, Vcc Max**

CY7C1041G30 (7A1710411AO)	BK48M	9507001	611516555	ASEK-G	1000	79	0	
CY7C1041G30 (7A1710411AO)	BK48M	9507001	611516556	ASEK-G	1000	80	0	
CY7C1041G30 (7A1710411AO)	BK48M	9507001	611516554	ASEK-G	1000	80	0	



## Reliability Test Data

### QTP #: 150418

Device	Package	Fab Lot #	Assy Lot #	Assy Loc	Duration	Samp	Rej	Failure Mechanism
<b>STRESS: HIGH TEMP DYNAMIC OPERATING LIFE-LATENT FAILURE RATE, 125C, 1.44V, Vcc Max</b>								
CY62147G30 (7A172147AO)	BK48A	9507001	611516597	ASEK-G	1000	80	0	
CY62147G30 (7A172147AO)	BK48A	9507001	611516595	ASEK-G	1000	79	0	
CY62147G30 (7A172147AO)	BK48A	9507001	611516596	ASEK-G	1000	78	0	

## Document History Page

Document Title: QTP#150408: AUTOMOTIVE 4-MBIT ASYNCHRONOUS SRAM FAMILY ULL65 (LL65UP-25ODR) TECHNOLOGY, UMC FAB12A  
Document Number: 002-03894

Rev.	ECN No.	Orig. of Change	Description of Change
**	4949693	HSTO	Initial spec release
*A	5218255	HSTO	Update title
*B	5481388	HSTO	Update Reliability Director contact person Add LFR data and FIT rate computation
*C	5603202	RT	Revised History table to Add Amazon Auto qual - 133601 Added Major Package Information for Amazon Auto Added Amazon shared Data Recalculated FIT value with additional HTOL Life stress device hours.