

# Product Qualification Report

## ESD0P8RFL

ESD protection of RF antenna / interfaces or ultra high speed data lines

### Description

This product qualification report describes the characteristics of the product with respect to quality and reliability.

The qualification sample selection was done on production lots which were manufactured and tested on standard production processes and meet the defined requirements.

The qualification test results of those products as outlined in this document are based on **JEDEC** for target applications and may reference existing qualification results of similar products. Such referencing is justified by the structural similarity of the products.

### Qualification Assessment

Fully qualified according to **JEDEC** for **Industrial Applications** and assessed as PASS

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**ESD0P8RFL**  
**PG-TSLP-4-7**  
**MSL: 1; 260 °C**

**qualified before 2011**

**Electrical Stress Test Results:**

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Electrical Parameter Assessment JESD86	ED	-55 °C - 150 °C		3 x 10	0 / 30	PASS
High Temperature Reverse Bias JESD22-A108	HTRB	T <sub>j</sub> = 150 °C V <sub>dd</sub> = V <sub>dd_max</sub>	1000 h	3 x 77	0 / 231	PASS

**Environmental Stress Test Results:**

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
High Temperature Storage Life JESD22-A103	HTSL	T <sub>a</sub> = 175 °C	1000 h	3 x 25	0 / 75	PASS
Pre-conditioning JESD22-A113, J-STD020	PC	Soak acc. MSL 1, 3x reflow, 260 °C		3 x 75	0 / 225	PASS
Temperature Cycling JESD22-A104	TC*	-55 °C to 150 °C	1000 cycles	3 x 25	0 / 75	PASS
Autoclave JESD22-A102	AC*	T <sub>a</sub> = 121 °C rh = 100 % p = 2.1 bar	96 h	3 x 25	0 / 75	PASS
High Humidity, High Temperature Bias JESD22-A101	H3TRB*	T <sub>a</sub> = 85 °C rh = 85 % V <sub>dd</sub> = V <sub>dd_max</sub>	1000 h	3 x 25	0 / 75	PASS

**Mechanical Stress Test Results:**

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Physical Dimensions JESD22-B100	PD			1 x 30	0 / 30	PASS
Solderability J-STD-002	SD			3 x 22	0 / 66	PASS

**Notes:**

\* For SMD devices reliability stress tests performed after preconditioning test (PC) according to JESD22-A113

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### Published by

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81726 München, Germany

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**Do you have a question about this document?**

**Email:** [erratum@infineon.com](mailto:erratum@infineon.com)

### Document reference

**n.a.**

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