

Please note that Cypress is an Infineon Technologies Company.

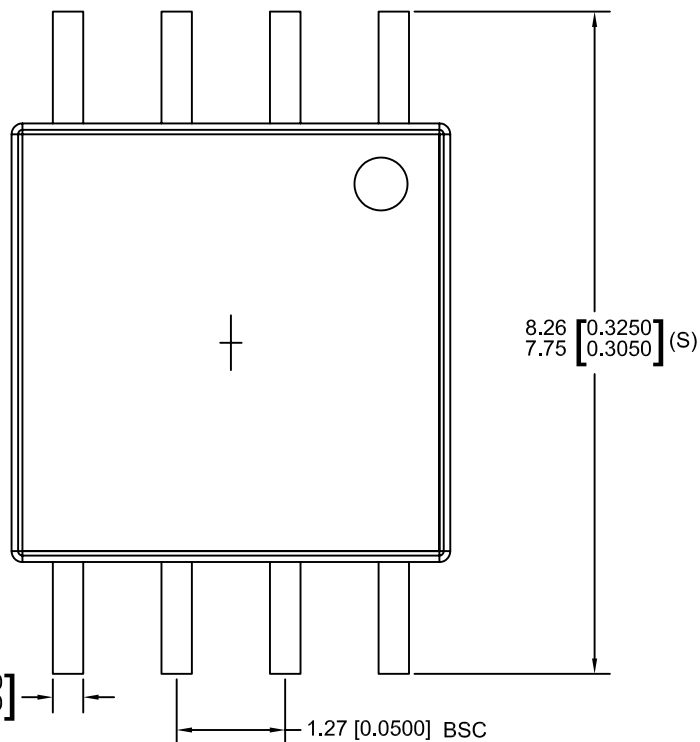
The document following this cover page is marked as “Cypress” document as this is the company that originally developed the product. Please note that Infineon will continue to offer the product to new and existing customers as part of the Infineon product portfolio.

Continuity of document content

The fact that Infineon offers the following product as part of the Infineon product portfolio does not lead to any changes to this document. Future revisions will occur when appropriate, and any changes will be set out on the document history page.

Continuity of ordering part numbers

Infineon continues to support existing part numbers. Please continue to use the ordering part numbers listed in the datasheet for ordering.

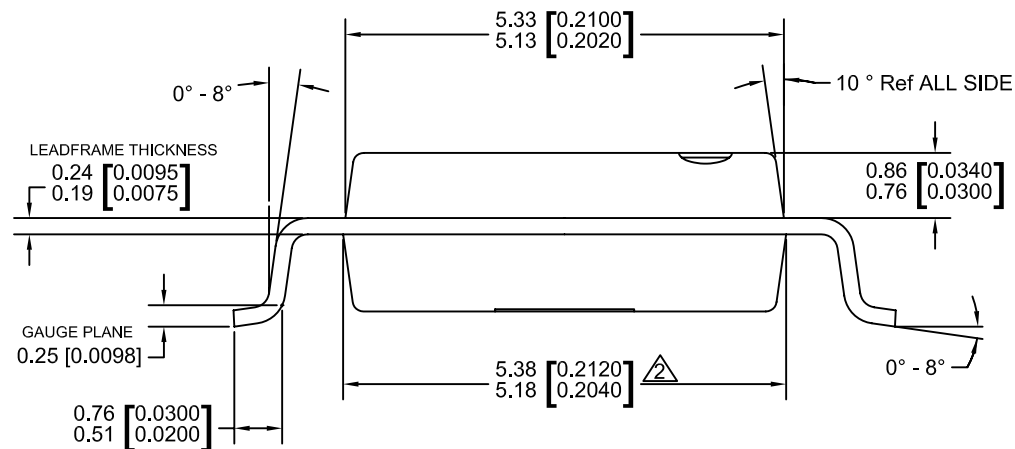
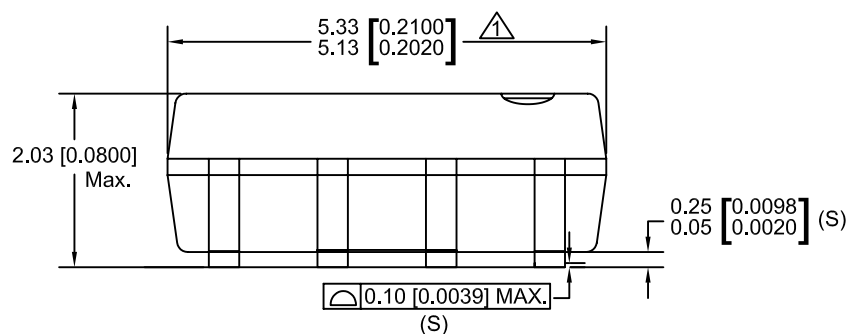


NOTE:

△ DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED 0.006 INCH PER SIDE

△ DOES NOT INCLUDE INTER-LEAD FLASH OR PROTRUSIONS. INTER-LEAD FLASH AND PROTRUSION SHALL NOT EXCEED 0.010 INCH PER SIDE.

- THIS PART IS COMPLIANT WITH EIAJ SPECIFICATION EDR-7320
- LEAD SPAN/STAND OF HEIGHT/COPLANARITY ARE CONSIDERED AS SPECIAL CHARACTER.
- CONTROLLING DIMENSIONS IN MM. [INCH]



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PACKAGE CODE(S)

SZ820



CYPRESS
Company Confidential

TITLE PACKAGE OUTLINE, 8LD SOIC 208MILS SZ820

SPEC. NO. 001-85261


REV. **

SCALE 1:1

SHEET 1 OF 2

1	2	3	4	5	6	
A						A
B						B
C						C
D						D

Rev.	ECN No.	Origin of Change	Description of change
**	3904942	XANC	Created Spec.



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TITLE
PACKAGE OUTLINE, 8LD SOIC 208MILS SZ820

SPEC. NO.
001-85261

SCALE 1:1

REV.

SHEET 2 OF 2

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