

Please note that Cypress is an Infineon Technologies Company.

The document following this cover page is marked as “Cypress” document as this is the company that originally developed the product. Please note that Infineon will continue to offer the product to new and existing customers as part of the Infineon product portfolio.

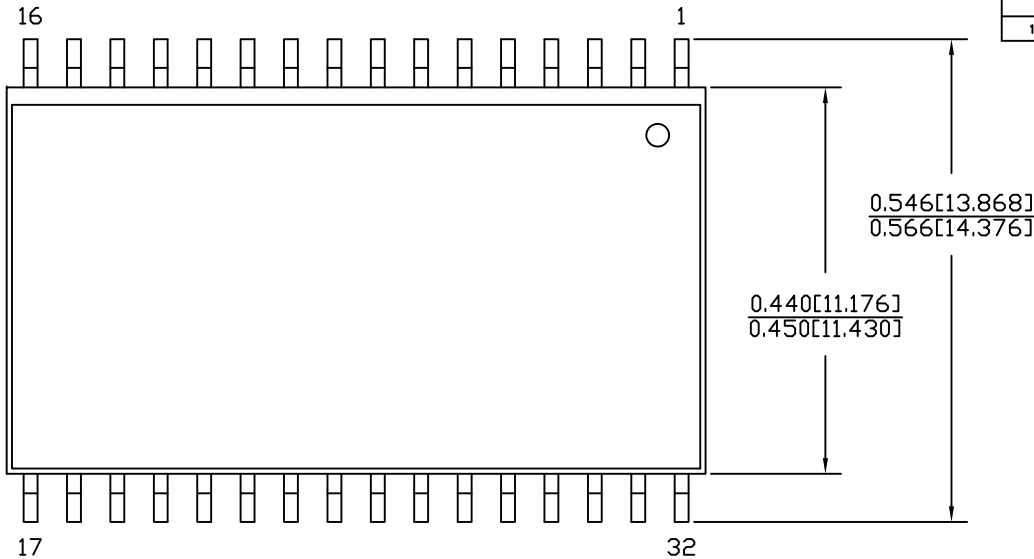
Continuity of document content

The fact that Infineon offers the following product as part of the Infineon product portfolio does not lead to any changes to this document. Future revisions will occur when appropriate, and any changes will be set out on the document history page.

Continuity of ordering part numbers

Infineon continues to support existing part numbers. Please continue to use the ordering part numbers listed in the datasheet for ordering.

32 LD (450 Mil) SOIC

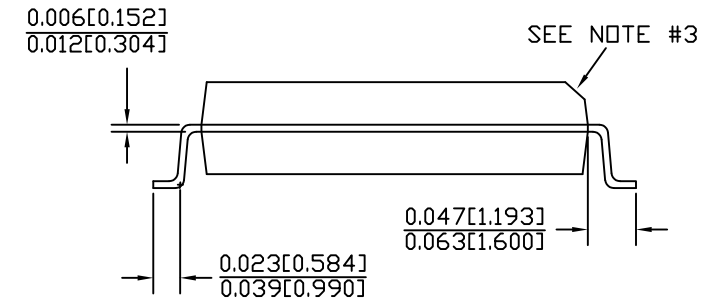
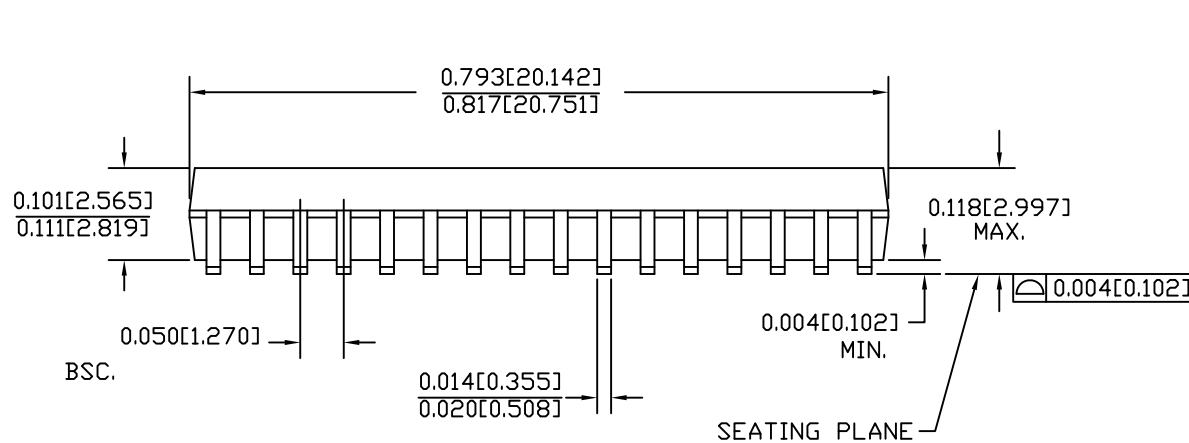


REVISIONS						
PAGE	ZONE	REV	ECN	DESCRIPTION	DATE	APPROVED
		**	1602	NEW RELEASE	05/15/96	
		*A	49484	CHG. TITLE/ ADD LEAD COPLANARITY	05/29/97	
		*B	128916	ADD MM DIM./ADD PKG. WEIGHT/ADD SZ PART #	08/07/03	
1	-			CHANGED TO STANDARD DRAWING TEMPLATE		
1	-	*C	2779866	CHANGED TITLE FROM 32LD (450 MIL) SOIC PKG OUTLINE TO PACKAGE OUTLINE 32L SOIC (450 MIL) S32.45/SZ32.45	10/07/09	TSV
1	-	*D	3357783	Update spec for sunset review, no change	08/30/11	QAD
1	-	*E	3731952	Changed package weight from "1.42gms" to "See Cypress Package Material Declaration Datasheet (PMDD) posted on the Cypress web."	09/03/12	QAD
1	-	*F	6585368	Add Note #3.	05/31/19	BITA


NOTE

1. DIMENSIONS IN INCHES[MM] MIN.
MAX.
2. PACKAGE WEIGHT: See Cypress Package Material Declaration Datasheet (PMDD) posted on the Cypress web.
3. The chamfer feature is optional. If it is not present, then Pin 1 identifier must be located within the index area indicated.

PART #	
S32.45	STANDARD PKG.
SZ32.45	LEAD FREE PKG.



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UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN MILLIMETERS STANDARD TOLERANCES ARE: DECIMALS .XX ± 0.05 .XXX ± .XXXX ± ANGLES ± 1°		DESIGNED BY	DATE	 CYPRESS Company Confidential
		DRAWN BY KOTA	DATE 05/31/19	
		CHECKED BY BITA	DATE 05/31/19	
		APPROVED BY RODP	DATE 05/31/19	
MATERIAL DEFAULT TEXT		APPROVED BY LTEO	DATE 05/31/19	TITLE PACKAGE OUTLINE 32L SOIC (450 MIL) S32.45/SZ32.45 SIZE A PART NO. SEE NOTE DWG NO. 51-85081 REV *F SCALED TO FIT
FINISH DEFAULT TEXT		APPROVED BY	DATE	
				SHEET 1 OF 1