

# **Next Generation Solenoid Driver**

# **Evaluation Kit User Manual**

### About this document

This document explains how the Evaluation Kit for the NextGen Solenoid Driver ICs TLE92464ED and TLE92466ED are taking into operation. The required hardware and software components for the evaluation of the Next Gen Solenoid Driver IC TLE92464ED/TLE92466ED are documentated. See the TLE92464ED/TLE92466ED datasheet for a detailed description of the device.

Note:

All references of the TLE92464ED Evaluation board within this document are also valid for TLE92466ED Evaluation board.

#### **Intended audience**

This document is intended for anyone who uses the Next Gen Solenoid Driver Evaluation Kit.

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# 1 Hardware

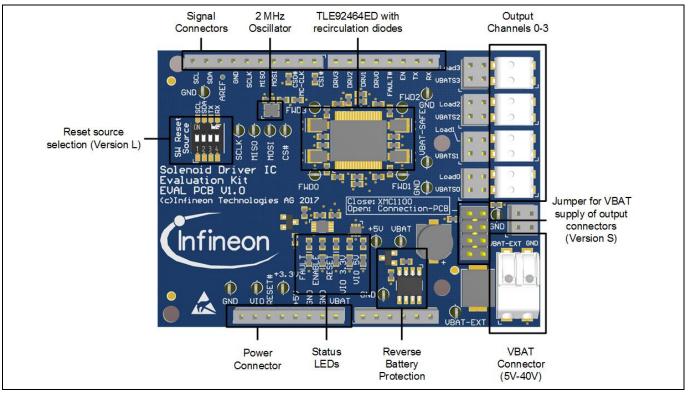
The Evaluation Kit is available in two versions. Version S includes an Eval PCB with an Arduino form-factor. It supports a quick and easy plug & play evaluation of the Next Gen Solenoid Driver. Version L includes the Eval PCB and an additional Connection PCB. The Version L is the more advanced setup option for detailed laboratory measurements.

# 1.1 Eval PCB

The Version S includes the Evaluation PCB with the Next Gen Solenoid Driver TLE92464ED and all required external components on it. The Evaluation PCB is desgined on an Arduino Formfactor which makes it controlled by all Arduino compatible microcontroller boards, e.g.:

- Infineon XMC1100 Boot Kit
- Infineon XMC4700 Relax Kit
- Aurix ShieldBuddy TC275
- Arduino Uno

Figure 1/Figure 2 give an overview of the component blocks on the Evaluation PCB for TLE92464ED/TLE92466ED. The IC itself is powered with 5V via the microcontroller board (version S) or the Connection PCB (version L), respectively. The load supply voltage VBAT which is available at the output channel connectors can be applied via the VBAT connector in version S (close JP1 – Pin Header, refer Jumper Options). In version L the VBAT level is supplied via the Connection PCB. The IC is clocked by an external rectangular 2 MHz oscillator. Another external clock source (e.g. microcontroller) can be selected via the ohmic Jumper (R12/R13). The SPI signals, the ENABLE signal and the RESETN signal must be provided by the microcontroller to enable a successful communication between the microcontroller and the TLE92464ED. The FAULTN and the DRVx signal are only necessary if the according features are used. The status LEDs indicate wheter the corresponding function is active (glowing) or not. The reset source DIP switch enables the selection of different pins for the RESETN signal. This option is only needed for the operation with multiply Evaluation PCBs.







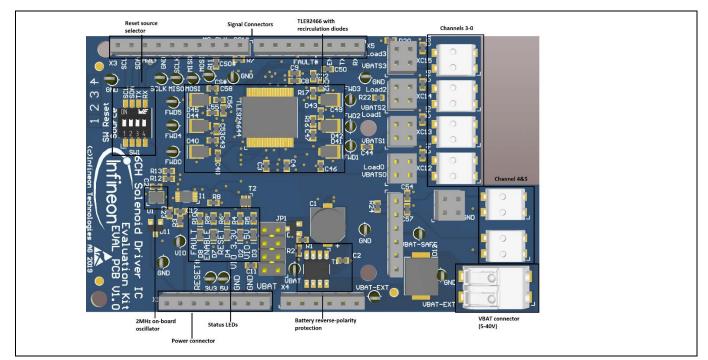


Figure 2 Functional component blocks on the Evaluation PCB – TLE92466ED



# 1.1.1 Connector Interface for Arduino

The Connector placement of the Evaluation PCB is compliant with the Arduino form-factor. The pinouts are listed below.

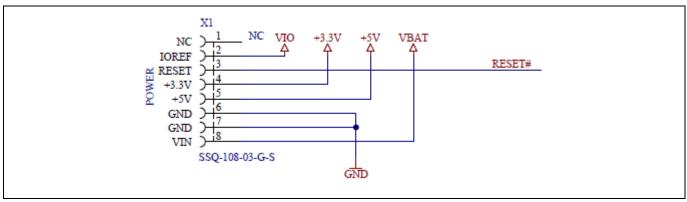




Table 1	Power Connector >	(1	
Pin no.	Arudino™ Signal Name	<b>Evaluation PCB Signal Name</b>	Description
1	-	-	-
2	IOREF	VIO	Voltage level for the SPI-MISO-Signal (3V3 or 5V)
3	RESET	RESET#	Reset signal (low active) for the TLE92464ED. All Reset signal source DIP-switches should be open.
4	3.3V	+3.3V	3V3 volt supply
5	5V	+5V	5V volt supply
6	GND	GND	Ground
7	GND	GND	Ground
8	VIN	VBAT	Voltage Level (usually 12V) applied on the VBAT-Connector to supply the Arduino board



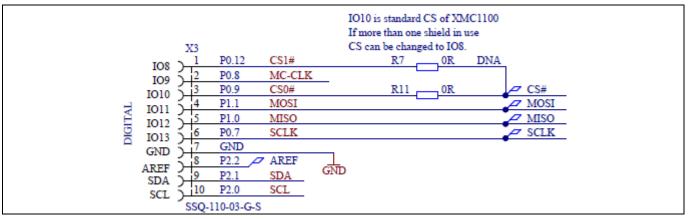




Table 2	Communication Interface Connector X3			
Pin no.	Arudino™ Signal Name	<b>Evaluation PCB Signal Name</b>	Description	
1	108	CS1#	SPI Chipselect signal for multiply Evaluation PCB use. Note: R7 must be assembled.	
2	109	MC-CLK	TLE92464ED Clock input signal from the microcontroller. Note: R13 must be assembled and R12 disassembled.	
3	IO10	CS0#	SPI Chipselect signal	
4	1011	MOSI	SPI Master Out Slave In	
5	1012	MISO	SPI Master In Slave Out	
6	1013	SCLK	SPI Clock (max. 8 MHz)	
7	GND	GND	Ground	
8	AREF	AREF	Analog reference Pin - not connected	
9	SDA	SDA	I2C SDA Pin – close DIP switch 2 to use it as reset signal source (digital IO)	
10	SCL	SCL	I2C SCL Pin – close DIP switch 1 to use it as reset signal source (digital IO)	

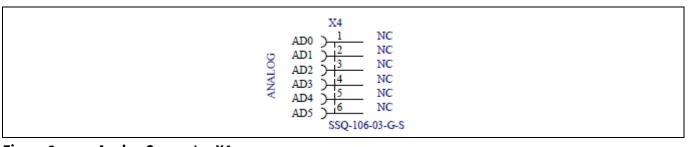
#### Table 2 Communication Interface Connector X3



|--|

### Figure 5 Digital IO Connector X5

Table 3	Digital IO Connector X5		
Pin no.	Arudino™ Signal Name	<b>Evaluation PCB Signal Name</b>	Description
1	100	RX	UART Receive – close DIP switch 4 to use it as reset signal source (digital IO)
2	101	ТХ	UART Transmit – close DIP switch 3 to use it as reset signal source (digital IO)
3	102	EN	TLE92464ED Enable Input Pin
4	103	FAULT#	TLE92464ED Fault Output Pin
5	104	DRV0	TLE92464ED DRV0 Pin for direct drive mode of Channel 0
6	105	DRV1	TLE92464ED DRV1 Pin for direct drive mode of Channel 1
7	106	DRV2	TLE92464ED DRV2 Pin for direct drive mode of Channel 2
8	107	DRV3	TLE92464ED DRV3 Pin for direct drive mode of Channel 3



#### Figure 6 Analog Connector X4

#### Table 4Analog Connecotr X4

Pin no.	Arudino™ Signal Name	<b>Evaluation PCB Signal Name</b>	Description
1	AD0	X4.1	Not connected
2	AD1	X4.2	Not connected
3	AD2	X4.3	Not connected
4	AD3	X4.4	Not connected



Pin no.	Arudino™ Signal Name	<b>Evaluation PCB Signal Name</b>	Description
5	AD4	X4.5	Not connected
6	AD5	X4.6	Not connected

#### **Jumper Options** 1.1.2

On the Evaluation PCB are various configuration options available to enable all features in both versions (S and L) and to test the chip in different conditions. The jumper options are descriped in the table below.

Jumper	Jumper Designation	Description	
IC Clock signal source	R12 (default)	External 2MHz Oscillator source	
	R13	Microcontroller Clock source via Connector Pin MC-CLK (e.g. PWM signal)	
Chip select signal source	R11 (default)	SPI chip select via Connector Pin CS0#	
	R7	SPI chip select via Connector Pin CS1#	
Reset signal source	DIP Switches open (default) RESET# Pin as Reset signal source		
	DIP Switch 1 closed	SCL Pin as Reset signal source	
	DIP Switch 2 closed	SDA Pin as Reset signal source	
	DIP Switch 3 closed	TX Pin as Reset signal source	
	DIP Switch 4 closed	RX Pin as Reset signal source	
VBAT supply	Pin header JP1 closed (default)	Supply Output Connector with VBAT voltage level via VBAT-EXT Connector	
	Pin header JP1 open	Evaluation PCB Output Connectors are supplied via the Connection PCB	
		Note: Open for use with Connection PCB	

#### **Jumper Options** Table 5

#### 1.1.3 LEDs

The Eval PCB has five LEDs which indicate the status and operating conditions of the IC. The meaning of the status LEDs is descriped below.

Table 6 LED d	escription	
LED designation	TLE92464ED Pin designation	Description
FAULT	FAULTN (low active)	Glows if the IC detected a fault condition
ENABLE	EN (high active)	Glows if the IC is enabled
RESET	RESN (low active)	Glows if the IC is in reset state
VIO 3.3V	VIO	Glows if the VIO Pin is connected to 3.3V (voltage level for SPI MISO Pin)
VIO 5V	VIO	Glows if the VIO Pin is connected to 5V (voltage level for SPI MISO Pin)

#### LED description Table C

#### 1.2 **Connection PCB**

The version L includes the Evaluation PCB, the Connection PCB and an Infineon Aurix TriBoard TC277. The Connection PCB supports the evaluation of up to three Evaluation PCBs in parallel. Furthermore it provides advanced measurement options and one high side switch for each Evaluation PCB. The control unit for version



L is intended to be an Infineon Aurix TriBoard TC277 which can be plugged on the Connection PCB. Figure 1 gives an overview of the Connection PCB.

Note:

For use of the Connection PCB please open the Pin header Jumper on the Evaluation PCB (see 1.1.2)

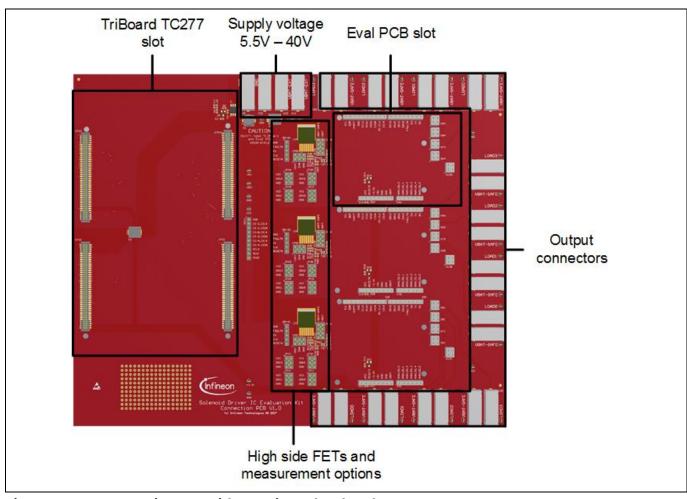


Figure 7 Connection PCB without TriBoard and Eval PCB

# 1.2.1 Connector Interface for TriBoard TC277

All three Evaluation PCB slots are connected to the TriBoard TC277 microcontroller board to enable the feature set of the Evaluation PCB. One SPI interface with three different Chip Select signals is used for the communication between the microcontroller and the Evaluation PCBs. Each digital IO signal (Enable, Reset, Fault, high side FET on/off, direct drive pins) is connected to an individual digital IO pin.

*Note:* For detailed pin assignment documentation see the Connection PCB schematic.

An individual Evaluation PCB reset can be triggered by software. The corresponding digital IO pin must be set to low. A reset signal for all three Evaluation PCB can be trigger via the PORST# signal of the TriBoard either by software or the corresponding button. Unused common signals (e.g. UART, I2C) are kept available for any extension.



# **1.2.2** Jumper Options

Table 7 Jumper Op	otions	
Jumper	Jumper Designation	Description
High side FET	JP6x	Close for use without high side FET
High side FET controlling	JP5x 3-4 "EN#" (default)	High side FET is controlled by the microcontroller via two digital IO-Pins (active on low level)
	JP5x 1-2 "VBAT"	Deactivates the high side FET (continuous off)
	JP5x 5-6 "GND"	Activates the high side FET (continuous on)
Direct Drive Pins	JP1x, JP2x, JP3x, JP4x 3-4	Direct drive Pins are controlled by the microcontroller
	"DRVx" (default)	
	JP1x, JP2x, JP3x, JP4x 1-2 "VIO"	Set direct drive pins to VIO level (5V default).
	JP1x, JP2x, JP3x, JP4x 5-6	Set direct drive pins to GND level.
	"GND"	

The Connection PCB can be configured via some Jumpers which are described below.

### 1.3 Version S setup

In Figure 8 a basic setup for the Version S of the Evaluation Kit is shown. On the external supply connector the voltage for V<sub>bat</sub> in the range of 5.5V up to 40V can be provided. The output channels are supplied with V<sub>bat</sub> by closing the Jumper JP1 (refer Table 5). If the XMC1100 microcontroller is flashed with the Commando Interpreter software the TLE92464ED can be controlled via the USB connection by the PC. The USB connector can also be used to falsh and debug own embedded software. It is recommended to use the DAVE IDE for programming XMC microcontrollers (<u>https://www.infineon.com/cms/de/product/microcontroller/32-bit-industrial-microcontroller-based-on-arm-registered-cortex-registered-m/dave-version-4-free-development-platform-for-code-generation/channel.html?channel=db3a30433580b37101359f8ee6963814).</u>



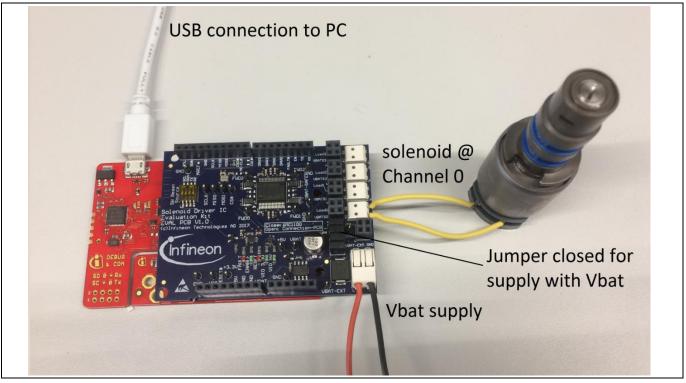


Figure 8 Setup example for Version S (XMC1100 Boot Kit with TLE92464ED Evaluation board)

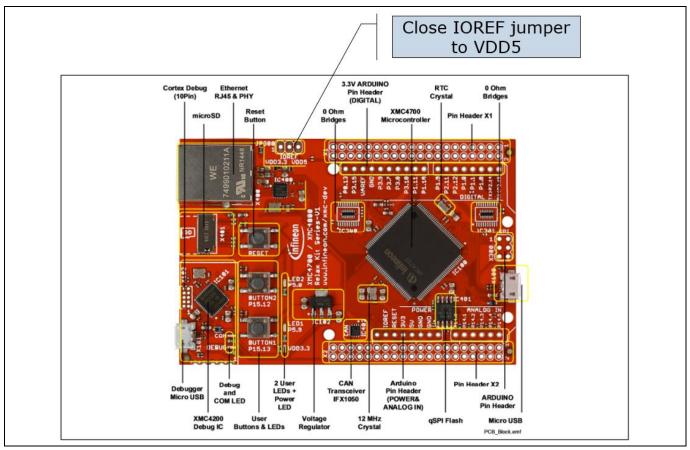


Figure 9

XMC4700 considerations – Evalboard setup is identical to XMC110, but the IORER jumper on the XMC board needs to be closed



### 1.4 Version L setup

In Figure 10 a Version L setup with one Eval PCB and a TC277 microcotroller TriBoard is shown. The main voltage input is connected to the upper middle connectors and supplies the whole board setup inclusive loads. Take care on maximum current ratings. In case of using Eval PCBs in parallel supply the board via all four supply connectors (two red, two black). Each Eval PCB slot has its own SPI chip select signal to support an individual communication. The slots are named alphabetic. In Figure 10 a solenoid is connected to Channel 0 at slot A of the connection PCB. If the TC277 microcontroller is flashed with the Commando Interpreter software the TLE92464ED can be controlled via the USB connection by the PC. The USB connector can also be used to falsh and debug own embedded software. It is recommended to use the TriCore Entry Toolchain (https://www.infineon.com/cms/de/product/microcontroller/32-bit-tricore-tm-microcontroller/tricore-tm-development-tools-software-and-kits/free-tricore-entry-tool-chain/channel.html?channel=db3a304344134c7a014420d628fa76ec).

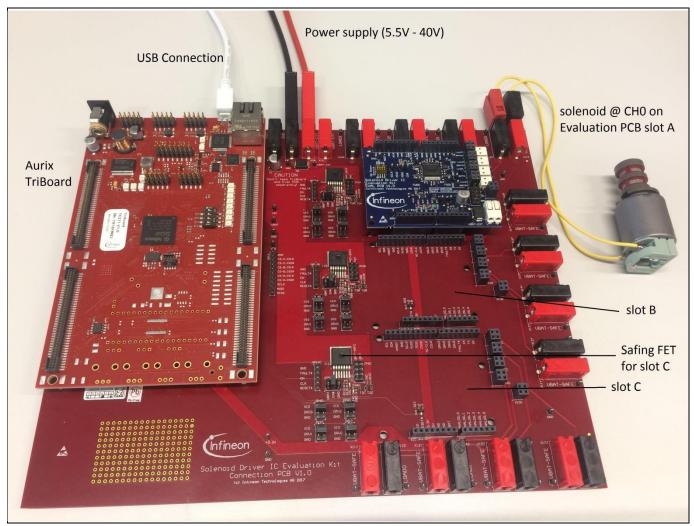


Figure 10 Setup example for Version L with a TC277 TriBoard

#### 1.5 Schematics

This chapter contains the schematics of the Evaluation PCBs and the Connection PCB.

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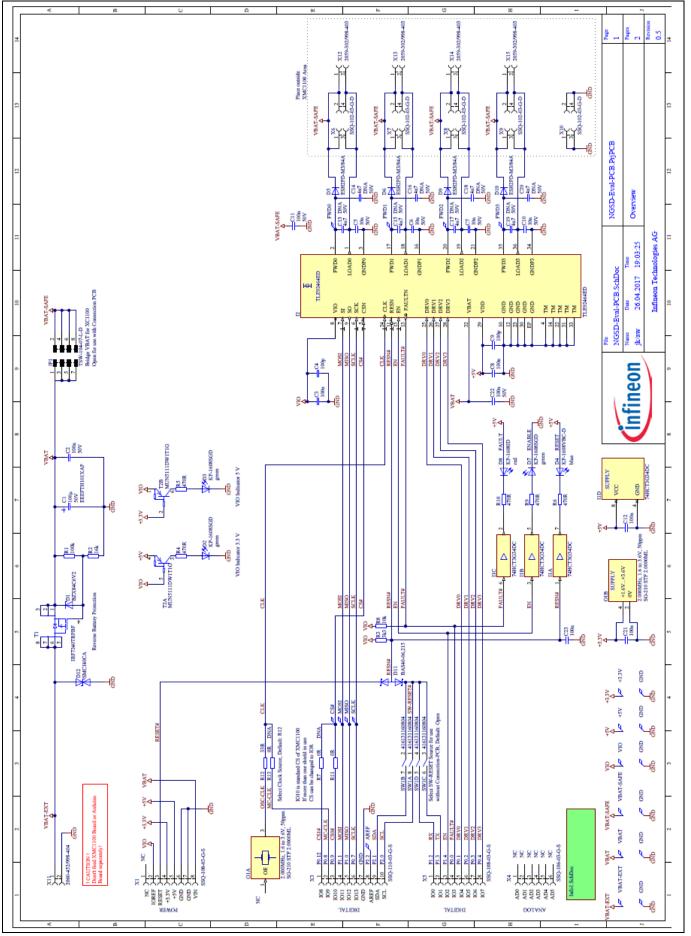


Figure 11

Schematic of the TLE92464ED Evaluation PCB



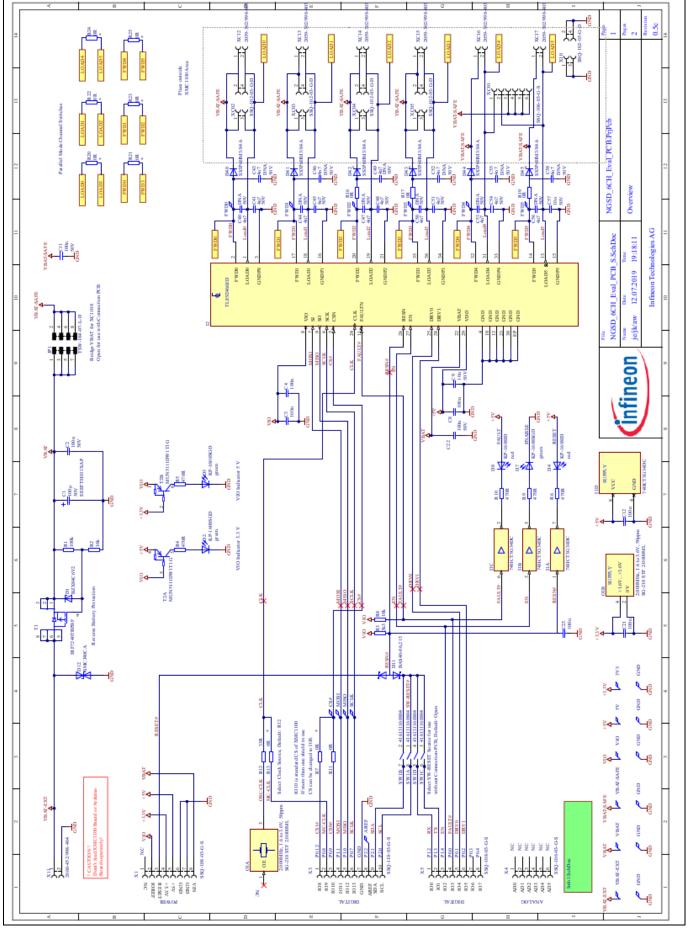


Figure 12 Schematic of the TLE92466ED Evaluation PCB

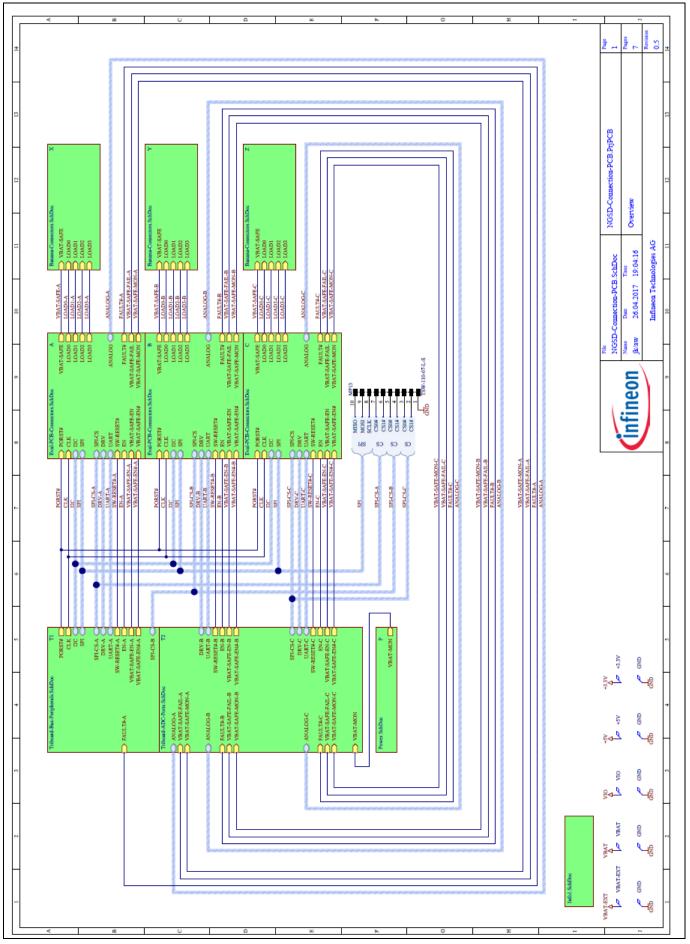


Figure 13 Schematic of the Connection PCB - Overview



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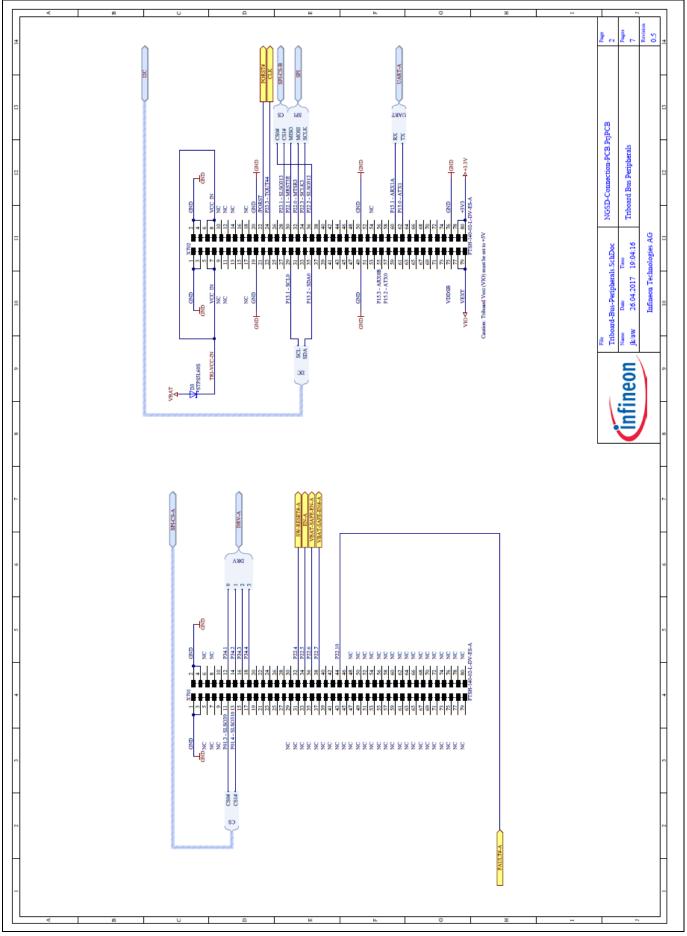
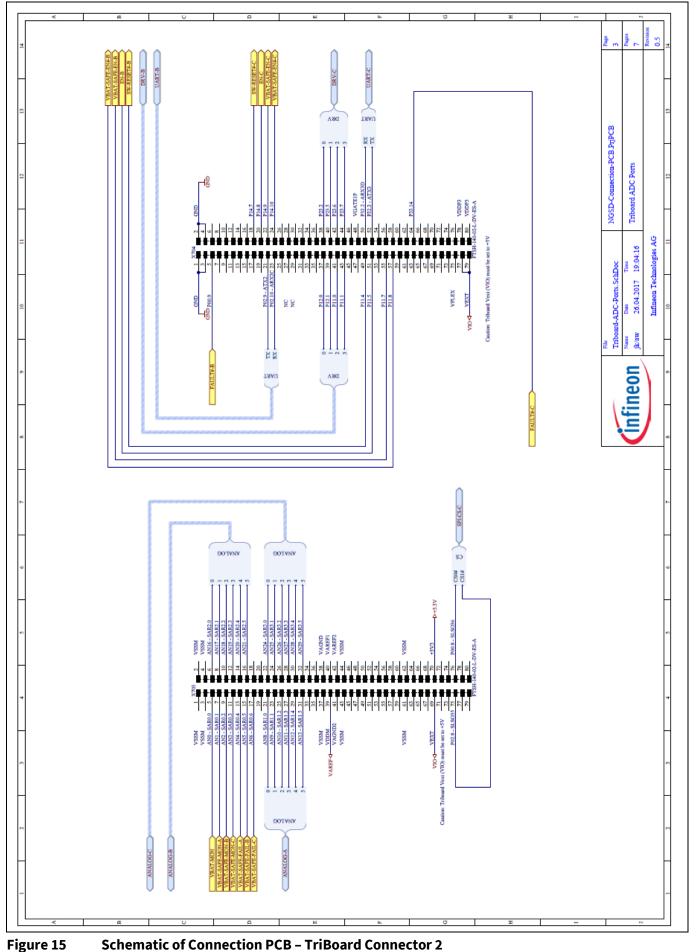
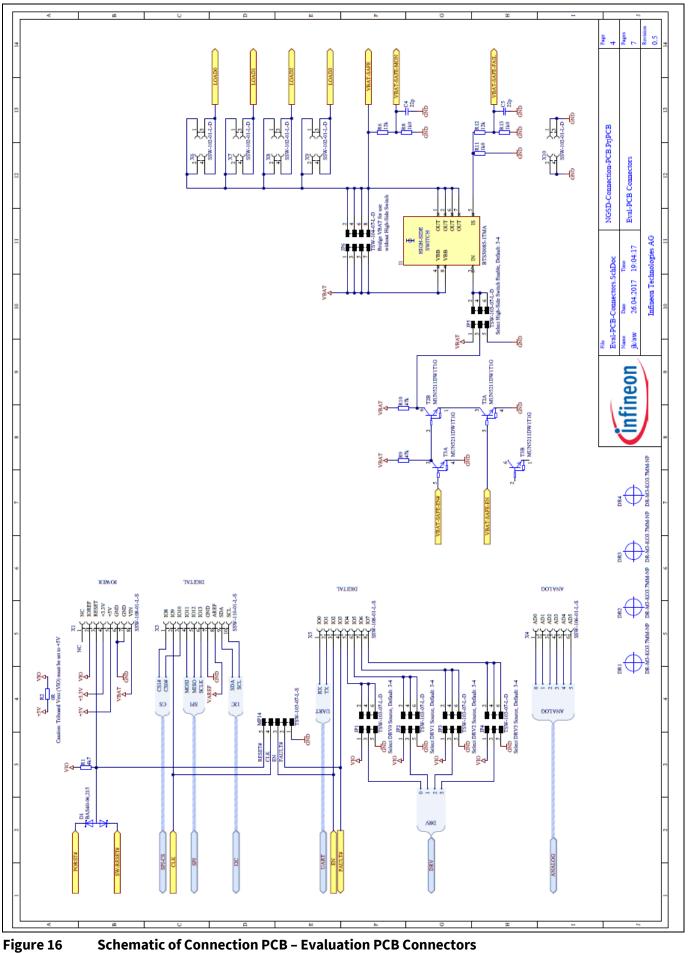


Figure 14 Schematic of Connection PCB – TriBoard Connector 1









**Schematic of Connection PCB – Evaluation PCB Connectors** 



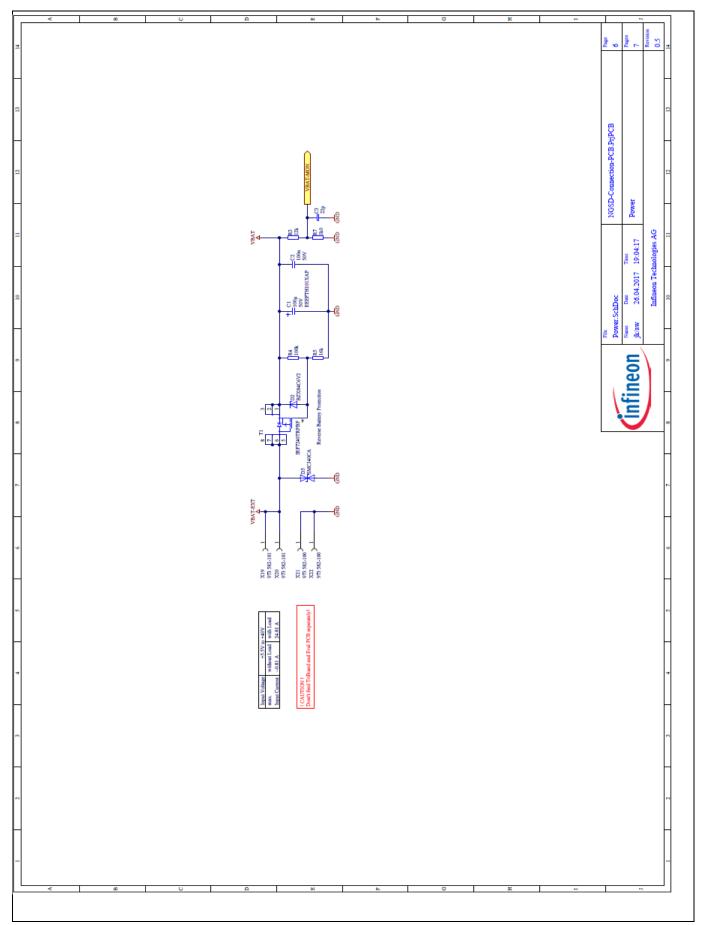


Figure 17Schematic Connection PCB - Power supply

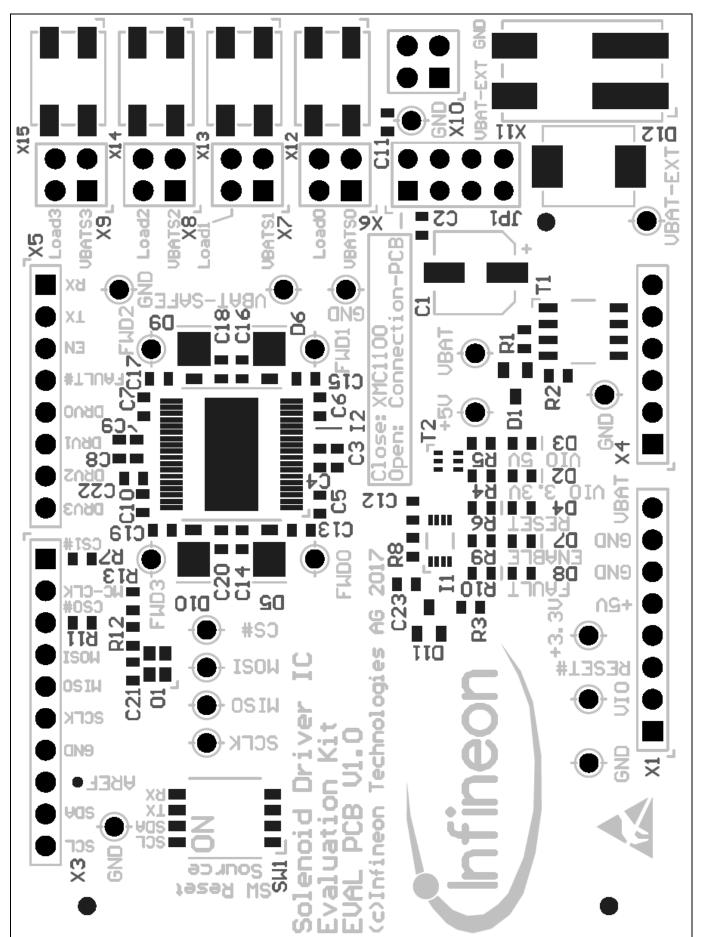
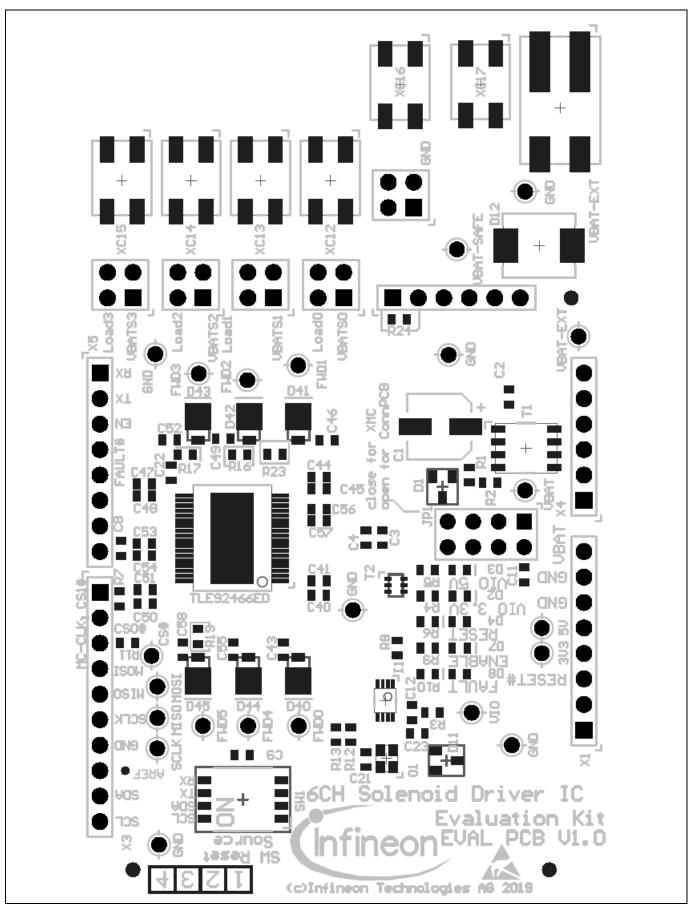


Figure 18

Layout Evaluation-PCB – TLE92464ED



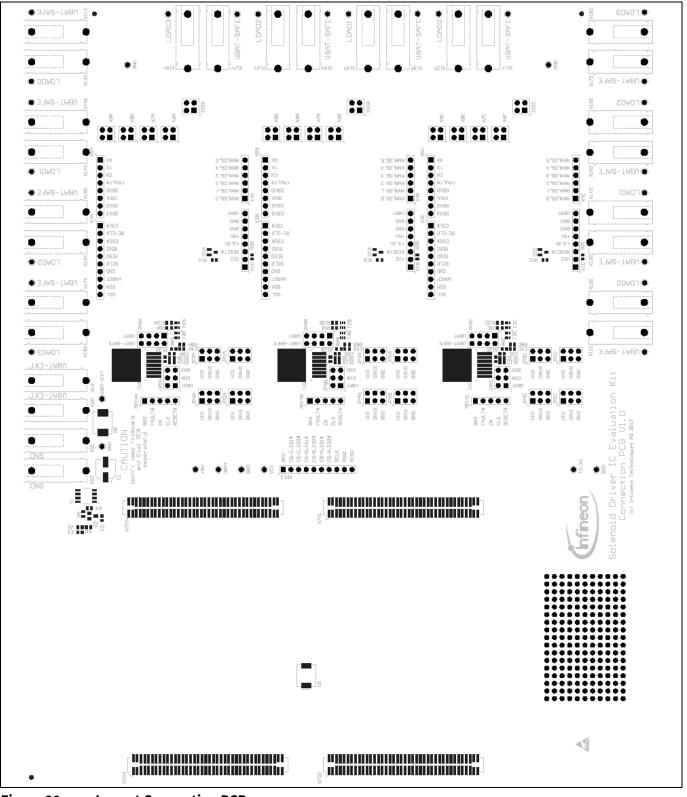


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Figure 19 Layout Evaluation-PCB – TLE92466ED











# 2 Software

Before using the GUI, the seleceted microcontroller must be flashed with the communication firmware. The sections Flashing/Updating Aurix TC277 Software and Flashing/Updating XMC Software explaining the steps to be taken for a successful flashing of the microcontroller GUI interface software.

# 2.1 Flashing/Updating Aurix TC277 Software

Following steps should be taken to flash the Aurix TC277 Triboard and enable a communication with the GUI:

- Plug the TriBoard and the Evaluation PCB on the Connection PCB
- Install the Infineon DAS-Tool inclustive drivers on your computer (<u>https://www.infineon.com/cms/en/product/promopages/das</u>)
- Install the Infineon Memtool SW: <u>https://www.infineon.com/cms/en/product/microcontroller/32-bit-tricore-microcontroller/#!tools</u> (NOTE: DAS-tool should already be installed)
- Supply and connect the Evaluation board
- Select the Target TriBoard (Target -> Change -> new)
- Select the appropriate Triboard (Tricore Aurix -> Application Kits (DAS) -> Application Kit with TC277T D-Step (DAS))
- Click the Connect button in Memtool and load the provided HEX file via the button "Open file"
- Click "Select All", Click "Add Sel.", Click "Program all"
- A small window should pop up and program the Aurix. The "result" should show "success"

X Infineon - Memtool 4.7 on Application Kit with TC27	7T D-Step (DAS)	- 🗆 X
File Target Device Log Help		
File :	FLASH/OTP - Memory Device PFLASH0: 2 MByte OnChip Program FLASH	▼ ▼ Enable
0x80000000 · 0x80000027	Index Start End Size 🔺	Erase
0x80000040 - 0x800000DF 0x80000100 - 0x8001AFCB 0x8001AFD0 - 0x8001AFDF Select All	0 0x4000000 0x40003FFF 16K 1 0x40004000 0x40007FFF 16K 2 0x40008000 0x40008FFF 16K	Program Program all
0x80020000 - 0x8002001F 0x801F4020 - 0x801F402D 0x801F4040 - 0x801F404D Add Sel.>>	3 0x4000C000 0x4000FFFF 16K 4 0x40010000 0x40013FFF 16K	Verify Verify all
0x801F4060 - 0x801F406D Save As 0x801F4080 - 0x801F408D 0x801F40A0 - 0x801F40AD Read	5 0xA0014000 0xA0017FFF 16K 6 0xA0018000 0xA001BFFF 16K 7 0xA001C000 0xA001FFFF 16K	SW Protect
0x801F40C0 - 0x801F40CD 0x801F4660 - 0x801F466D 0x801F4680 - 0x801F468D Edit	8 0xA0020000 0xA0027FFF 32K 9 0xA0028000 0xA002FFFF 32K	Info
0x801F46A0 · 0x801F46AD 0x801F4780 · 0x801F478D	10 0xA0030000 0xA0037FFF 32K *	State
0x801F6000 - 0x801F60FF 0x801F6200 - 0x801F7B07	Remove All Remove Sel.	Setup
<b>Cinfineon</b> Tool Disconnect	Ready for Memtool Command	Help Exit

Figure 21 Successful flashing setup of Memtool

### 2.2 Flashing/Updating XMC Software

The Evaluation board can be fully controlled via the GUI by following Infineon XMC board

- XMC1100 Boot Kit (SP001069652)
- XMC4700 Relax Kit (SP001427974) recommended



A full GUI support for the Version S setup presupposes the according microcontroller firmware for correct interaction. The according Firmware for the XMC1100 and XMC4700 is provided as .hex file in the GUI folder under "XMCFirmwareFiles". The flashing of the XMC microcontroller is explained by means of XMC1100 (please refer XMC4700 RelaxKit "Getting started"-guide).

Flashing XMC4700 Microcontroller is done via the J-Link Flash lite which can be obtained from the segger website (<u>https://www.segger.com/products/debug-probes/j-link/technology/flash-download/</u>).

Upon opening JLink Flash lite, this screen pops up:

🔝 SEGGER J-Flash Lite V6.44b			
Device XMC4700-2048	Interfa	<ul> <li>✓ 4000 kHz ▼</li> </ul>	ОК

Figure 22 Segger J-Flash Lite start up – select microcontroller

When executing J-flash lite, select the XMC 4700 microcontroller as shown in Figure 22. By clicking ok, the flasher tool pops up where the downloaded firmware needs to be selected in the section "Data File". If the Relax Kit is connected via USB, the firmware can be flashed onto the microcontroller and the J-falsher should acknowledge the successful flash operation as shown in Figure 23.

SEGGER J-Flash Lite V6.44b	
File Help	
Target       Device       Interface       Speed         XMC4700-2048       SWD       4000 kHz         Data File (bin / hex / mot / srec /)       Prog. addr. (bin file only)         irmwareFiles/xmc4700.hex        0x00000000	
Program Device	
Log	
Selected file: C:/Users/kaffigeo/GIT_EVAL_GUI/XMCFirmwareFiles/xmc4700.hex Conecting to J-Link Connecting to target Downloading Done	
Ready	

Figure 23 Successful flashing of the XMC4700 microcontroller

#### 2.3 XMC for Arduino IDE

Own software projects can easily be written and updated to the XMC microcontroller boards.



- Install Arudino IDE
- Install Infineon's XMC Microcontroller Boards for Ardunio: <u>https://github.com/Infineon/XMC-for-Arduino</u>
- Create your own SW Project
- Compile the project (Sketch -> Verify/Compile)
- Connect the XMC1100 Boot kit/XMC4700 Relax Kit via USB and upload the compiled project (Sketch -> Upload)

### 2.4 Graphical User Interface (GUI)

The GUI can be use either with an XMC or TriBoard (incl. Connection PCB) setup. Both setups require a microcontroller software for the communication with the GUI. The steps to flash the used microcontroller with the firmware is explained in the previous sections.

Following steps should be taken to start before starting the GUI (see also "getting started"):

- XMC: Plug a 12V supply into VBAT\_EXT and GND and connect the XMC board via the USB connector to the computer (see Figure 8)
- TriBoard:
  - Please install the DAS tool (see 2.1)
  - Plug a 12V supply into the VBAT\_EXT and GND Connectors on the Connection PCB and connect the TriBoard via the USB connector with your computer (see Figure 10)
- Start the GUI and connect to the right COM Port
- When setting the EN Checkbox the EN-LED on the Eval PCB should glow and the communication to the Solenoid Driver IC is fully operational.

#### 2.4.1 Main Window

After starting the GUI a dialog window will pop up (Figure 24). From the first drop-down menu, select the connection PCB or the microcontroller board (XMC1100 or XMC4700) which is being used to interface with the evaluation board. Based on the first selection of either a common connection PCB or XMC microcontroller board, it is possible to test either three or one evaluation board respectively. If the common connection PCB was first chosen, the three IC evaluation boards being used should be selected for each slot from the three subsequent drop-down menus. Alternatively, if a XMC is the tool of choice, then the user can only an evaluation kit for Slot A in the dialog window shown in Figure 24.

Dialog	X
Connection and Slot Selection	
Select Connection-PCB/ Microcontroller Board	
Please Select Connection-PCB	
Slot A (Top Right)	
Slot B (Middle Right)	
Slot C (Bottom Right)	
OK Cancel	

Figure 24 Slot selection for the Connection PCB



After selecting the connection-PCB or microcontroller board from the first drop-down menu, a prompt pops up for selecting the COM port for the USB- Serial interface. Locate the correct port in the 'Ports' section of the Device Manager on your PC's Control Panel.

COM10: (Infineon DAS JDS COM (COM10))  OK Cancel	I Dialog	8 8
OK Cancel	COM10: (Infineon DAS JDS COM (COM10	))) 🔻
		OK Cancel

#### Figure 25 COM port selection

đ Steel Steel 0. 0.004 G 31me View Button 1 10 a 12 1.00 (a) 10.17 () WERN . A 14.307.2 Contra Contra Connect Button Macro-loading tab 🖌 HELJEC DR COL PAR AD Initialized evaluation board tabs Dial INC. (a) a1 0.00 .... 0 on test line Very Direct Com

Once the correct COM port is identified, the GUI starts up in its default state, see Figure 26.

Figure 26Main window overview

The Connect button in the top left corner opens the slot-selection menu. Next to it is the View menu which offers different layout options for the main window, such as hiding certain widgets or tabs, as well as saving the layout preferences. The bottom left corner has three buttons for switching across the three tabs corresponding to the three evaluation boards.

One useful feature of this GUI is its ability to load and run a user-written macro sequence, as an alternative to utilizing the evaluation board tabs. The user can write a command sequence by activating the Macro Recorder indicated by as Macro loading tab in Figure 26.

#### 2.4.2 Evaluation Board Tabs

If selected, each EvalBoard gets their own Tab in the MainWindow, which contains all available controls for that particular board. The Tab for the TLE9246x family of devices is structured in 5 main widgets as can be seen in Figure 27.



- Global control
- Channel widgets
- Diagnostics
- SPI register widget

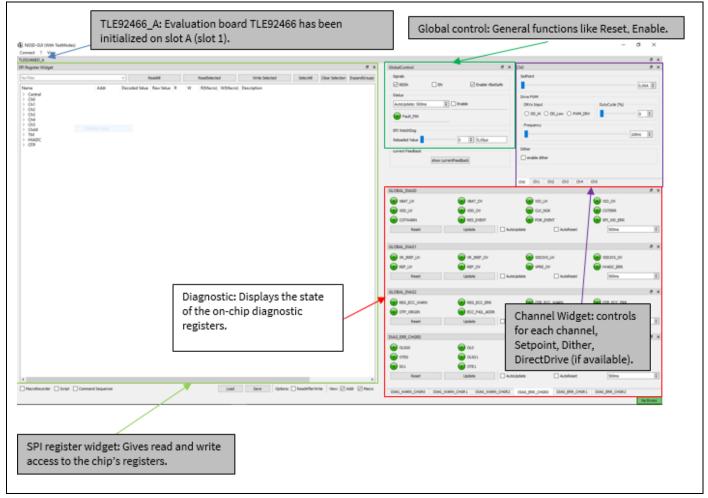


Figure 27 Example TLE92466 GUI Tab



To configure the evaluation boards with the GUI, the following sequence is recommended

- Reset the Chip by unchecking and the re-checking RESN via GlobalControl
- Set appropriate channel settings in the CHx registers.
- Activate needed channels in Central -> CH\_CTRL register
- Activate MissionMode in the Central -> CH\_CTRL register
- Enable the Chip by setting EN via Global Control

Additionally, the watchdog value can be modified using the slider on the global control widget. The watchdog is, however, triggered properly at startup and does not need to be configured.

The Diagnostics widget displays the state of the chip's diagnostic registers, by default, these values don't update automatically, for this you need to check the AutoUpdate checkbox in GlobalControl

### 2.5 Information on the GUI communication interface

The GUI software sends commands via UART to the TriBoard based on the User input. The TriBoard microcontroller firmware decodes the commands and excutes them accordingly. The following list shows available commands that can be used to directly execute commands with the help of a terminal program.

- All values have to be typed in as hexadecimal numbers. If the first digit is alphabetic, a zero has to be typed in front of the value.
- The Connection-PCB must be initialized on startup. The type of Connection-PCB is displayed on startup or when entering the *help()* command.
- For most commands a slot number (slot) has to be entered. Slot 0 selects the Connection PCB. The Eval-PCBs are enumerated according to the slot naming shown in figure 11 (Slot A: (1) to Slot C: (3). The XMC1100 has a single slot (1) only.
- Not all Eval-PCBs can be mounted on every slot. The user can initialize Eval-PCBs by the initialization commands and the slot number. The type of initialized Eval-PCBs is displayed when entering the *help()* command.
- CRCs, Parity Bits or similar have to be calculatet by the user.
- The UART interface should be configured as shown in following figure.

Tera Term: Serial po	ort setup	<b>X</b>
Port:	сомз ~	ОК
<u>B</u> aud rate:	115200 ~	
Data:	8 bit $\sim$	Cancel
P <u>a</u> rity:	none ~	
<u>S</u> top:	1 bit $\sim$	<u>H</u> elp
Elow control	ol: none ~	
Transmit	-	sec/line



### 2.5.1 List of Commands

InitEvalNGSD(slot) – NGSD-Connection-PCB (slot 1 or 2), Microcontroller board slot 1.



• help() – help

Displays board info, names and command list. Returns 0 always.

• delay(ticks) - delay

Waits for ticks time in milliseconds. Returns ticks. Expected values:

- ticks: [0x1] to [FFFFFFF]
- ior(slot, ioDev) io read

Returns state of input or output ioDev, if successful. Expected values:

- slot: [0] to [3], depending on the initialized boards
- ioDev: Names displayed in help command or XMC1100 Bootkit IO numbers depending on the initialized boards
- iow(slot, ioDev, outState) io write

Writes outState to output ioDev.

Returns outState, if successful.

Expected values:

- slot: [0] to [3], depending on the initialized boards
- ioDev: Names displayed in help command or XMC1100 Bootkit IO numbers depending on the initialized boards
- outState: [0] or [1]
- iop(slot, ioDev, outState, ticks) io pulse

Sets ioDev to outState for the time of ticks ms, then sets ioDev to inverse outstate. Returns ioDev, if successful.

Expected values:

- Slot: [0] to [3], depending on the initialized boards
- IoDev: Names displayed in help command or XMC1100 Bootkit IO numbers depending on the initialized boards
- OutState: [0] or [1]
- Ticks: [1] to [FFFFFFF]
- iowt(slot, ioDev, inState) io wait

Waits until ioDev has the value of instate. Returns inState, if successful. Expected values:

- slot: [0] to [3], depending on the initialized boards
- ioDev: Names displayed in help command or XMC1100 Bootkit IO numbers depending on the initialized boards
- inState: [0] or [1]
- spi(slot, spiDev, out) spi write

Sends an spi command with content out to device spiDev (normaly 0). Returns the return value of the previous command, if successful (previous command can be the watchdog command, if activated).



Expected values:

- slot: [0] to [3], depending on the initialized boards, msc is only available for slot [3] on Engine-Management-Connection-PCB yet
- spiDev: [0]
- out: [0] to [FFFF] or [0] to [FFFFFFF], depending on the datawith of the particular Eval-PCB
- spis(slot, spiDev, out1, out2, ticks) spi sequence

Sends two spi commands with a delay of ticks ms to device spiDev (normaly 0). Returns the return value of the command before the second command, if successful (watchdog command can be sent between both commands, if activated). Expected values:

- slot: [0] to [3], depending on the initialized boards, msc is only available for slot [3] on Engine-Management-Connection-PCB yet
- spiDev: [0]
- outx: [0] to [FFFF] or [0] to [FFFFFFF], depending on the datawith of the particular Eval-PCB
- ticks: [1] to [FFFFFFF]
- spir(slot, spiDev, out, nop) spi read

Sends two spi commands in quick succession to device spiDev (normaly 0).

In contrast to other spi functions not interrupted by watchdog.

Returns the return value of first command (which is received while sending the second command). Second command can either be a "no operation" value or another command whose answer is not needed.

Expected values:

- slot: [0] to [3], depending on the initialized boards, msc is only available for slot [3] on Engine-Management-Connection-PCB yet
- spiDev: [0]
- out: [0] to [FFFF] or [0] to [FFFFFFF], depending on the datawith of the particular Eval-PCB
- nop: [0] to [FFFF] or [0] to [FFFFFFF], depending on the datawith of the particular Eval-PCB



# **Revision history**

#### Major changes since the last revision

Page or Reference	Description of change
V1.0	Initial release
V1.1	Added TLE92466ED layout

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