

# ModusToolbox™ Programming tools release notes

ModusToolbox™ Programming tools package version 1.4.0

[A newer version of this document may be available on the web here.](#)

## About this document

### Scope and purpose

This document describes the features and known limitations for the ModusToolbox™ Programming tools package. This package includes several cross-platform programming tools that allow you to program flash on Infineon MCU and Kit devices. These are used within the ModusToolbox™ ecosystem, and you may also use them separately, if needed.

For more details about what is included with the ModusToolbox™ Programming tools package, see the [What's included](#) section in this document.

### Reference documents

Refer to the Infineon [programming solutions website](#) for more information as needed.

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## Primary changes

### 1 Primary changes

This programming tools package release includes the following updates:

#### 1.1 Version 1.4.0

This is a new version of the programming tools package. It includes:

##### New part numbers in OpenOCD and ModusToolbox™ Programmer

Added support for PSoC™ 4100T Plus devices and new part numbers in the CCG7S, CYW20829, PSoC 4000T, and PSoC 62 devices.

##### In KitProg3 enabled DapLink mode

Updated KitProg3 with support for DAPLink mode on PSoC™ 4100T Plus CAPSENSE™ Prototyping Kit and CY8CPROTO-040T-MS PSoC™ 4000T Multi-Sense Prototyping Kit.

##### ModusToolbox™ Programmer UI update

Updated icons and font in UI of the ModusToolbox™ Programmer.

#### 1.2 Version 1.3.1

##### New part numbers in OpenOCD and ModusToolbox™ Programmer

Added support for new part numbers in the CCG7S device family.

Updated OpenOCD to address known issue about the PSoC™ Control C3 device being permanently disabled if Flash row 0 contains uncorrectable ECC errors.

#### 1.3 Version 1.3.0

This is a new version of the programming tools package. It includes:

##### New device series-dependent approach in OpenOCD target configurations

This approach was implemented to enhance the experience when choosing the correct config file for the device.

*Note: Since old config files will be removed in future releases of ModusToolbox™ Programming tools, consider using new config files.*

The following table shows the old and corresponding new target config files:

Old Target Config	New Target Config	Description
<i>psoc6.cfg</i>	<i>infineon/cy8c6xxx.cfg</i>	CY8C6xx7, CY8C6xx6 target configuration
<i>psoc6_2m.cfg</i>	<i>infineon/cy8c6xxa.cfg</i>	CY8C6xxA, CY8C6xx8 target configuration
<i>psoc6_512k.cfg</i>	<i>infineon/cy8c6xx5.cfg</i>	CY8C6xx5 target configuration
<i>psoc6_256k.cfg</i>	<i>infineon/cy8c6xx4.cfg</i>	CY8C6xx4 target configuration
<i>psoc6_2m_secure.cfg</i>	<i>infineon/cy8c6xx4a.cfg</i>	CYS0644A, CYB0644A target configuration

## Primary changes

Old Target Config	New Target Config	Description
<i>psoc6_secure.cfg</i>		
<i>psoc6_512k_secure.cfg</i>	<i>infineon/cyxx64x5.cfg</i>	CYB06445 target configuration
<i>psoc4.cfg</i>	<i>infineon/psoc4.cfg</i>	Configuration for all PSOC™ 4 MCU, CCGx targets except PSOC™ 4500H MCU
<i>psoc4500.cfg</i>	<i>infineon/psoc4500h.cfg</i>	Configuration file for PSOC™ 4500H MCU
<i>pag2s.cfg</i>	<i>infineon/pag2s.cfg</i>	Configuration file for PAG2S MCU
<i>cyw208xx.cfg</i>	<i>infineon/cyw20829.cfg</i>	Configuration file for the AIROC™ CYW208xx Wi-Fi & Bluetooth® combo chips
<i>cat1c.cfg</i>	<i>infineon/xmc7xxx.cfg</i>	Configuration file for XMC7100/XMC7200 series of devices
<i>cat1c.cfg</i>	<i>infineon/cytxbb.cfg</i>	Configuration file for CYT3BB/CYT4BB series of TRAVEO™ T2G Body High MCU devices
<i>cat1c.cfg</i>	<i>infineon/cyt4bf.cfg</i>	Configuration file for CYT4BF series of TRAVEO™ T2G Body High MCU devices
<i>cat1c.cfg</i>	<i>infineon/cyt4dn.cfg</i>	Configuration file for CYT4DN series of TRAVEO™ T2G Cluster 6M MCU devices
<i>traveo2_1m_a0.cfg</i>	<i>infineon/cyt2b7.cfg</i>	Configuration file for CYT2B7 series of TRAVEO™ T2G Body Entry MCU devices
<i>traveo2_2m.cfg</i>	<i>infineon/cyt2b9.cfg</i>	Configuration file for CYT2B9 series of TRAVEO™ T2G Body Entry MCU devices
<i>traveo2_be_4m.cfg</i>	<i>infineon/cyt2bl.cfg</i>	Configuration file for CYT2BL series of TRAVEO™ T2G Body Entry MCU devices
<i>traveo2_512k_a0.cfg</i>	<i>infineon/cyt2b6.cfg</i>	Configuration file for CYT2B6 series of TRAVEO™ T2G Body Entry MCU devices
<i>psoc4hv_a0.cfg</i>	<i>infineon/psoc4hv_a0.cfg</i>	Configuration file for PSOC4-HV devices
<i>fx3g2.cfg</i>	<i>infineon/fx3gx.cfg</i>	Configuration file for FX3G2 and FX2G3 series of devices

## New part numbers in OpenOCD and ModusToolbox™ Programmer

Added support for PSOC™ Control C3 devices and part numbers of XMC5100/XMC5200/XMC5300 series MCU devices.

### 1.4 Version 1.2.1

This is a new version of programming tools package. It includes:

#### Improved performance of UART communication for Bluetooth® devices

The delay between UART data packet transfers was reduced from about 16 ms to 2 ms. This improves the overall performance of programming Bluetooth® devices via the ChipLoad tool on Windows OS.

#### New part numbers in OpenOCD and ModusToolbox™ Programmer

Added support of new part numbers for EZ-PD™ CCG7S, PSOC™ 4 HVMS and FX3G2 device families.

### 1.5 Version 1.2.0

#### New release of ModusToolbox™ Programmer

This release provided a new delivery mechanism for the various programming tools, as well as the ModusToolbox™ Programmer that replaced the legacy Cypress Programmer.

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## Primary changes

ModusToolbox™ Programmer version 5.2.0 provides updated GUI controls for more flexible device selection. The **Programmer** pull-down allows you to select the hardware probe, the **Board** pull-down provides the list of supported board (Kit) names, and the **Device** pull-down allows you to select the MPN of the target device to be programmed with the tool.

## What's included in the programming tools package

## 2 What's included in the programming tools package

This programming tools package release includes the following tools and versions:

Tool Name	Current Release (1.4.0)	Previous Release (1.3.1)
ModusToolbox™ Programmer (mtb-programmer)	5.4.0	5.3.0
Firmware Loader (fw-loader)	3.9.0	3.8.0
OpenOCD	5.7.0	5.6.1
KitProg3	2.8.0	2.7.0
ChipLoad	1.6.7	1.6.6
DetAndID	5.0.1 (no change)	5.0.1
MbtP	5.0.1 (no change)	5.0.1
Driver media	1.2.0 (no change)	1.2.0

- ModusToolbox™ Programmer graphical user interface: This tool provides a graphical user interface to program, erase, verify, and read the flash of the target device.
- Firmware Loader: This tool provides a command-line interface to upgrade KitProg3 firmware and easily switch back and forth between legacy KitProg2 and KitProg3.
- OpenOCD: This tool provides debugging and in-system programming functionality for target devices for ModusToolbox™ and mtb-programmer.
- KitProg3: This asset provides communication firmware that supports both CMSIS-DAP programming and debugging.
- ChipLoad: This tool allows you to download firmware to AIROC™ Bluetooth® platforms.
- DetAndID: This tool allows you to detect the serial port to which AIROC™ Bluetooth® device is connected.
- MbtP: This tool allows you to download firmware files to CYW955572BTEVK-01 devices to quickly disable the autobaud watchdog.
- Driver media: This asset delivers hardware device drivers for Windows and udev\_rules scripts for Linux.

### 2.1 Supported operating systems

- Windows 11 (x64), Windows 10 (x64)
- macOS 13 "Ventura," macOS 14 "Sonoma," macOS 15 "Sequoia" (x64) Intel and ARM processors via Rosetta
- Ubuntu 22.04 (x64), and Ubuntu 24.04 (x64)

## What's included in the programming tools package

### 2.2 Supported Kits/Platforms

This release provides support for the following kits/platforms:

Platform	MCU/SOC/SIP
<b>PSOC™ 6 kits</b>	
CY8CKIT-062-BLE	CY8C6347BZI-BLD53
CY8CKIT-062-WIFI-BT	CY8C6247BZI-D54
CY8CPROTO-062-4343W	CY8C624ABZI-S2D44
CY8CPROTO-063-BLE	CYBLE-416045-02
CY8CKIT-062S2-43012	CY8C624ABZI-S2D44
CY8CPROTO-062S2-43012	CY8C624ABZI-S2D44
CY8CPROTO-062S3-4343W	CY8C6245LQI-S3D72
CY8CPROTO-062S2-43439	CY8C624ABZI-S2D44
CY8CEVAL-062S2	CY8C624ABZI-S2D44
CY8CKIT-062S4	CY8C6244LQI-S4D92
CYW9P62S1-43438EVB-01	CY8C6247BZI-D54
CYW9P62S1-43012EVB-01	CY8C6247FDI-D32
CY8CPROTO-064S1-SB	CYB06447BZI-D54
CY8CPROTO-064B0S3	CYB0445LQI-S3D42
CY8CPROTO-064B0S1-BLE	CYB06447BZI-BLD53
CY8CKIT-064B0S2-4343W	CYB0644ABZI-S2D44
CY8CKIT-064S0S2-4343W	CYS0644ABZI-S2D44
CY8CEVAL-064SXS2	CYB0644ABZI-S2D44
CY8CKIT-062S2-AI	CY8C624ABZI-S2D44
CYUSB3G2KIT	CYUSB4024-BZXI
<b>PSOC™ 4 kits</b>	
CY8CKIT-041-40XX	CY8C4045AZI-S413
CY8CKIT-041-41XX	CY8C4146AZI-S433
CY8CKIT-145-40XX	CY8C4045AZI-S413
CY8CKIT-149	CY8C4147AZI-S475
CY8CKIT-045S	CY8C4548AZI-S485
CY8CKIT-041S-MAX	CY8C4149AZI-S598
CY8CKIT-040T	CY8C4046LQI-T452
CY8CPROTO-040T	CY8C4046LQI-T452
CYHVMS-64K-56-001	CY8C4146LWE-HVS115X
CYHVPA-128K-32-001	CY8C4147LCE-HV423
KIT_PSoC4-HVPA-144K_LITE	CY8C4147LCE-HV423
KIT_PSoC4-HVMS-128K_LITE	CY8C4147LWE-HVS135X
KIT_PSoC4-HVMS-64K_LITE	CY8C4146LWE-HVS115X
<b>PMG1 kits</b>	
CY7110	CYPM1011-24LQXI
CY7111	CYPM1111-40LQXIT
CY7112	CYPM1211-40LQXIT
CY7113	CYPM1311-48LQXI

## What's included in the programming tools package

Platform	MCU/SOC/SIP
EVAL_PMG1_B1_DRP	CYPM1116-48LQXI
EVAL_PMG1_S1_DRP	CYPM1111-40LQXIT
EVAL_PMG1_S3_DUALDRP	CYPM1321-97BZXIT
<b>AIROC™ Wi-Fi kits and boards</b>	
BCM94343WWCD2	STM32F412
BCM943362WCD4	STM32F205
BCM943438WCD1	STM32F411
BCM943364WCD1	STM32F411
CYW943012EVB-04	STM32L4A6
CYW943340WCD1	STM32F417
CYW943455EVB-02	STM32H7
CYW943907AEVAL1F (Rev 1.1)	CYW43907KWBG
CYW943907WAE4	CYW43907KWBG
CYW954907AEVAL1F	CYW54907KWBG
<b>AIROC™ Bluetooth® kits and boards</b>	
CYBT-213043-EVAL	CYW20819A1
CYBT-213043-MESH	CYW20819A1
CYBT-343026-EVAL	CYW20706A2
CYBT-413055-EVAL	CYW20719B2
CYBT-423054-EVAL	CYW20719B2
CYBT-483056-EVAL	CYW20719B2
CYW920719Q40EVB-01	CYW20719
CYW920719B2Q40EVB-01	CYW20719B2KUMLG
CYW920706WCDEVAL	CYW20706A2
CYW920721B2EVK-02	CYW20721B2KUMLG
CYW920721B2EVK-03	CYW20721B2KUMLG
CYW920735Q60EVB-01	CYW20735B1
CYW920819EVB-02	CYW20819A1KFBG
CYW920820EVB-02	CYW20820A1KFBG
CYW989820EVB-01	CYW89820BWMLG
CYW9M2BASE-43012BT	CYW43012C0
CYW920829M2EVK-02	CYW20829B0LKML
CYW955513EVK-01	CYW55513IUBG
CYW920829SM2EVK-02	CYW20829B0
CYW920829B0M2P4TAI100-EVK	CYW20829B0
CYW920829B0M2P4EPI100-EVK	CYW20829B0
<b>XMC7100/7200 kits and boards</b>	
KIT_XMC72_EVK	XMC7200D-E272K8384
KIT_XMC71_EVK_LITE_V1	XMC7100D-F176K4160
<b>TraveoT2G kits</b>	
KIT_T2G-B-E_LITE	CYT2BL5CAE
KIT_T2G_C-2D-6M_LITE	CYT4DNJBZS



## What's included in the programming tools package

Platform	MCU/SOC/SIP
<b>PSOC™ Control C3 kits</b>	
KIT_PSC3M5_EVK	PSC3M5FDS2AFQ1
KIT_PSC3M5_CC1	PSC3M5FDS2AFQ1
KIT_PSC3M5_CC2	PSC3M5FDS2AFQ1

### 2.3 Supported product families

- PSOC™ 60xx, PSOC™ 61xx, PSOC™ 62xx, PSOC™ 63xx, PSOC™ 64xx
- PSOC™ 4000S, PSOC™ 4000T, PSOC™ 4100S, PSOC™ 4100S Plus, PSOC™ 4100T Plus, PSOC™ 4100S Max, PSOC™ 4500S, PSOC™ 4500H, PSOC™ 4 HVPA, PSOC™ 4 HVMS
- PMG1-Sx, PMG1-Bx
- WLC1
- EZ-PD™ CCG7S, CCG7D
- CCG2, CCG3PA, CCG3PA2, CCG4, CCG6 and CCG8
- PAG2S
- XMC7xxx, XMC5xxx
- T2G BH/BE (CYT4BB/BF, CYT2Bx, CYT3Bx), T2G C2D (CYT4Dx)
- AIROC™ CYW20829
- FX3G2 and FX2G3
- PSoC C3

### 2.4 Supported programming hardware

- SEGGER J-Link stand-alone probe
- SEGGER J-Link onboard probe
- MiniProg4 stand-alone programmer/debugger
- KitProg3 onboard programmer/debugger
- FTDI FT2232H

### 2.5 Open source

Portions of this software package are licensed under free and/or open source licenses such as the GNU General Public License. Such free and/or open source software is subject to the applicable license agreement and not our license agreement covering this software package. The applicable license agreements are available online:

<https://www.infineon.com/cms/en/design-support/software/free-and-open-source-software-foss/modustoolbox-foss-packages/>

## Installation

### 3 Installation

Download the applicable ModusToolbox™ Programming tools package for your operating system from the link available here:

<https://softwaretools.infineon.com/tools/com.ifx.tb.tool.modustoolboxprogtools>

*Note: If your installation directory contains spaces or illegal characters, ModusToolbox™ Programming tools installer prevents you from installing into that directory.*

#### 3.1 System prerequisites

We recommend the following minimum system configuration:

- PassMark CPU score > 2000 ([cpubenchmark.net](http://cpubenchmark.net))
- Minimum of 4 GB RAM
- Minimum 140 MB free disk space
- Minimum 1280x1024 screen resolution

#### 3.2 Installing on Windows

1. Run the Windows installer program: *ModusToolboxProgtools\_1.x.x.<build>.exe*
2. Follow the instructions of the installation wizard.
3. For unattended install run the installer with `/silent` argument:

```
.\ModusToolboxProgtools_1.x.x.<build>.exe /silent
```

#### 3.3 Installing on Ubuntu

Run the installer program: *ModusToolboxProgtools\_1.x.x.<build>.deb*

#### 3.4 Installing on macOS

1. Run the pkg installer: *ModusToolboxProgtools\_1.x.x.<build>.pkg*.

Follow the instructions on the installation wizard.

2. For unattended install, run the installer from the terminal:

```
sudo installer -pkg ModusToolboxProgtools_1.x.x.<build>.pkg -target /
```

Known issues/limitations

## 4 Known issues/limitations

This section lists the known issues/limitations of this release.

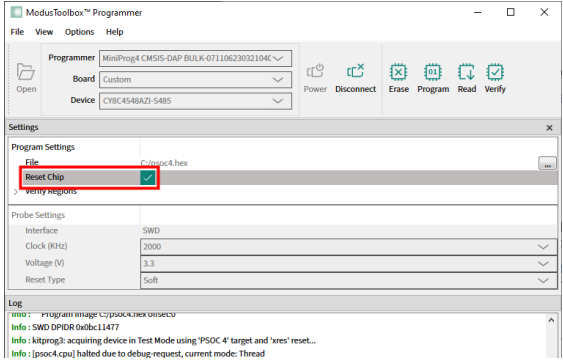
### 4.1 ModusToolbox™ Programmer GUI

Problem	Workaround
<p>The ModusToolboxProgtools 1.3.x installer has the same GUID as the ModusToolboxProgtools 1.2.0 installer.</p> <p>As a result, the ModusToolbox™ Setup program and the Windows Apps setting only show the most recent installed version (either 1.3.0 or 1.2.0), but not both. So, you cannot uninstall the version that is not shown.</p>	<p>To uninstall the version that isn't shown in the ModusToolbox™ Setup program (or via the Apps setting on Windows), do the following:</p> <ul style="list-style-type: none"> <li>• On Windows, open File Explorer to C:\Infineon\Tools\ModusToolboxProgtools-1.X and run <i>unins000.exe</i> binary.</li> <li>• On macOS, open a terminal and run:  <pre>% sudo /Applications/ModusToolboxProgtools-1.X/uninstall.sh</pre></li> <li>• On Linux, open a terminal and run:  <pre>\$ sudo dpkg -r modustoolbox-programming-tools-1.X.0</pre></li> </ul> <p>Where X is the version to uninstall.</p>
<p>Not able to reliably program the following kits:</p> <ul style="list-style-type: none"> <li>• CYBT-213043-EVAL</li> <li>• CYBT-213043-MESH</li> <li>• CYBT-343026-EVAL</li> <li>• CYBT-413055-EVAL</li> <li>• CYBT-423054-EVAL</li> <li>• CYBT-483056-EVAL</li> <li>• CYW920719Q40EVB-01</li> <li>• CYW920719B2Q40EVB-01</li> <li>• CYW920706WCDEVAL</li> <li>• CYW920721B2EVK-02</li> <li>• CYW920721B2EVK-03</li> <li>• CYW920735Q60EVB-01</li> <li>• CYW920819EVB-02</li> <li>• CYW920820EVB-02</li> <li>• CYW989820EVB-01</li> <li>• CYW9M2BASE-43012BT</li> <li>• CYW955513EVK-01</li> </ul>	<p>Put the device into recovery mode:</p> <ol style="list-style-type: none"> <li>1. Press and hold the Recovery button.</li> <li>2. Press and hold the Reset button for one second.</li> <li>3. Release the Reset button.</li> <li>4. Release the Recovery button.</li> <li>5. Re-program the board as usual.</li> </ol>

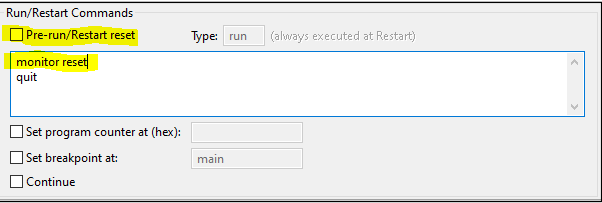
## Known issues/limitations

Problem	Workaround
<p>Read operation does not work on the following kits:</p> <ul style="list-style-type: none"> <li>• CYBT-213043-EVAL</li> <li>• CYBT-213043-MESH</li> <li>• CYBT-343026-EVAL</li> <li>• CYBT-413055-EVAL</li> <li>• CYBT-423054-EVAL</li> <li>• CYBT-483056-EVAL</li> <li>• CYW920719Q40EVB-01</li> <li>• CYW920719B2Q40EVB-01</li> <li>• CYW920706WCDEVAL</li> <li>• CYW920721B2EVK-02</li> <li>• CYW920721B2EVK-03</li> <li>• CYW920735Q60EVB-01</li> <li>• CYW920819EVB-02</li> <li>• CYW920820EVB-02</li> <li>• CYW989820EVB-01</li> <li>• CYW9M2BASE-43012BT</li> <li>• CYW955513EVK-01</li> </ul>	<p>No workaround</p>
<p>Unable to program the following kits after erase:</p> <ul style="list-style-type: none"> <li>• CYW920719Q40EVB-01</li> <li>• CYW920719B2Q40EVB-01</li> <li>• CYW920819EVB-02</li> <li>• CYW920820EVB-02</li> <li>• CYW989820EVB-01</li> <li>• CYW920721B2EVK-02</li> <li>• CYW920721B2EVK-03</li> <li>• CYW920706WCDEVAL</li> <li>• CYW920735Q60EVB-01</li> <li>• CYBT-213043-EVAL</li> <li>• CYBT-213043-MESH</li> <li>• CYBT-343026-EVAL</li> <li>• CYBT-413055-EVAL</li> <li>• CYBT-423054-EVAL</li> <li>• CYBT-483056-EVAL</li> <li>• CYW9M2BASE-43012BT</li> <li>• CYW955513EVK-01</li> </ul>	<p>After erase put the device into recovery mode:</p> <ol style="list-style-type: none"> <li>1. Press and hold the Recovery button.</li> <li>2. Press and hold the Reset button for one second.</li> <li>3. Release the Reset button.</li> <li>4. Release the Recovery button.</li> <li>5. Re-program the board as usual.</li> </ol>
<p>Some AIROC™ Bluetooth® kits may not be accessible if hot-plugged on particular Windows 10 x64 PCs. Affected platforms are CYW9207xx and CYW9208xx.</p>	<p>After restarting application, the device should be available in mtb-programmer.</p>
<p>AIROC™ Bluetooth® kits of CY208xx family (CYW920819EVB-02, CYW920820EVB-02, etc.) may fail when programming hex files that include data in static section at addresses 0x0050000-0x00500400.</p>	<p>No workaround.</p>

## Known issues/limitations

Problem	Workaround
<p>When trying to read memory from a PSoC™ 6 product family's chip and with external memory option turned on, ModusToolbox™ Programmer fails to succeed and prints error message:  <b>Error: Failed to initialize bank psoc6_smif1_cm0</b>                      ...                      ...  <b>Error: ** SMIF configuration structure not found or invalid</b></p>	<p>In general (due to SMIF design), you will not be able to read or even erase SMIF bank if your PSoC™ 6 target device does not contain correct SMIF configuration.</p> <p>Since the QSPI flash programming algorithms (flash loaders) are universal and supposed to work regardless of the memory MPN and mapping settings used in the design, you must put the configuration data into the internal flash and provide a pointer in SFlash TOC2 region.</p>
<p>In PSoC™ 4000S, PSoC™ 4000T, PSoC™ 4100S, PSoC™ 4100S Plus, PSoC™ 4100S Max, PSoC™ 4500S, PSoC™ 4500H, PSoC™ 4 HVPA, PSoC™ 4 HVMS, PMG1-Sx, PMG1-Bx, CCG2, CCG3PA, CCG3PA2, CCG4, CCG6, CCG7S, CCG7D, CCG8 and PAG2S product families after programming you may see the error message:  <b>Error connecting DP: cannot read IDR</b></p> <p>This occurs in such cases:</p> <ol style="list-style-type: none"> <li>1. When application starts after programming and reconfigures SWD pins.</li> <li>2. Device go to deep sleep mode or other low power mode.</li> </ol>	<p>We recommend either of the following options to work around this issue:</p> <ol style="list-style-type: none"> <li>1. Deselect the <b>Reset Chip</b> check box and execute reset manually when disconnected from the device:</li> </ol>  <ol style="list-style-type: none"> <li>2. Do not reconfigure SWD pins in the application.</li> </ol>

## 4.2 OpenOCD

Problem	Workaround
<p>When programming or debugging, you see the following error message:  <b>Error connecting DP: cannot read IDR</b></p> <p>This occurs if the debug port is disabled in the BSP for the specific project.</p>	<p>You can enable the debug port for the specific project, or safely ignore this message if the debug port is still disabled.</p>
<p>When programming a PSoC™ 64 device, the following error displays:  <b>Error: timed out while waiting for target halted</b></p>	<p>There is no impact on functionality, programming is successful. To suppress the message:</p> <ol style="list-style-type: none"> <li>1. Open the "Program" launch configuration and navigate to the <b>Startup</b> tab.</li> <li>2. In the <b>Run/Restart Commands</b> section, deselect the <b>Pre-run/Restart reset</b> check box.</li> <li>3. Add the monitor reset command to the text field below the check box:</li> </ol> 
<p>Debug doesn't work on AIROC™ CYW20829 devices with a J-Link probe in JTAG in Eclipse and VS Code IDEs.</p>	<p>Use the SWD interface.</p>

## Known issues/limitations

Problem	Workaround
While using the Eclipse IDE or Visual Studio Code on Windows for various program/debug operations, there's a plugin issue that prevents the debug port from shutting down. This could result in abnormal power consumption, the watchdog timer being blocked, or the inability to connect in JTAG mode after a successful connection in SWD mode.	Reset the device. For example, on the CY8CKIT-062S2-40312 kit, press the <b>SW1/XRES</b> button.
Openocd cannot detect incorrect configuration file for the connected target in next possible scenarios: <ul style="list-style-type: none"> <li>Selected config does not support JTAG interface, in this case the next error displays: <b>JTAG transport not supported by selected target, please switch to SWD</b></li> <li>Selected config has differences in JTAG parameters, in this case the next error displays: <b>DAP initialization failed (check connection, power, transport, DAP is enabled etc.)</b></li> </ul>	No workaround

## 4.3 Installer

Problem	Workaround
Visual installer issues on Windows <ul style="list-style-type: none"> <li>If “Custom Installation” option chosen in the installation wizard, then “Next” and “Back” buttons clicked, the “Next” button disappears and “Install” button is shown instead.</li> <li>If “Custom Installation” option chosen in the installation wizard, a custom installation path is provided and then user returns to the install mode screen and chooses “Quick Installation”, the tool is still installed into the previously selected custom folder.</li> </ul>	No workaround

## 4.4 Limitations

### Limitation description

Not able to detect KitProg3/MiniProg4 probe when the OpenOCD process has been killed. Killing the OpenOCD process leaves KitProg3/MiniProg4 in unpredictable/invalid state. Unplug KitProg3/MiniProg4 from the USB port and re-attach.

ModusToolbox™ Programmer GUI loses connection with CYW943907AEVAL1F, CYW943907WAE4 kits in case they are programmed with an invalid image. Messages about lost connection can be safely ignored. Connection with target is restored during next operation.

Unable to access PSOC™ 6 MCU via JTAG of J-Link if DAP has been switched to SWD mode previously. Hardware reset or power cycle is required to switch the DAP back to JTAG mode

External memory programming does not work on CYW943340WCD1 kit

The CYW9M2BASE-43012BT kit supports only direct download to the RAM. To recover or reset this device, you should power cycle the board. Due to this limitation, the only allowed operation for this device is program to the RAM.

Due to significant changes in design of production PSOC™ 64 devices, mtb-programmer does not support previous pre-released PSOC™ 64 secure devices. In case if pre-released PSOC™ 64 silicon is connected to mtb-programmer, the appropriate warning message is displayed in the Log view.

Some PMG1 devices can be acquired by programmer tool only in the Power Cycle mode. See description of the “Programming Mode” setting in the ModusToolbox™ user guide

## Revision history

### Revision history

Revision	Date	Description of Change
**	2023-09-13	Initial release.
*A	2024-04-29	Updates per 1.1.0 release
*B	2024-06-03	Updates per 1.2.0 release
*C	2024-08-13	Updates per 1.2.1 release
*D	2024-10-04	Updates per 1.3.0 release Updated section "Known issues/limitations" Updated PSoC™ product category trademark to PSOC™
*E	2024-12-19	Added known issue about installer GUID.
*F	2025-01-06	Updates per 1.3.1 release
*G	2025-01-16	Updated details for changes included in version 1.3.1.
*H	2025-03-18	Updates per 1.4.0 release

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