Pairing Guide

Infineon memory solutions

Pairing guide for SemiDrive

To simplify your development process and get you to market faster, Infineon collaborates with chipset manufacturers worldwide to ensure that our memory devices work with leading microprocessors, microcontrollers, SoCs and FPGAs. The program covers design validation and interoperability testing, and you will often find our devices on industry chipset reference designs.

SemiDrive Technology Ltd. develops SoCs for automotive applications. The X9 family is designed for cockpit applications, and provides the performance and AI acceleration needed for the latest computing and multimedia cockpits.

The G9 family is optimized for central gateway applications, and supports high functional safety integrity levels. The V9 family is ideal for autonomous driving applications, and meets the increasing compute demands of next-generation ADAS systems.

Infineon SEMPER™ NOR Flash is architected and designed for functional safety. It delivers the highest levels of reliability, and is ASIL-B-compliant and ASIL-D-ready. Infineon HYPERRAM™ 2.0 operates across the HYPERBUS™ 12-pin interface, a low-cost, low pin-count interface supporting up to 400MB/s. It is ideal for use as a buffer.

SEMPER™ NOR Flash and HYPERRAM™ memories supported by SemiDrive

SemiDrive SoC part number	Application	Density	NOR Flash part number	HYPERRAM™ part number
X9	Automotive digital cockpit	512 Mb	S26HS512TGA S28HS512TGA	
		1 Gb	S26HS01GTGA S28HS01GTGA	
G9	Automotive gateway	64 Mb		S27KL0642 S27KS0642
		128 Mb		S71KL1282 S71KS1282
		512 Mb	S26HS512TGA S28HS512TGA	
		1 Gb	S26HS01GTGA S28HS01GTGA	
V9	Automotive ADAS	512 Mb	S25HL512TFA S26HL512TGA S28HL512TGA	
		1 Gb	S25HL01GTFA S26HL01GTGA S28HL01GTGA	

Please contact SDS-memory-ecosystem@infineon.com for additional details.