

KIT_PSC3M5_EVK

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Drawing Numbers

PCBA	121-60703-01
PCB	600-60703-01
FAB DRW	610-60703-01
ASSY DRW	620-60703-01
SCH DRW	630-60703-01

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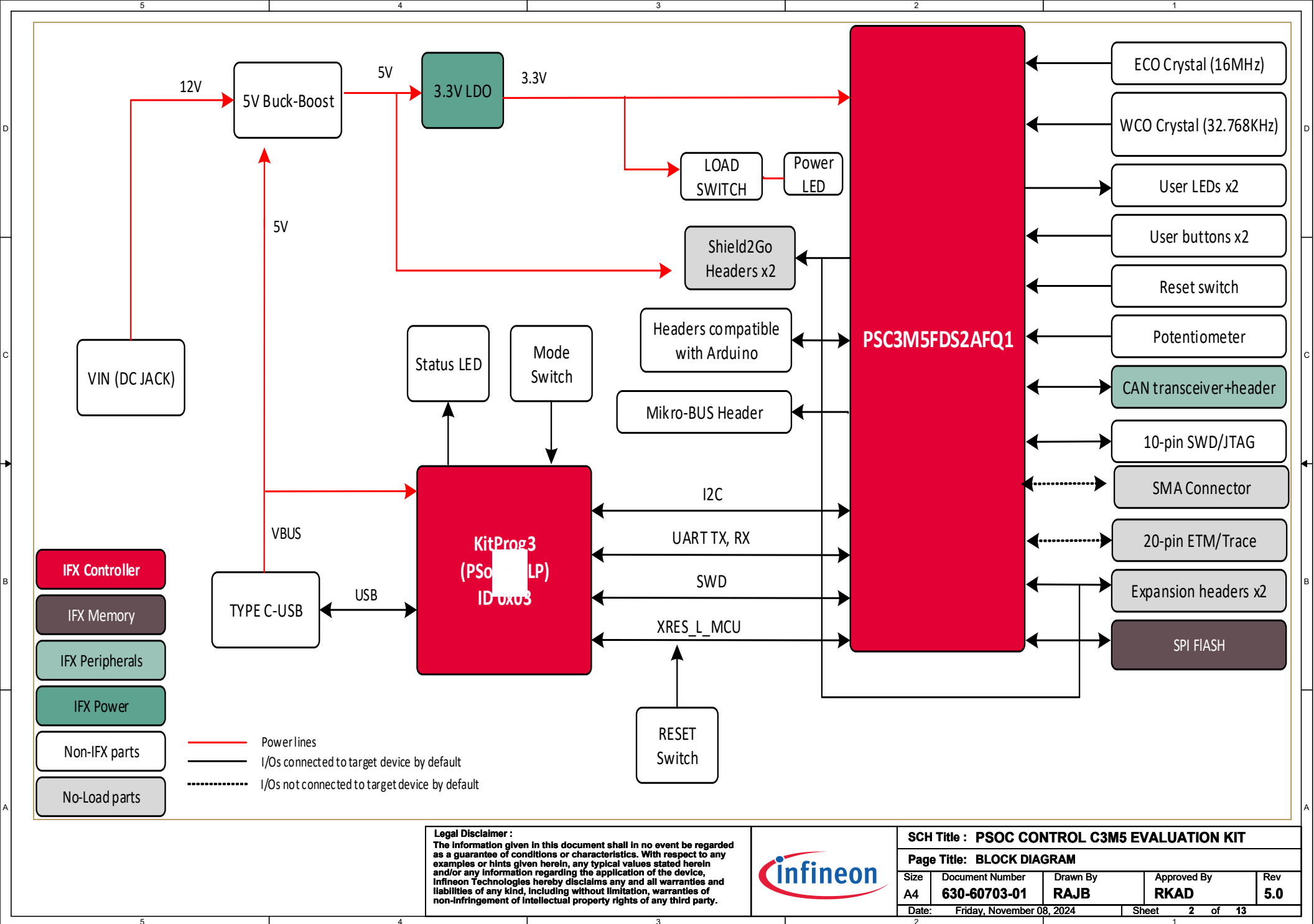
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SCH Title : PSOC CONTROL C3M5 EVALUATION KIT

Page Title: TITLE PAGE

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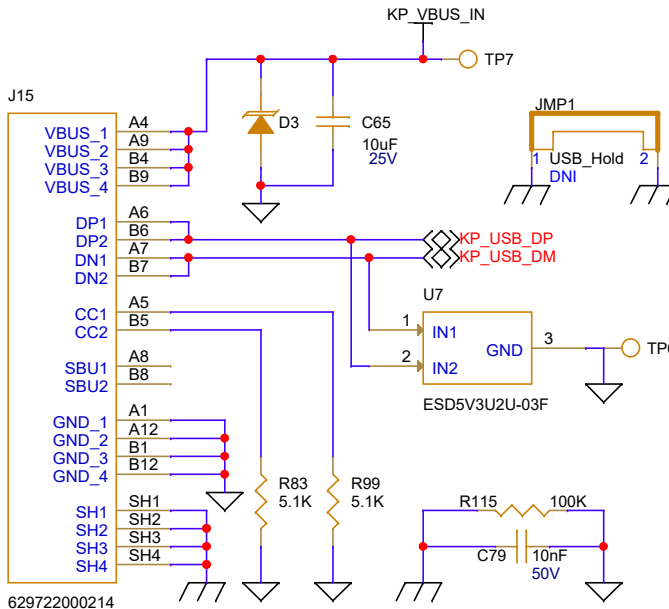


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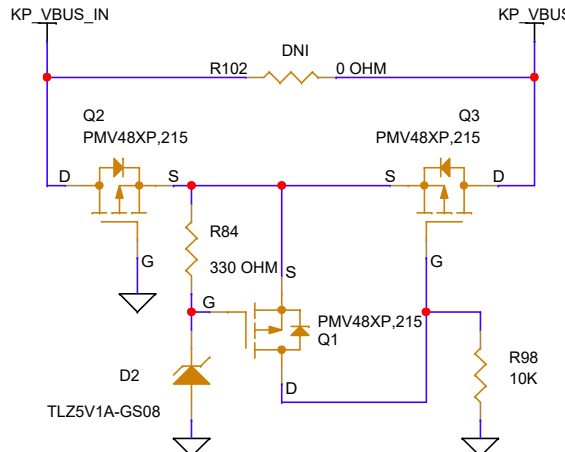


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Size A4	Document Number 630-60703-01	Drawn By RAJB	Approved By RKAD	Rev 5.0
Date: Friday, November 08, 2024		Sheet 2 of 13		

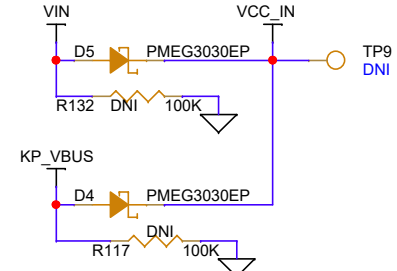
USB Power Supply and Interface



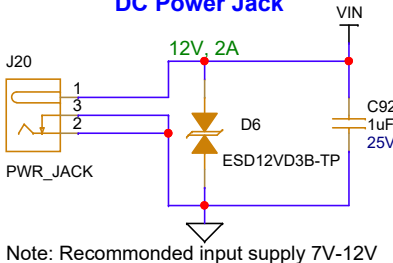
20V Protection Circuit



Power Supply 'OR'ing

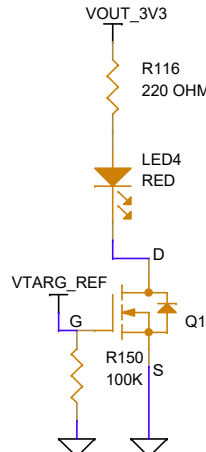


DC Power Jack



Note: Recommended input supply 7V-12V

Power LED



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Page Title: POWER

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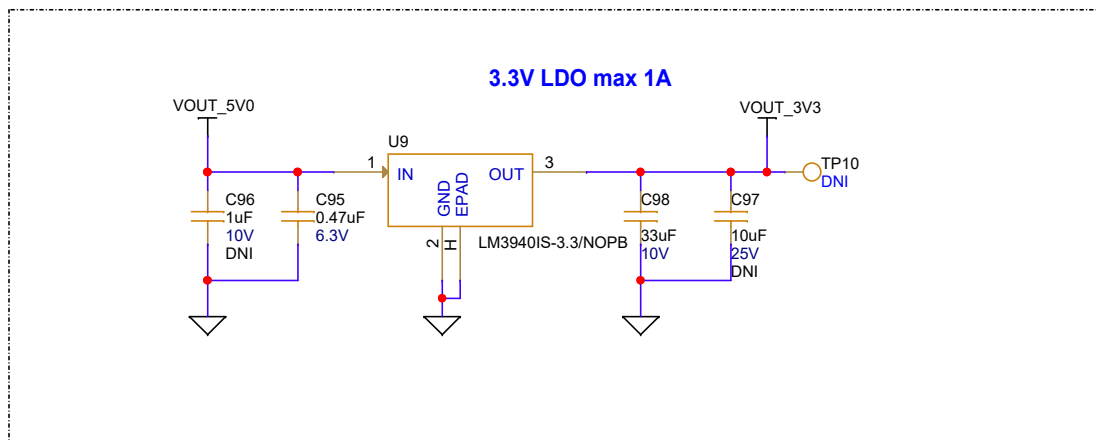
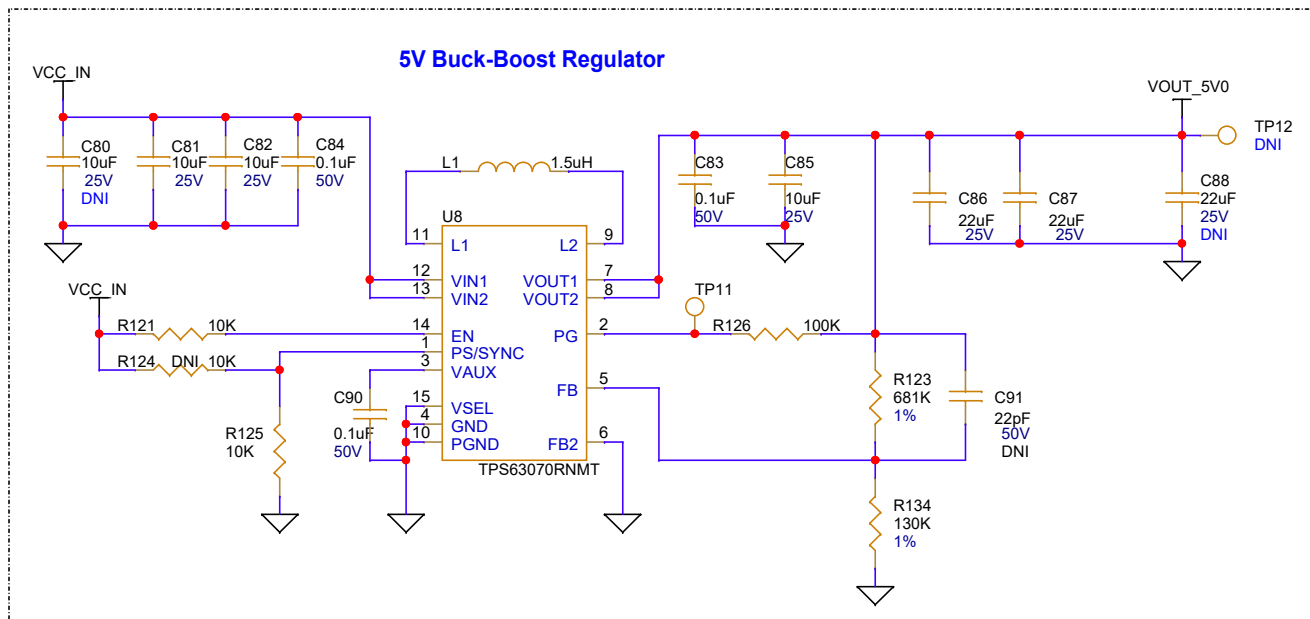
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2

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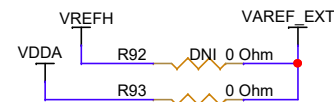
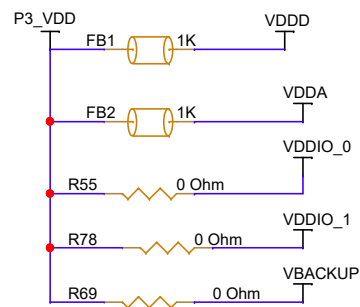
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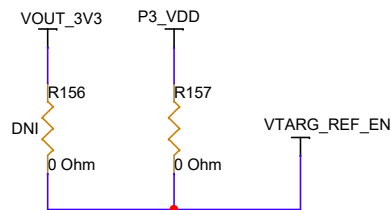
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Page Title: POWER SUPPLY-2				
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The schematic diagram illustrates the power supply connections for the PSC3M5FDS2AFQ1 microcontroller (U1B). The chip is shown with its pins and internal connections to various power rails. The connections are as follows:

- VDD** (Pin 9) is connected to the VDD supply.
- VDDA** (Pin 66) is connected to the VDDA supply.
- VDDIO_0** (Pin 20) is connected to the VDDIO_0 supply.
- VDDIO_0_1** (Pin 40) is connected to the VDDIO_0_1 supply.
- VDDIO_0_2** (Pin 49) is connected to the VDDIO_0_2 supply.
- VDDIO_1** (Pin 80) is connected to the VDDIO_1 supply.
- VCCD** (Pin 10) is connected to the VCCD supply.
- VSS_1** (Pin 6), **VSS_2** (Pin 7), **VSS_3** (Pin 77), **VSS_4** (Pin 78), and **VSS_5** (Pin H) are connected to ground.
- VBACKUP** (Pin 11) is connected to the VBACKUP supply.
- VAREF_EXT** (Pin 65) is connected to the VAREF_EXT supply.
- XRES_L_MCU** (Pin 8) is connected to the XRES_L_MCU supply.

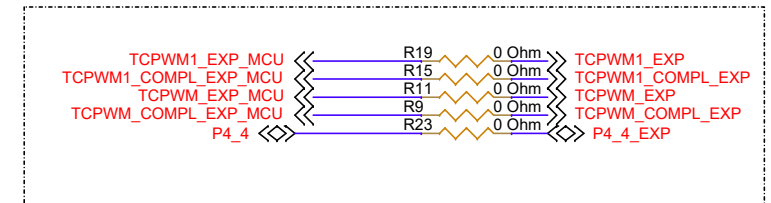
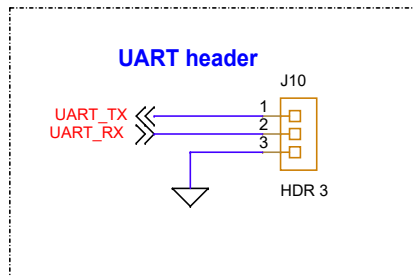
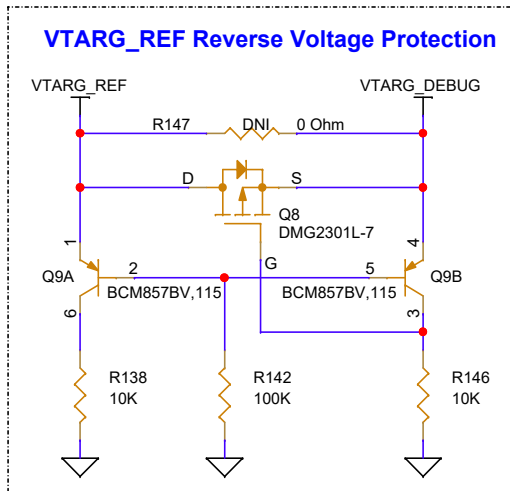
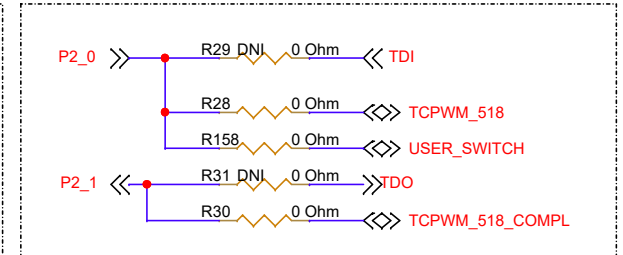
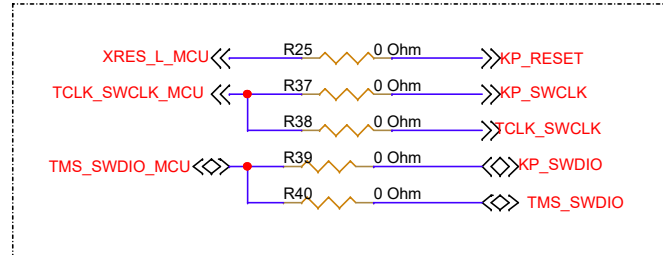
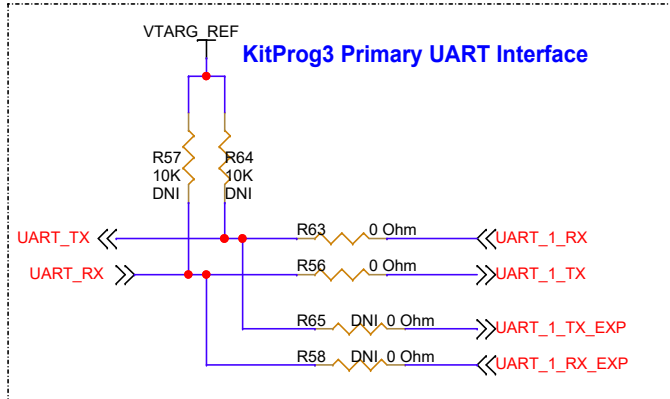
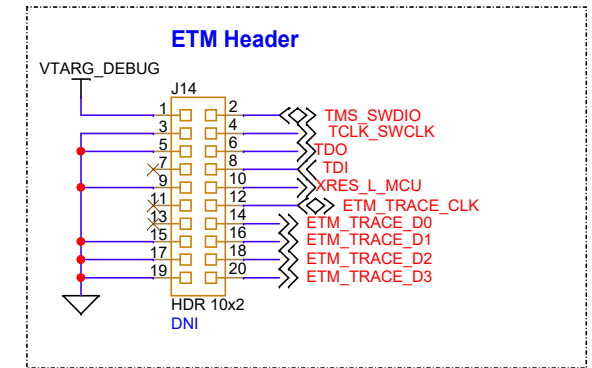
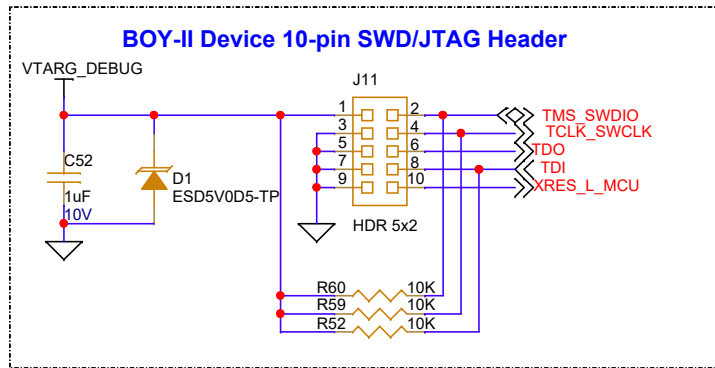
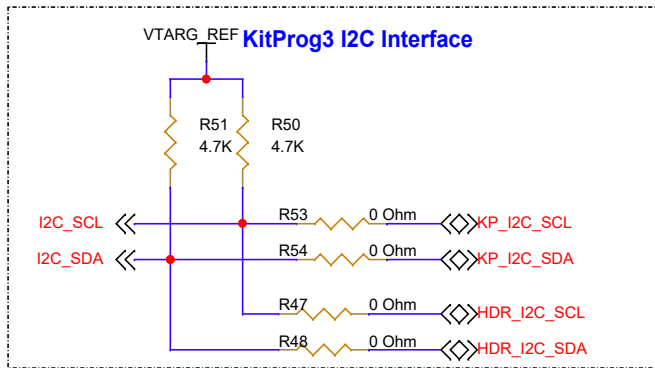
A 0.22uF capacitor (C37) is connected between VAREF_EXT and ground.



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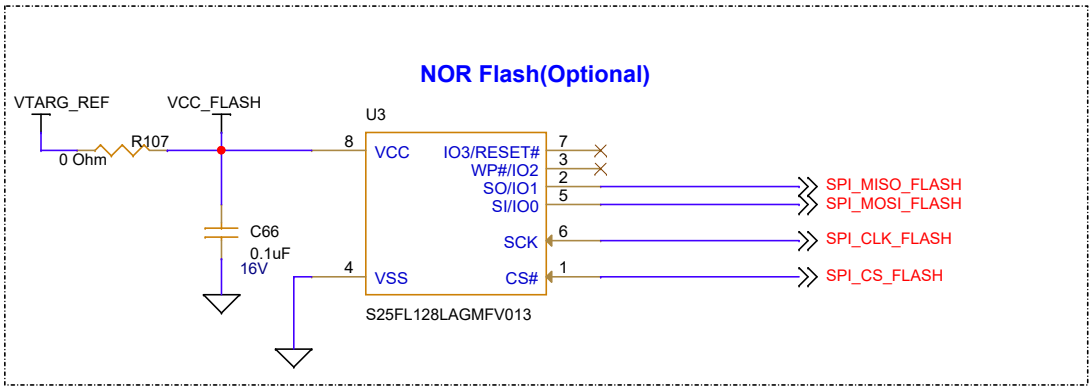
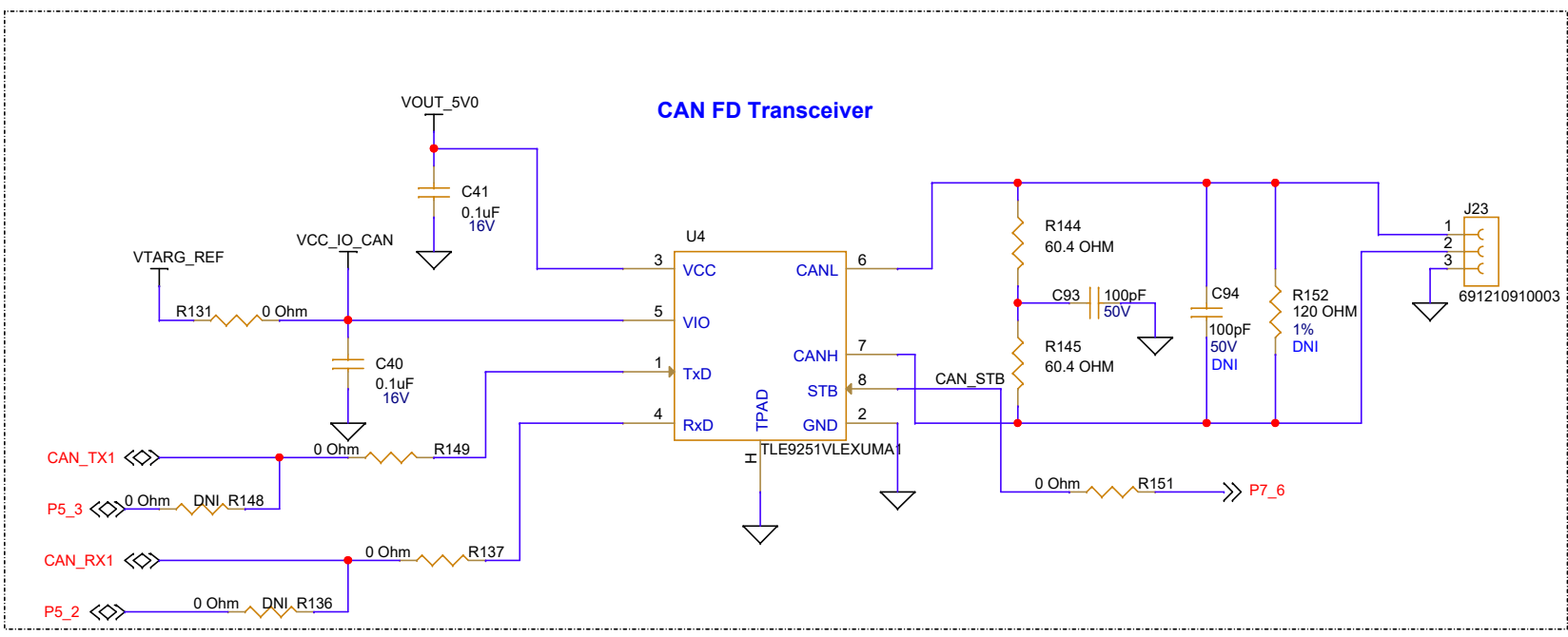
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Page Title: COMMUNICATION AND PROGRAM				
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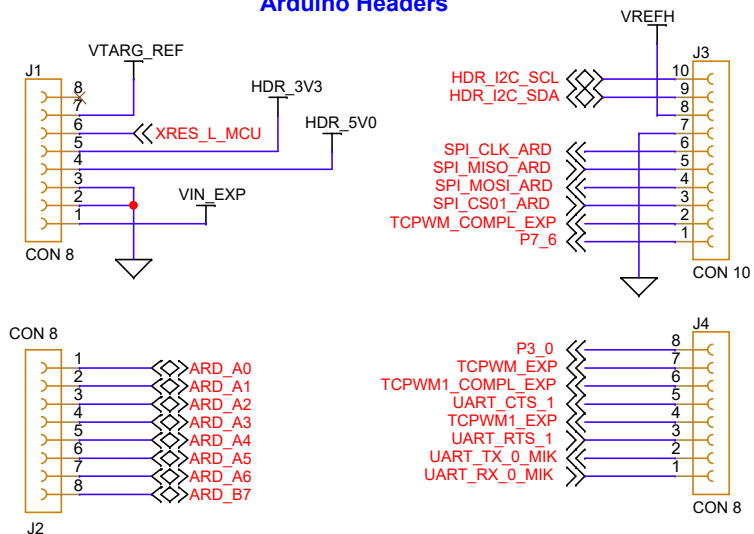


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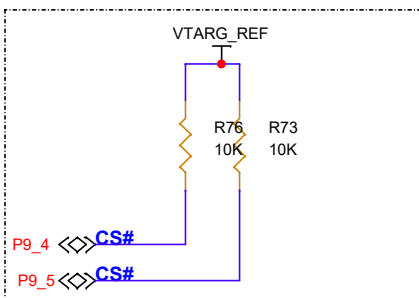
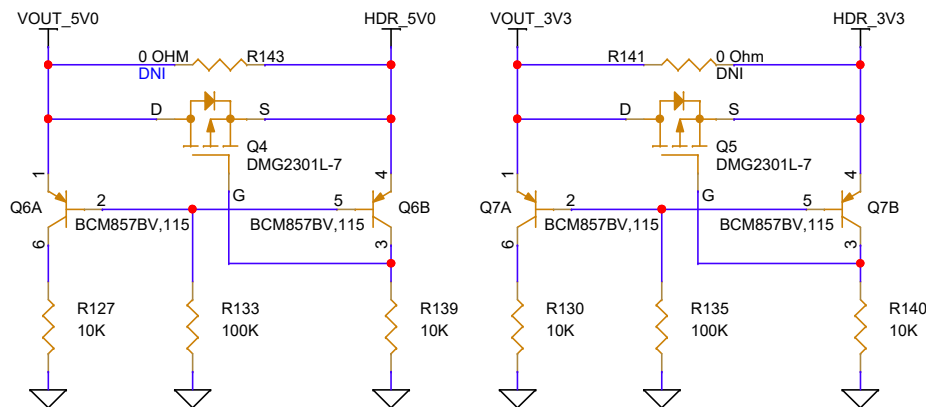


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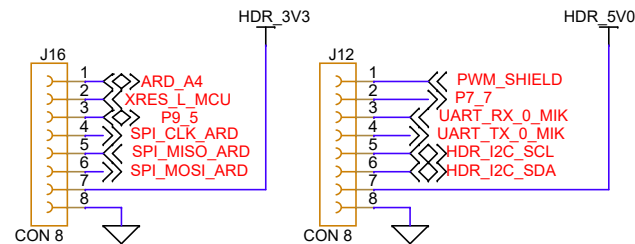
Arduino Headers



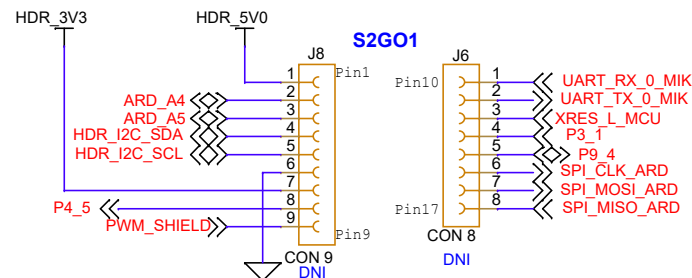
Reverse Voltage Protection for Arduino Power



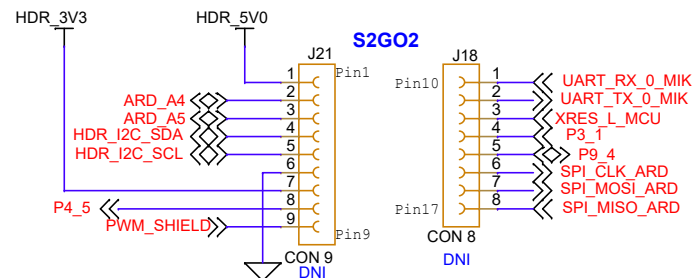
mikroBUS Headers



Shield2Go Interface



Shield2Go Interface



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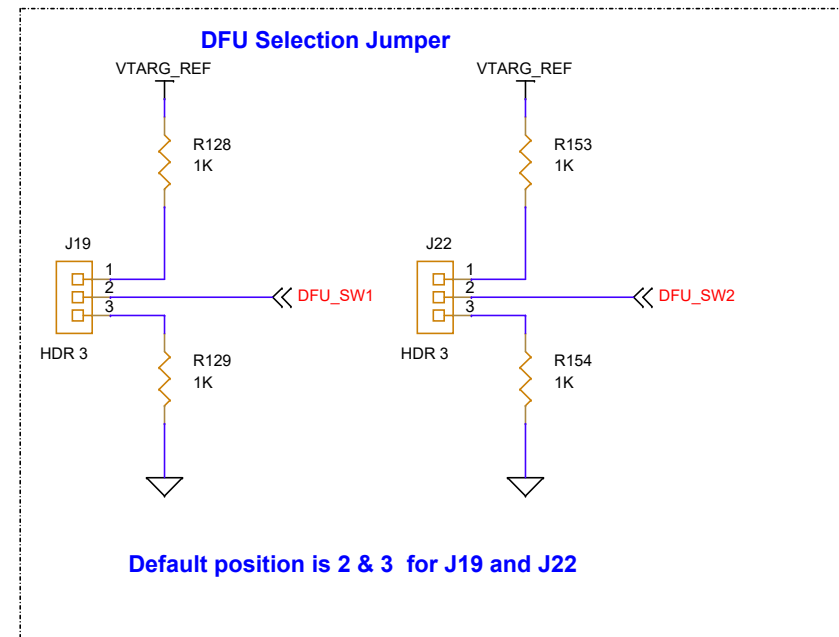
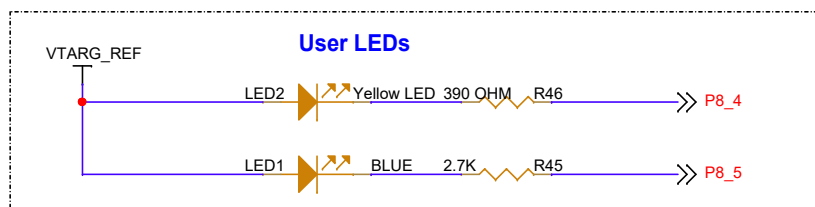
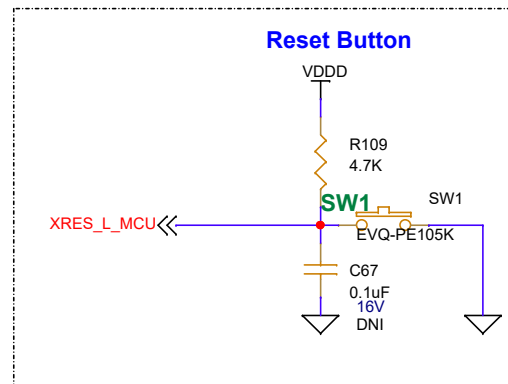
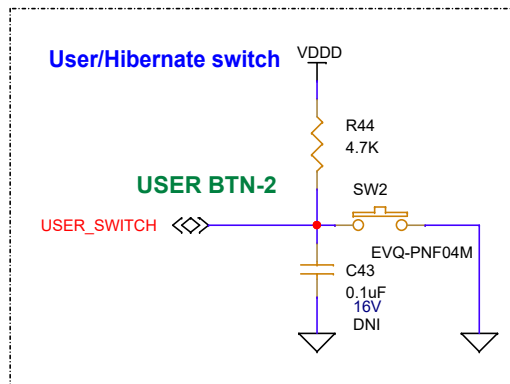
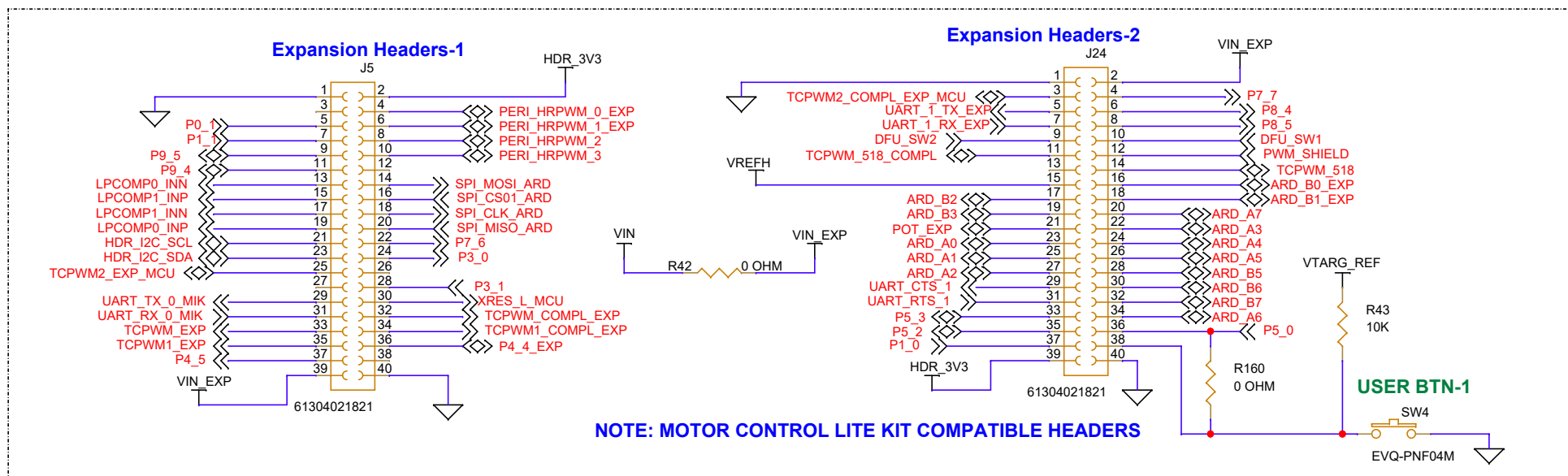
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Page Title: INTERFACE HEADERS

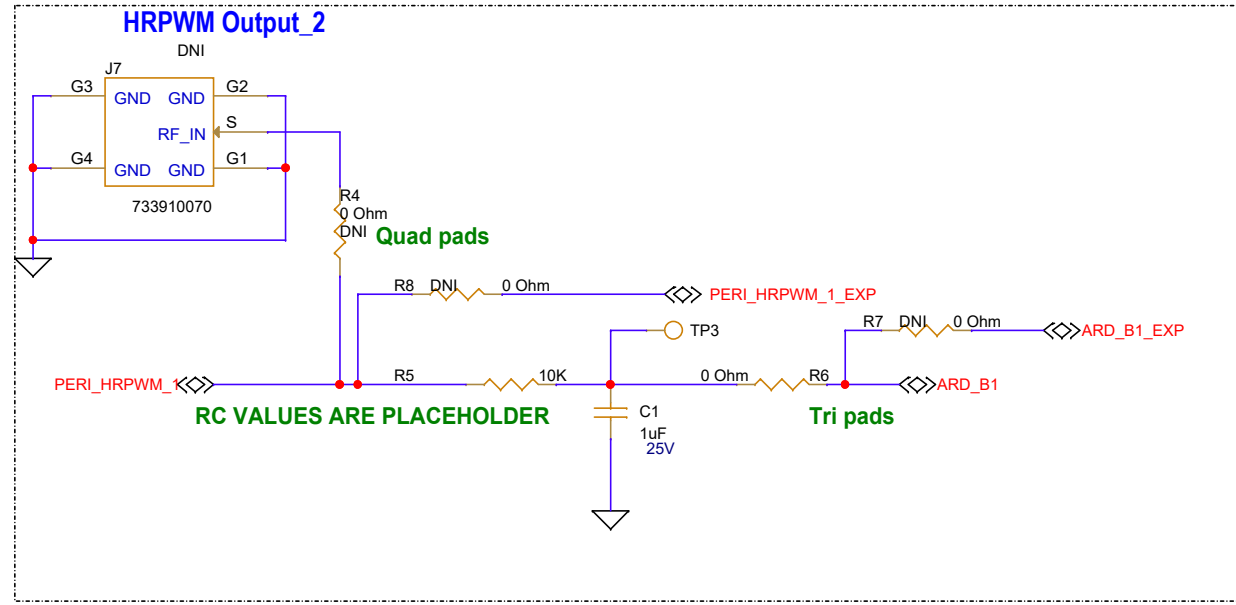
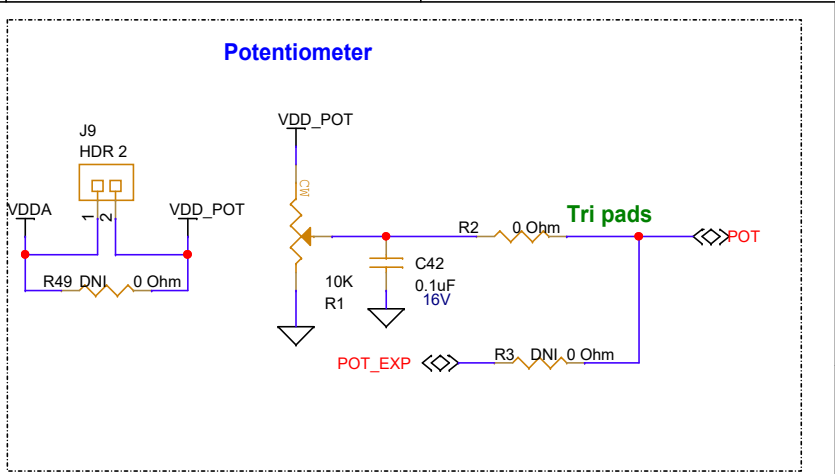
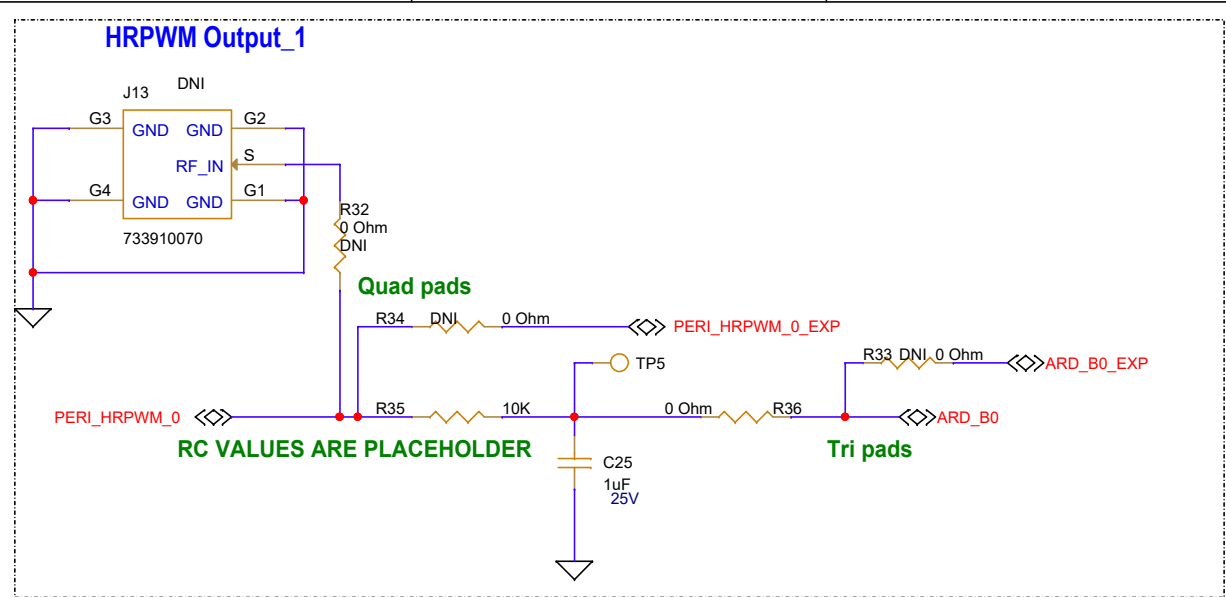
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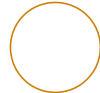


SCH Title : PSOC CONTROL C3M5 EVALUATION KIT				
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Accessories

Cylindrical Bump-ons

ACC6



SJ61A6

ACC7



SJ61A6

ACC8



SJ61A6

ACC9



SJ61A6

Jumper Shunts

ACC11



60900213421

ACC12



60900213421

ACC13



60900213421

ACC18



60900213421

PCBA Labels

ACC16



PCA Label

ACC17



LBL QR Code

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Page Title: ACCESORIES

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REVISION HISTORY

REV	DESCRIPTION OF CHANGE	ORIG. OF CHANGE	DATE
0.01	Preliminary release	RAJB	11/08/2023
0.2	Updated as per the latest requirement and SAS pinout	RAJB	16/11/2023
0.3	Updated as per the latest requirement	RAJB	30/11/2023
0.4	Changed the Hibernate switch logic and Power switch, SPI Flash is added.	RAJB	08/12/2023
0.5	Pin mapping changed in Expansion connector, Hibernate switch changed to previous logic	RAJB	12/12/2023
0.6	Added R195, changed the package of c87, c88 net name change	RAJB	13/12/2023
0.7	Hardfixed the Switch refdes locations	RAJB	18/12/2023
0.8	Back annotated	RAJB	20/12/2023
0.9	VSS and GND net shorted for SMA connector	RAJB	29/01/2024
1.0	Added 100K resistor on load switch enable and BDRB comments addressed, SW5 removed from the design	RAJB	02/02/2024
1.1	LED1,2,3,4 colors changed as per the Marketing team requirement and Expansion header made stuff , P9_0 text added for information, Changed R98 to 500-60433-01	RAJB	19/03/2024
3.0	Updated ETM trace port, POT connection, DFU PU changed Regulator to LM3940	RAJB	21/08/2024
4.0	VAREF part changed to 0.22uF as per JIRA, BD updated, Changed SW4 to USER Button -1 , DFU PU changed to 1K Changed R84 to 330E	RAJB	10/09/2024 16/09/2024
5.0	Silk screen changed and revised to align with Layout as 5.0	RAJB	08/11/2024

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