

Infineon Simplifies Power Sequencing of Xilinx Zynq UltraScale+ Resolve complex power sequencing in minutes ...

www.infineon.com/xilinx

IR PowIRCenter Build 6200 - C:\Users\tochoa1\Desktop\UltraZED docs\UltraZED_Zu03_design.pcd*

File Options Help

Total Pout : 1.90 W

- IRPS5401R2::i2c x12**
 - VCCIO 1.8V::pmb x42
 - Sys Voltage 5.0V::pmb x42
 - Sys Voltage 3.3V::pmb x42
 - VPS_MGTRAVCC::pmb x42
 - LDO::pmb x42
- IRPS5401R2::i2c x11**
 - VCCO_PSDDR_504::pmb x41
 - VCCINT_IO/VCCBRAM::pmb x41
 - Sys Voltage 3.3V::pmb x41
 - VCCINT::pmb x41
 - VCC_PSDDR_PLL::pmb x41
- IRPS5401R2::i2c x10**
 - VCCO_PSIO, VCCO_PSADC, VCC_PSAUX::pmb x40
 - VCCAUX, VCCADC, VCCAUX_IO::pmb x40
 - VCC_PSINTLP::pmb x40
 - VCC_PSINTFP, VCC_PSINTFPDDR::pmb x40
 - VCC_PSPLL::pmb x40

Device	Loop	TON_DELAY (ms)	TON_RISE (ms)	TOFF_DELAY (ms)	TOFF_FALL (ms)
IRPS5401R2	VCCIO 1.8V	10	6	0	1
IRPS5401R2	Sys Voltage 5.0V	10	6	0	1
IRPS5401R2	Sys Voltage 3.3V	10	6	0	1
IRPS5401R2	VPS_MGTRAVCC	10	6	0	1
IRPS5401R2	LDO	20	0	0	0
IRPS5401R2	VCCO_PSDDR_504	48	6	24	6
IRPS5401R2	VCCINT_IO/VCCBRA	36	6	12	6
IRPS5401R2	Sys Voltage 3.3V	24	6	24	6
IRPS5401R2	VCCINT	30	6	18	6
IRPS5401R2	VCC_PSDDR_PLL	0	0	0	0
IRPS5401R2	VCCO_PSIO	12	6	48	6
IRPS5401R2	VCCAUX_VCCADC	42	6	6	6
IRPS5401R2	VCC_PSINTLP	6	6	54	6
IRPS5401R2	VCC_PSINTFP	18	6	36	6
IRPS5401R2	VCC_PSPLL	0	0	0	0

Update FMBus Registers

Ready

