USB
Universal Serial Bus
XMC™ microcontrollers
September 2016
Agenda

1. Support DMA mode
2. 2 KBytes RAM available for FIFO
3. USB host
4. USB device
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USB Universal Serial Bus

**Highlights**

XMC™4000 provides USB module that complies with USB 2.0 specification as well as the on-the-go supplement to USB 2.0 specification.

**Key feature**

- Support full speed data transfer with internal PHY
- Support OTG dual-role device
- Power saving functionality

**Customer benefits**

- Reduce cost for external PHY and allow fast data transfer (12 Mbps)
- Can support USB application for host only, device only or OTG device
- Reduce power consumption when USB is not in use

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USB
Full speed data transfer with internal PHY

› Internal USB PHY is included in the USB module
› USB PHY supports full speed transfer rate up to 12 Mbps
› It also supports USB low speed data transfer
USB
Supports host only, device only and dual-role

› Supports Host Negotiation Protocol (HNP) → allows OTG devices to exchange their capabilities and adjust the device type
› Supports also host only or device only
› Suitable for all kind of USB applications
USB
Power saving functionality

› Supports USB suspend/resume command
› Supports clock gating of USB hardware when suspend command is used
USB System integration

Target applications
- Virtual COM Port
- Mass storage device
- Firmware upgrade
- Human machine interface
- Connectivity
- General purpose

* USB device only

The USB module provides single service request connected to the NVIC. The interrupt node is handled by the USB driver in order to manage the USB protocol.
Application example: serial communication with PC via virtual COM port

Overview

USB module can be configured to work as device only. With CDC device class stack running on the microcontroller, it is able to create a virtual COM port on the PC with the microcontroller connected through the PC USB port. Thus allows the PC to communicate with the microcontroller via USB.

In brief

With USB module, the microcontroller is able to create a virtual COM port on the PC and allows the PC to communicate with the microcontroller via terminal program such as HYPERTERMINAL™.
USB
Support DMA mode

› In DMA mode, the core fetches the data to be transmitted or updates the received data on the AHB
› Two modes are supported in DMA mode
  - Scatter/gather mode
  - Buffer-pointer based mode
› Decouple the CPU from direct data transfer on USB line
Agenda

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USB
2 KBytes RAM available for FIFO

› USB host
  • Outgoing traffic ➔ two FIFOs with different priorities
    • Periodic FIFO has the higher priority
    • Non periodic FIFO has the lower priority
  • Incoming traffic ➔ All incoming traffic is stored in one FIFO

› USB device
  • Outgoing traffic ➔ each endpoint has its own FIFO. No priority between the FIFOs required. The host starts the transfer from endpoint FIFOs
  • Incoming traffic ➔ all incoming traffic is stored in one FIFO
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USB

USB host

› Up to 14 host channels
› Supports all transfer types: control, bulk, interrupt and isochronous
› Can drive the $V_{BUS}$ by using an external charge pump
› Two transmit FIFOs
  - Periodical
  - Non periodical
USB

USB device

- 7 bidirectional endpoints; endpoint 0 used for control transfers
- Support all transfer types: control, bulk, interrupt, isochronous
- Self powered device; no additional external voltage required
- Individual transmit FIFOs for each IN endpoint
General information

› For latest updates, please refer to:

www.infineon.com/xmc4000

› For support:

http://www.infineonforums.com/forums/8-XMC-Forum
## Support material

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