

Product Qualification Report

IDW20G65C5

SIC DIODES

Description

This product qualification report describes the characteristics of the product with respect to quality and reliability.

The qualification sample selection was done on production lots which were manufactured and tested on standard production processes and meet the defined requirements.

The qualification test results of those products as outlined in this document are based on **JEDEC** for target applications and may reference existing qualification results of similar products. Such referencing is justified by the structural similarity of the products.

Qualification Assessment

Fully qualified according to JEDEC for Industrial Applications and assessed as PASS

For further information about comparable products, please contact the nearest Infineon Technologies office (www.infineon.com).



qualified since June 2012

IDW20G65C5 PG-TO247-3 MSL: NA

Electrical Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
High Temperature Reverse Bias	HTRB	Ta = 175°C **	1000 h	3×77	0/231	PASS
JESD22 A108		$V_R = V_{R,max}$				
High Humidity High Temperature Reverse Bias	H ₃ TRB*	Ta = 85°C	1000 h	3×77	0 / 231	PASS
JESD22 A101		RH = 85%				
		$V_R = 80\% V_{R,max}$				
Intermitted Operational Life Test	IOL*	Delta T =100K	15000 cyc.	3×77	0 / 231	PASS
MIL-STD 750 / Meth.1037						

Environmental Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Pre-conditioning (SMD device only)	PC	MSL and 3x		4 X 231	0/924	PASS
J-STD020 / JESD22 A113		reflow 260°C				
Resistance to Solder Heat (TH device only)	SHR	Solder dip		3X 22	o / 66	PASS
JESD B-106		Ts=260°C				
		3x 10 sec				
Temperature Cycling	TC*	-55°C to +150°C	1000 cyc.	3×77	0/231	PASS
JESD22 A104						
Autoclave***	AC*	Ta=121°C	96 h	3×77	0/231	PASS
JESD22 A102		RH=100%				
or						
Unbiased Highly Accelerated Stress Test ***	UHAST*	Ta = 130°C				
JESD22 A118		RH = 85%				

Mechanical Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Physical Dimensions	PD			3 X 10	0/30	PASS
JESD B-100						
Solderability	SD			3×5	0/15	PASS
J-STD-002						

Notes:

- * For SMD devices reliability stress tests are performed after preconditioning test (PC) according to JESD22
- ** Specifically used stress temperature is according to product capability documented in the product datasheet
- *** Selection of used stress test depending on specific availability of respective reliability stress equipment

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Do you have a question about this document?

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Document reference

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