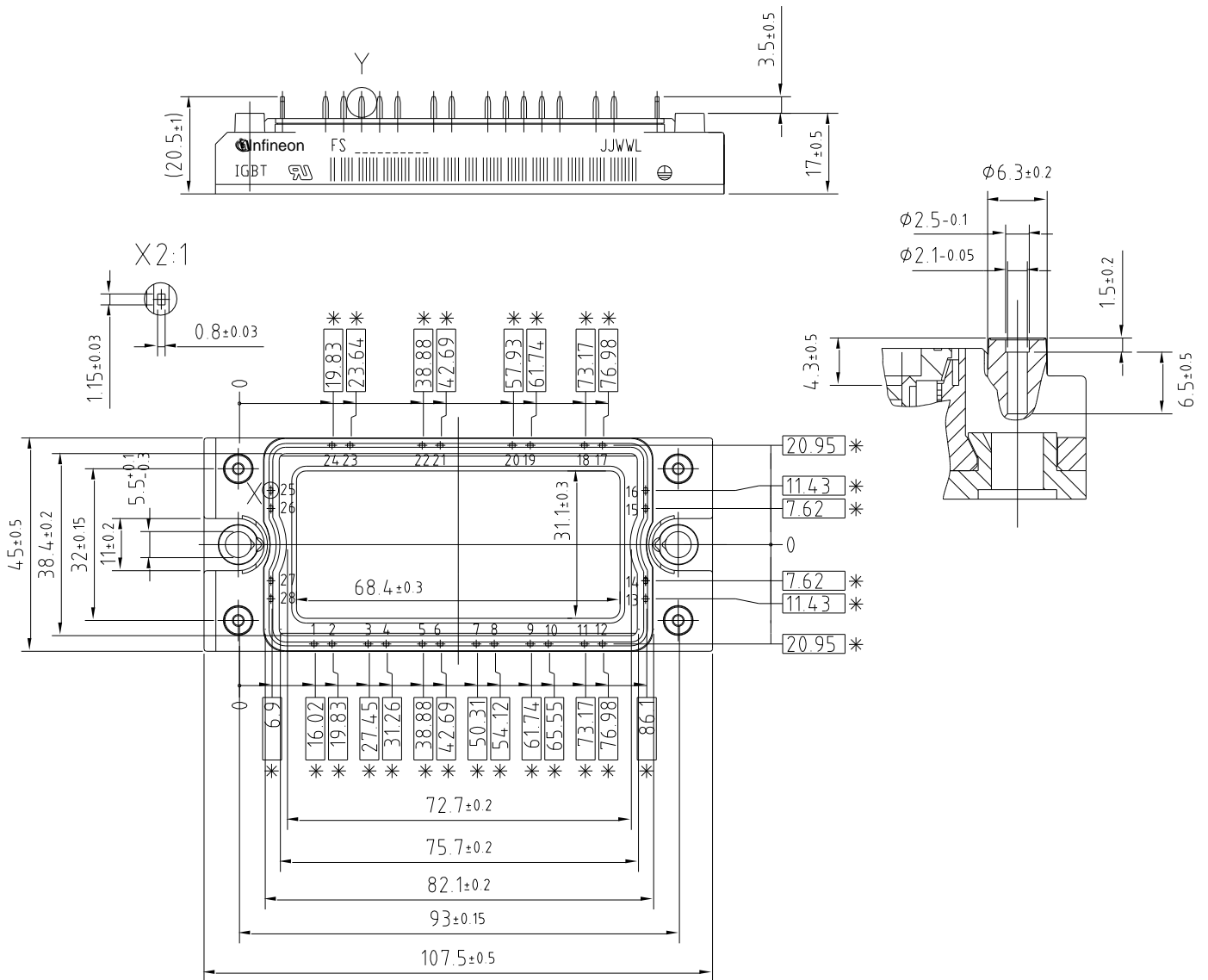
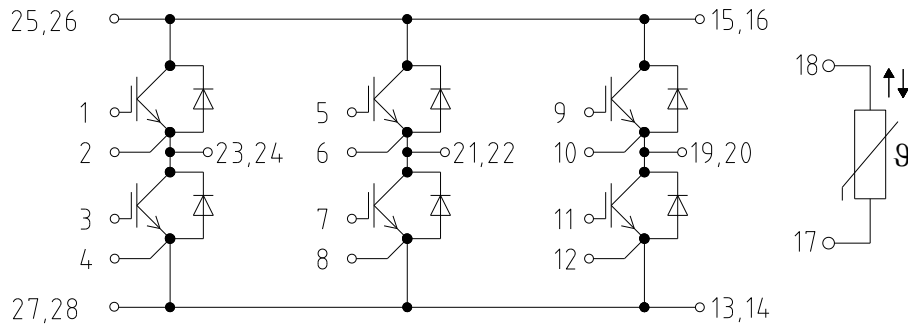


Package outline AG-ECONO2-6-1



* = alle Maße mit einer Toleranz von $\phi 0.4$
 * = all dimensions with tolerance of $\phi 0.4$

Circuit diagram FS50R12KT3



Edition 2016-04-11

Published by

Infineon Technologies AG

81726 München, Germany

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