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Continuity of Specifications

There is no change to this document as a result of offering the device as a Cypress product. Any changes that have been made are the result of normal document improvements and are noted in the document history page, where supported. Future revisions will occur when appropriate, and changes will be noted in a document history page.

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Cypress continues to support existing part numbers. To order these products, please use only the Ordering Part Numbers listed in this document.

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About Cypress

Cypress (NASDAQ: CY) delivers high-performance, high-quality solutions at the heart of today's most advanced embedded systems, from automotive, industrial and networking platforms to highly interactive consumer and mobile devices. With a broad, differentiated product portfolio that includes NOR flash memories, F-RAM™ and SRAM, Traveo™ microcontrollers, the industry's only PSoC® programmable system-on-chip solutions, analog and PMIC Power Management ICs, CapSense® capacitive touch-sensing controllers, and Wireless BLE Bluetooth® Low-Energy and USB connectivity solutions, Cypress is committed to providing its customers worldwide with consistent innovation, best-in-class support and exceptional system value.

Major Changes

■ Document Information

	Previous edition
Document title	F ² MC-16LX 16-BIT MICROCONTROLLER MB90460/465 Series HARDWARE MANUAL
Document code	CM44-10120-3E
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	Revised edition
Document title	F ² MC-16LX 16-BIT MICROCONTROLLER MB90460/465 Series HARDWARE MANUAL
Document code	CM44-10120-4E
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■ Major Changes in Revised Edition

See the following pages.

■ Issuance of this list

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Main changes in this edition

Page	Section	Change Results
	-	The product name is changed to "MB90460/465 series".
83	CHAPTER 5 CLOCK 5.3 Clock Selection Register (CKSCR)	The function column of bit 14 in Table 5.3-1 is changed. ("· Writing has no effect on the operation." is added.)
106	CHAPTER 6 LOW POWER CONSUMPTION MODE 6.6 State Change Diagram	Figure 6.6-1 is changed.
109	CHAPTER 6 LOW POWER CONSUMPTION MODE 6.7 State of Pins in Standby Mode and during Reset	"*3" under Table 6.7-1 is changed.
123	CHAPTER 7 INTERRUPT 7.3.2 Interrupt Control Register Functions	The summary is changed.
230	CHAPTER 12 16-BIT RELOAD TIMER 12.1 Overview of the 16-bit Reload Timer	"● External trigger operation" is changed.
253	CHAPTER 12 16-BIT RELOAD TIMER 12.8 Sample Programs for the 16-bit Reload Timer	"● Coding example" is changed.
298	CHAPTER 14 MULTI-FUNCTIONAL TIMER 14.4.3 Timer Control Status Register (TCCSH, TCCSL)	The function column of bit 3 in Table 14.4-2 is changed. ("· Even after "1" is written, the counter value is not initialized if "0" is written to this bit before the next count clock." is added.)
521	CHAPTER 18 DTP/EXTERNAL INTERRUPT CIRCUIT 18.4.1 DTP/interrupt Cause Register (EIRR)	"■ DTP/interrupt Cause Register (EIRR)" is changed. ("Notes:" is added.)
522	CHAPTER 18 DTP/EXTERNAL INTERRUPT CIRCUIT 18.4.2 DTP/interrupt Enable Register (ENIR)	"■ DTP/interrupt Enable Register (ENIR)" is changed. ("Note:" is added.)
525	CHAPTER 18 DTP/EXTERNAL INTERRUPT CIRCUIT 18.5 Operation of the DTP/External Interrupt Circuit	"■ Setting the DTP/external Interrupt Circuit" is changed. ("1. Set as an input port the general I/O port to be used also as a pin to input external interrupts." is added.)
530	CHAPTER 18 DTP/EXTERNAL INTERRUPT CIRCUIT 18.6 Usage Notes on the DTP/External Interrupt Circuit	"● Input polarities of external interrupts" is changed. ("If the request input level is level setting, the pulse width requires a longer period than the minimum pulse width stated on the data sheet. Also, as long as the interrupt input pin retains the active level, interrupt requests continue to be generated to the interrupt controller, even if the DTP/external interrupt cause register is cleared." is added.)
552	CHAPTER 20 8/10-BIT A/D CONVERTER 20.4.2 A/D Control Status Register 0 (ADCS0)	The function column of bit5 to bit3 in Table 20.4-2 is changed. ("Notes:" is added.)

Page	Section	Change Results
607	CHAPTER 23 512K / 1024K BIT FLASH MEMORY 23.6.5 Temporarily Stopping the Sector Deletion	"■ Temporarily Stopping the Sector Deletion" is changed. (· up to 15 μ s \rightarrow up to 20 μ s · The sector deletion temporary stop command must be executed 20 μ s or more after the sector deletion command or sector deletion restart command is issued.)
616	CHAPTER 24 EXAMPLE OF F2MC-16LX MB90F462/ F462A/F463A CONNECTION FOR SERIAL WRITING 24.1 Standard Configuration for Serial On-board Writing (Fujitsu Standard)	Table 24.1-1 is changed.

The vertical lines marked in the left side of the page show the changes.

Main changes in this edition

Page	Changes (For details, refer to main body.)
631 to 692	Changed the entire part of "APPENDIX B Instructions"

The vertical lines marked in the left side of the page show the changes.