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About Cypress

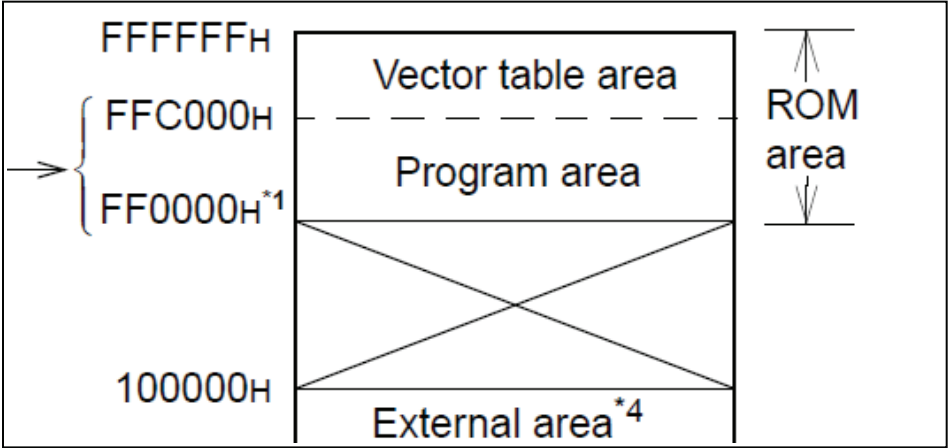
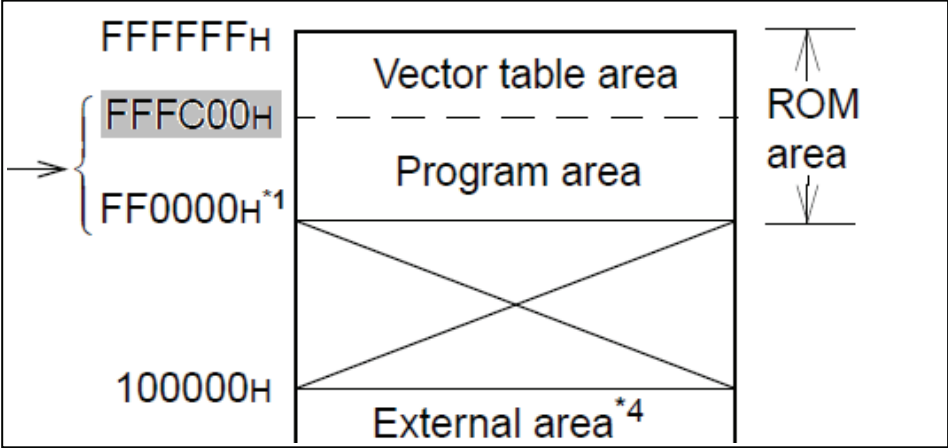
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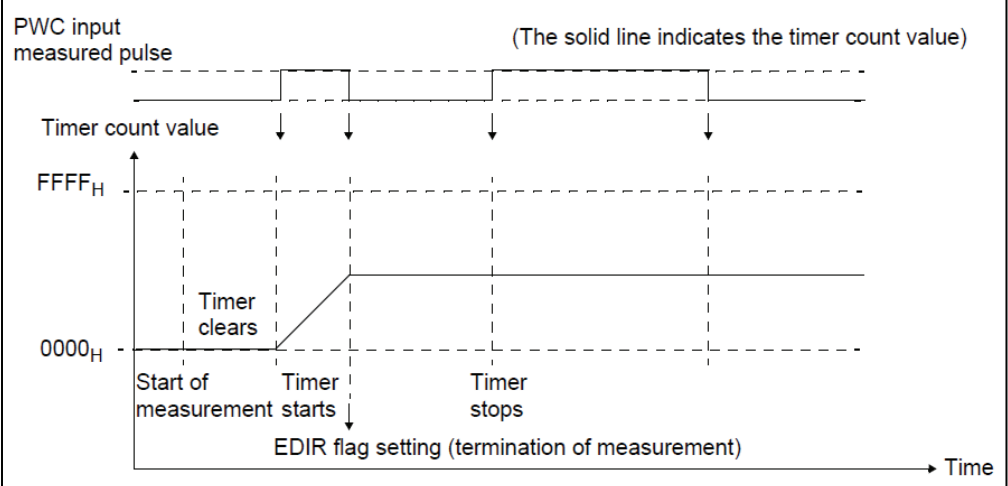
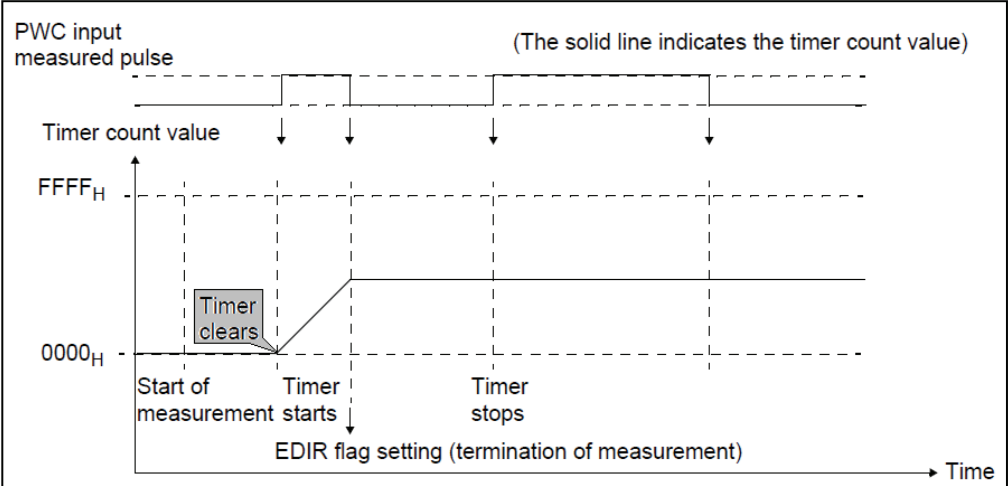
Errata

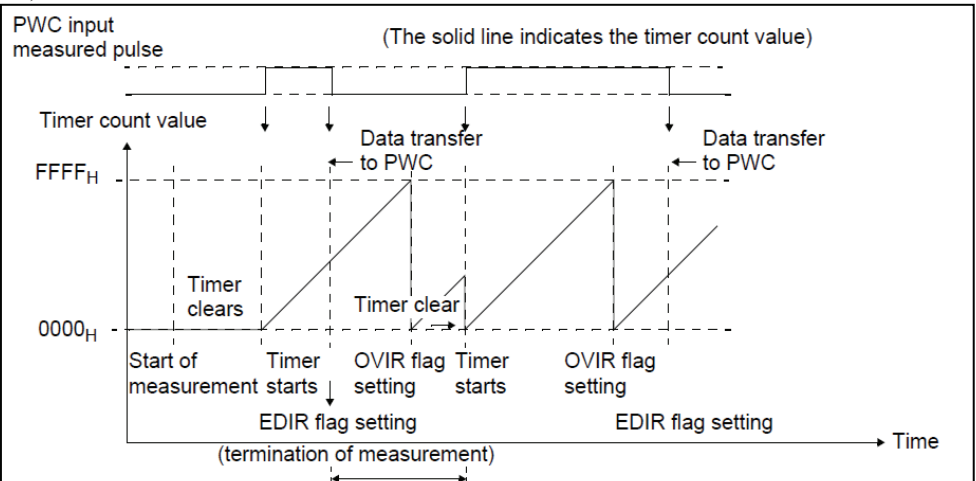
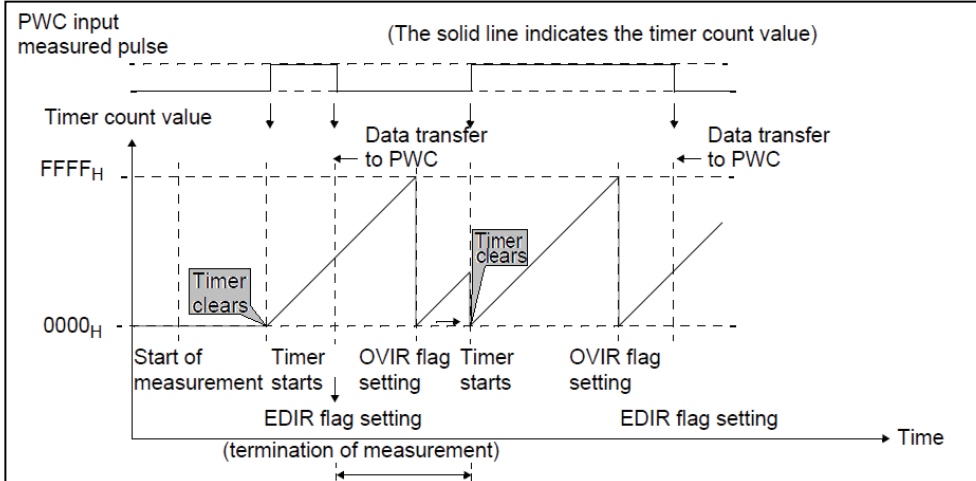
This errata sheet is for MB90460/465 Series Hardware Manual Rev.4 (CM44-10120-4E)

F²MC-16LX
16-BIT MICROCONTROLLER
MB90460/465 Series
HARDWARE MANUAL

2011. 8.2

Date	Page	Item	Description
2011/8/2	29	3.2	<p>The following description of "Figure 3.2-1 Sample Relationship between the F²MC-16LX System and the Memory Map" was corrected as indicated by shading below.</p> <p>(Error)</p>  <p>(Correct)</p>  <p>[mcu_doc1011]</p>

Date	Page	Item	Description
2009/4/2	449	16.6	<p>The following description of "Figure 16.6-3 Pulse-width Measurement Operation (Single Measurement Mode, H-width Measurement Mode)" was corrected as indicated by shading below.</p> <p>(Error)</p>  <p>(Correct)</p>  <p>[mcu doc0872]</p>

Date	Page	Item	Description
2009/4/2	449	16.6	<p>The following description of "Figure 16.6-4 Pulse-width Measurement Operation (Continuous Measurement Mode, H-width Measurement Mode)" was corrected as indicated by shading below.</p> <p>(Error)</p>  <p>*: The timer value during this period is not guaranteed (a timer overflow may result in OVIR being set)</p> <p>(Correct)</p>  <p>*: The timer value during this period is not guaranteed (a timer overflow may result in OVIR being set)</p>

[mcu_doc0872]

Date	Page	Item	Description																														
2009/4/2	549	20.4.1	<p>Function of "bit11, bit10" in "Figure 20.4-2 A/D Control Status Register 1 (ADCS1)" was corrected as indicate by shading below.</p> <p>(Error)</p> <table><tr><th>STS1</th><th>STS0</th><th>A/D activation select bit</th></tr><tr><td>0</td><td>0</td><td>Activation by software</td></tr><tr><td>1</td><td>1</td><td>Activation by external trigger or software</td></tr><tr><td>0</td><td>1</td><td>Activation by timer or software</td></tr><tr><td>0</td><td>1</td><td>Activation by external trigger, timer, or software</td></tr></table> <p>(Correct)</p> <table><tr><th>STS1</th><th>STS0</th><th>A/D activation select bit</th></tr><tr><td>0</td><td>0</td><td>Activation by software</td></tr><tr><td>1</td><td>1</td><td>Activation by 16-bit free-run timer zero detection or software</td></tr><tr><td>0</td><td>1</td><td>Activation by 16-bit reload timer 1 output(Rising edge)or software</td></tr><tr><td>0</td><td>1</td><td>Activation by 16-bit free-run timer zero detection, 16-bit reload timer 1 output(Rising edge) or software</td></tr></table> <p style="text-align: right;">[mcu_doc0493]</p>	STS1	STS0	A/D activation select bit	0	0	Activation by software	1	1	Activation by external trigger or software	0	1	Activation by timer or software	0	1	Activation by external trigger, timer, or software	STS1	STS0	A/D activation select bit	0	0	Activation by software	1	1	Activation by 16-bit free-run timer zero detection or software	0	1	Activation by 16-bit reload timer 1 output(Rising edge)or software	0	1	Activation by 16-bit free-run timer zero detection, 16-bit reload timer 1 output(Rising edge) or software
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2008/11/4	694	APPEN DIX B	<p>■ Table B.9-20 XCH Ri, ea Instruction (First Byte = 7EH) is changed.</p> <ul style="list-style-type: none">• Error Item "A" Line of +A "W2+d16,A"• Correct Item "A0" Line of +A "@RW2+d16"																														