

**Schematic modified for
CYW9P62S1-43012EVB-01 PSoC 62S1 Wi-Fi BT Pioneer Kit**

CYW9-BASE-01 Pioneer Baseboard

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Drawing Numbers

PCBA	121-60565-01
PCB	600-60552-01
FAB DRW	610-60552-01
ASSY DRW	620-60552-01
SCH DRW	630-60552-01



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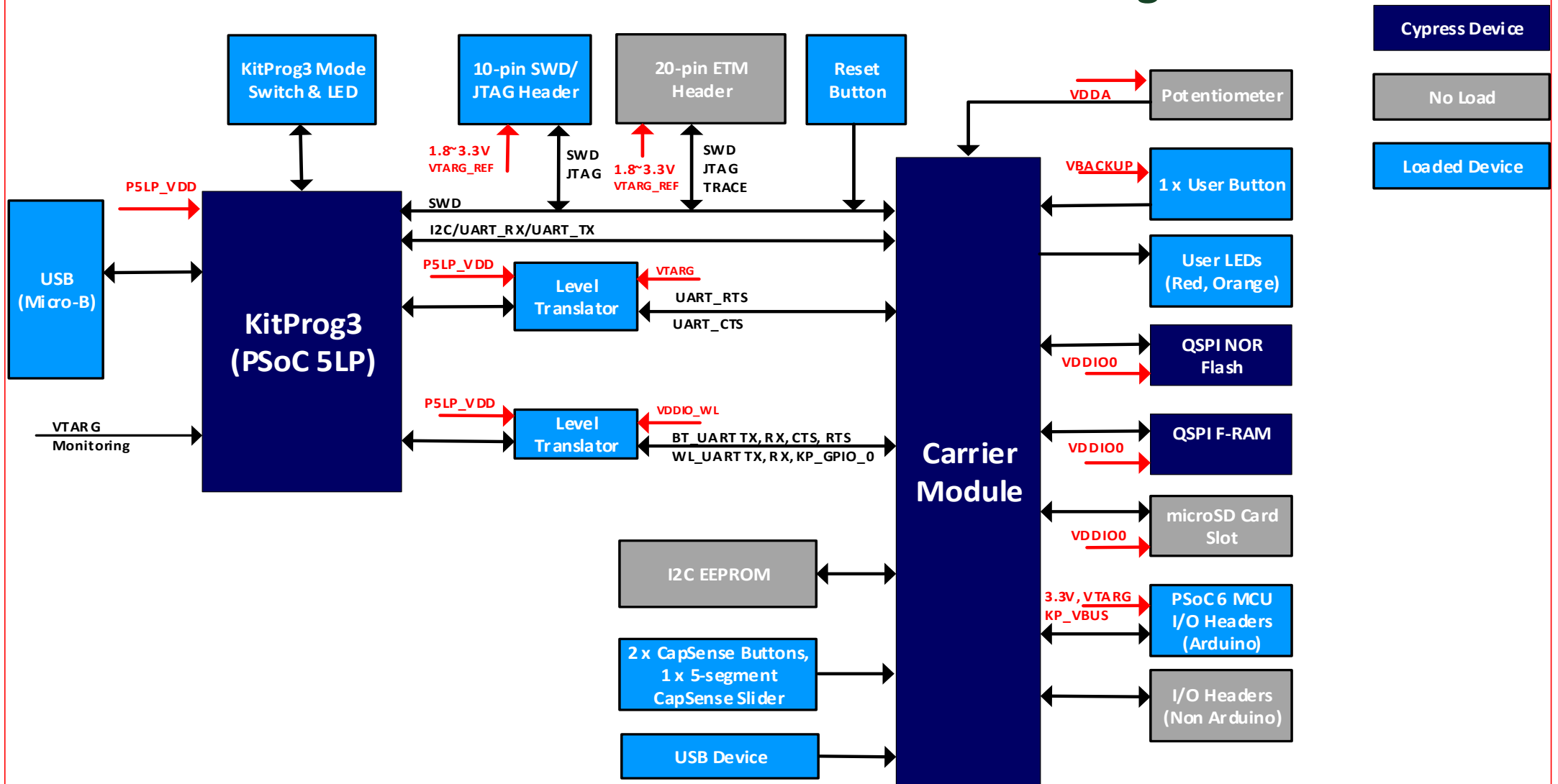
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SCH Title : CYW9-BASE-01 Pioneer Baseboard

Page Title : Title Page

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CYW9-BASE-01 Architecture Block Diagram



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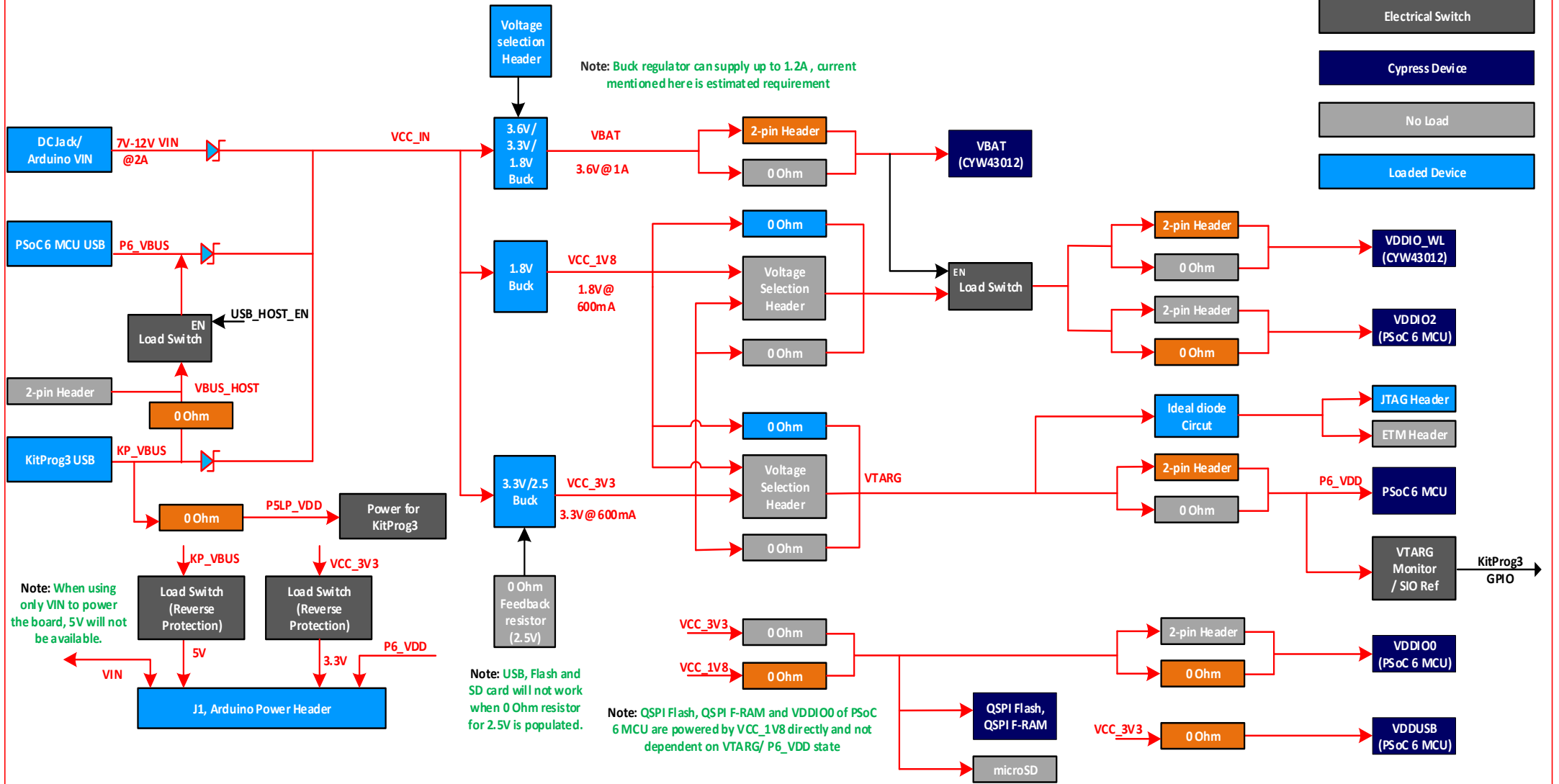
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SCH Title : CYW9-BASE-01 Pioneer Baseboard

Page Title : Functional Block Diagram

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Power Supply Section



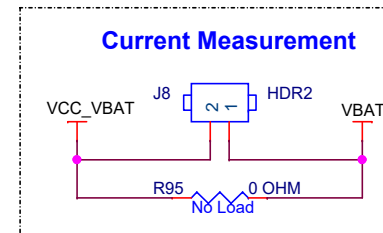
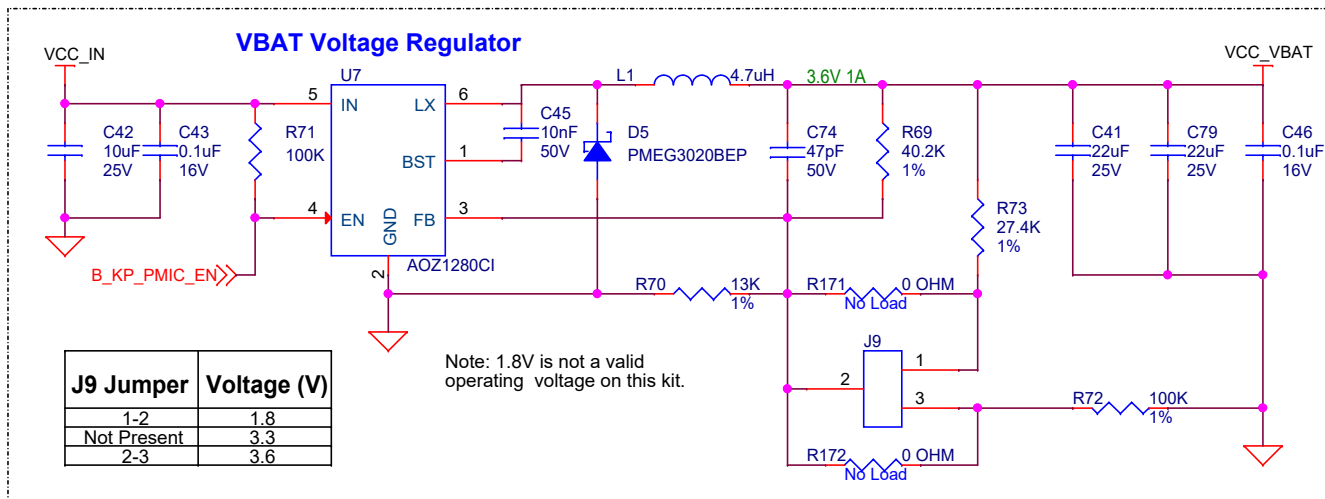
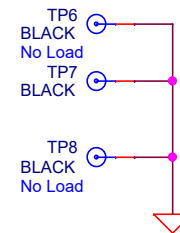
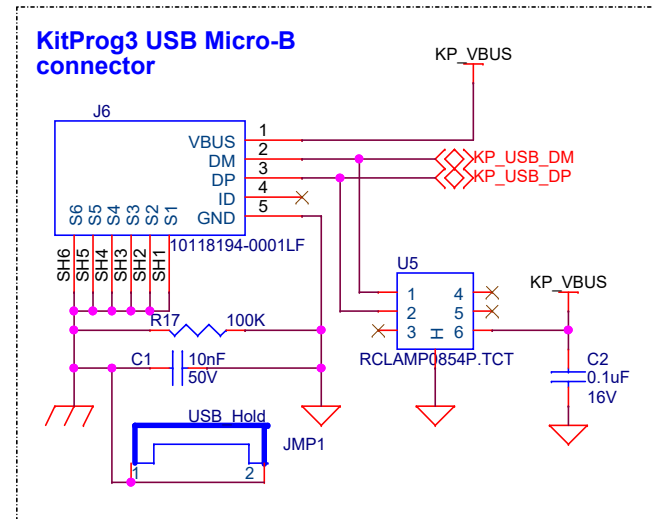
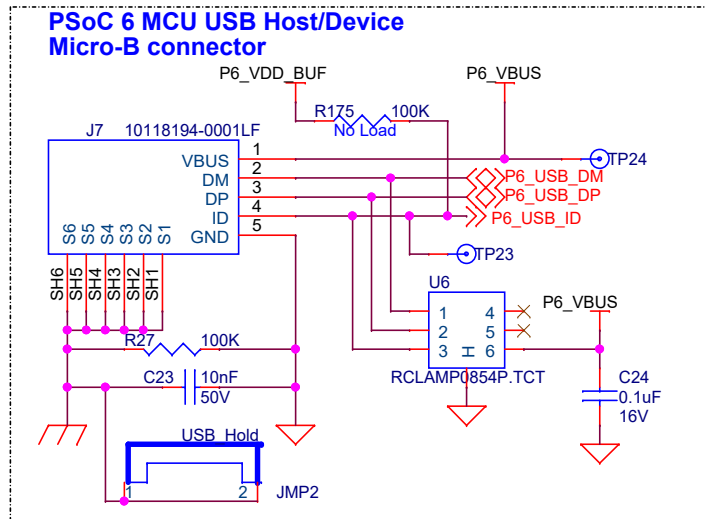
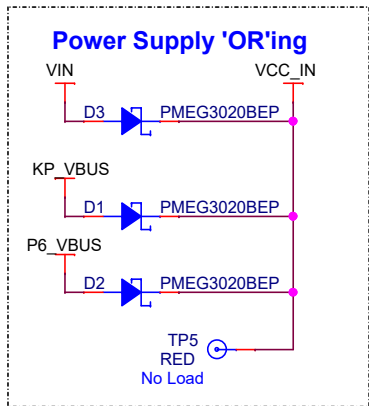
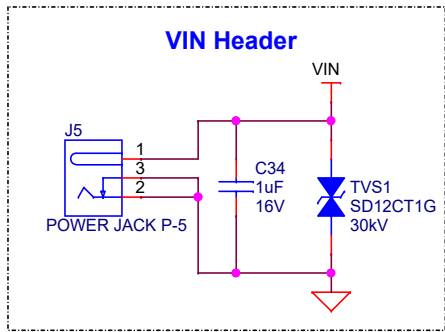
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Page Title : Power Supply Block Diagram

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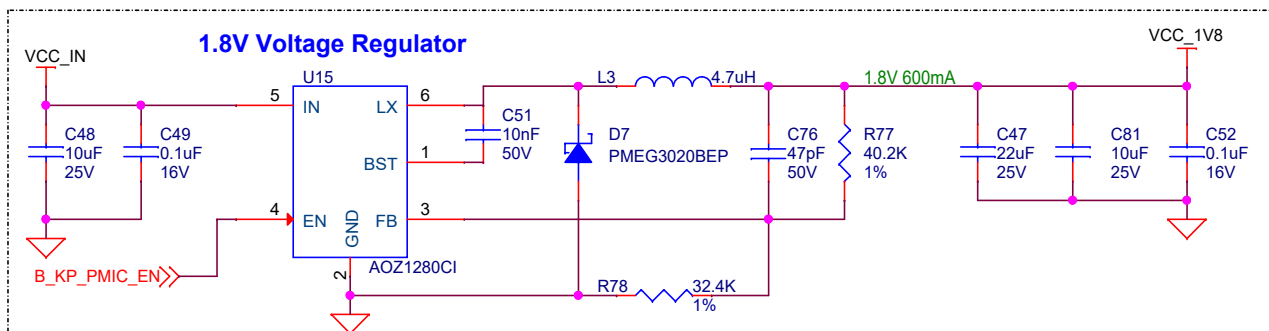
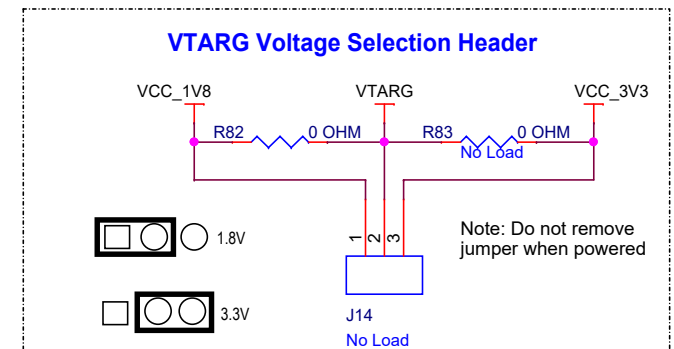
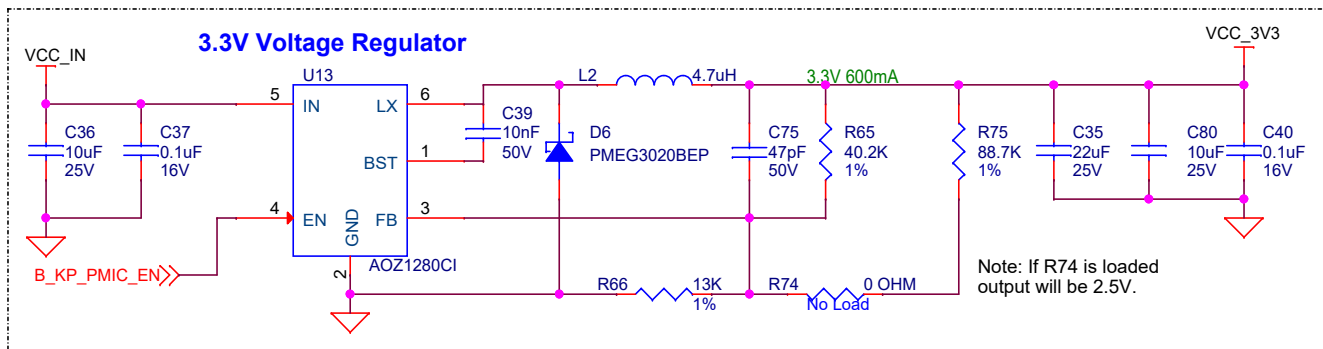
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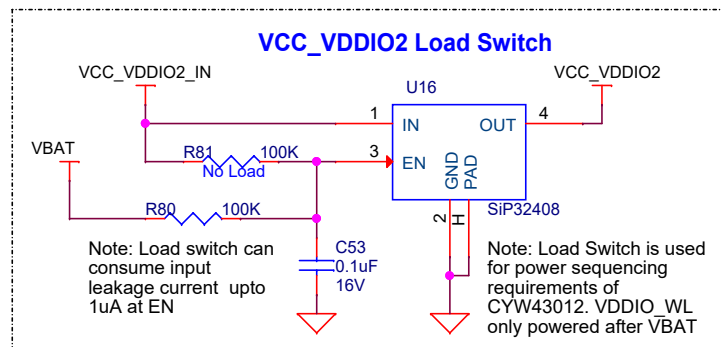
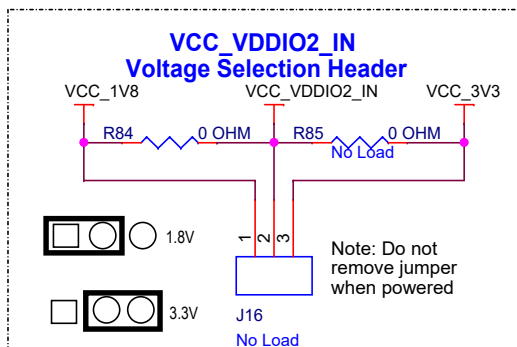
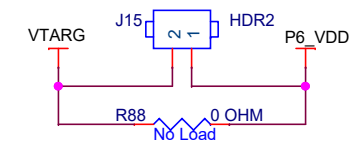
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Page Title : Power Supply (1)

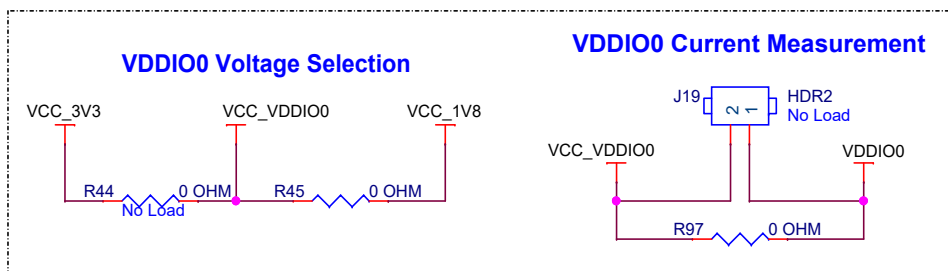
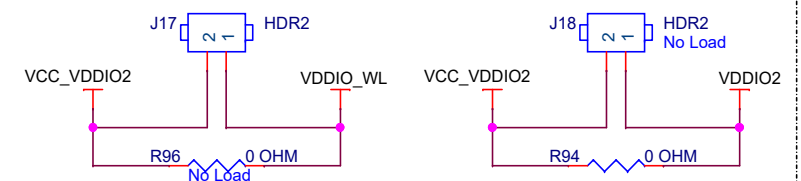
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P6_VDD Current Measurement



VDDIO_WL & VDDIO2 Current Measurement



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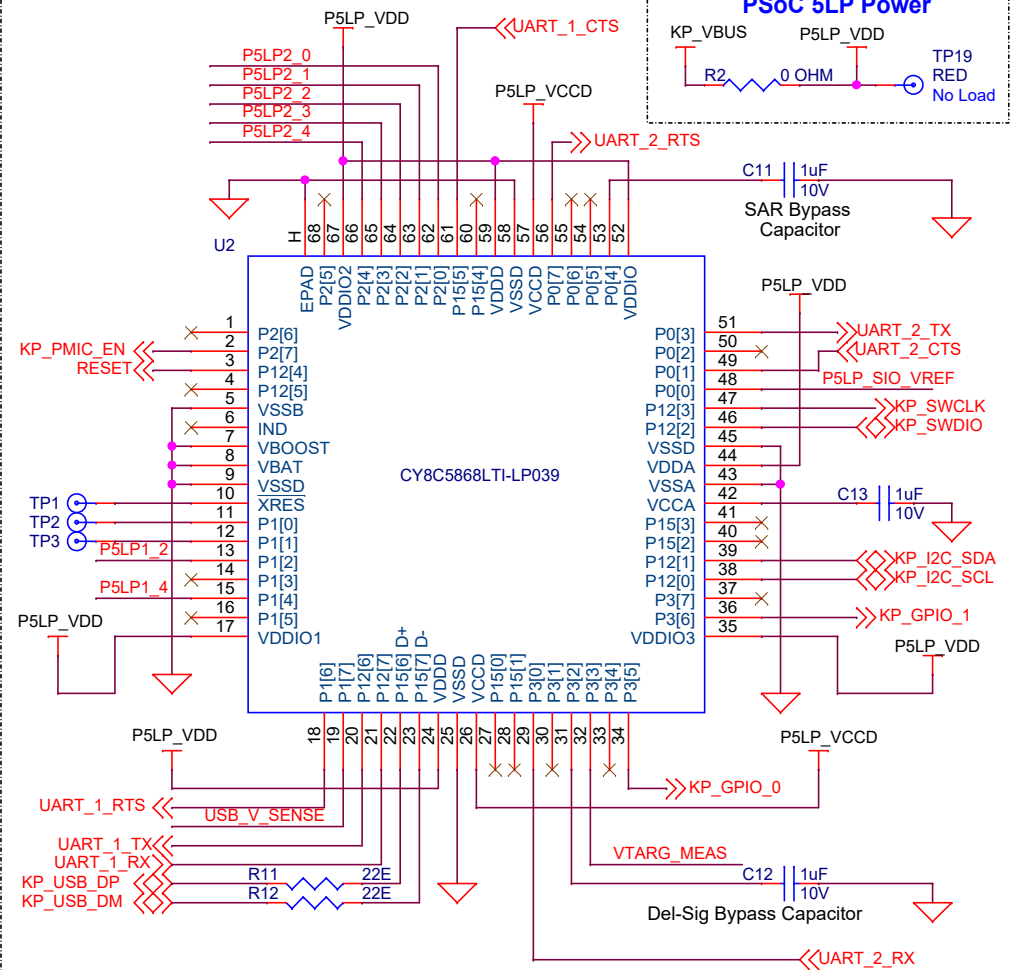
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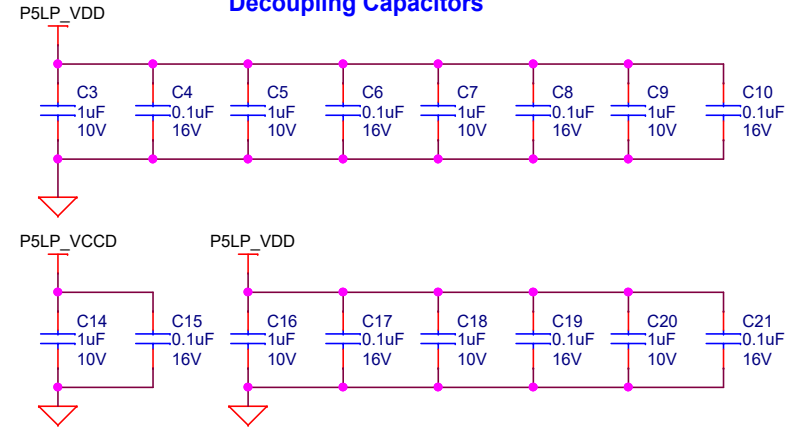
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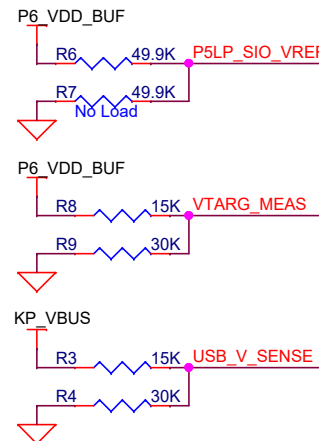
PSoC 5LP based KitProg3



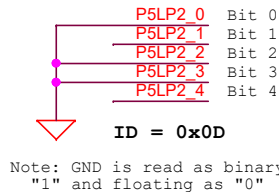
Decoupling Capacitors



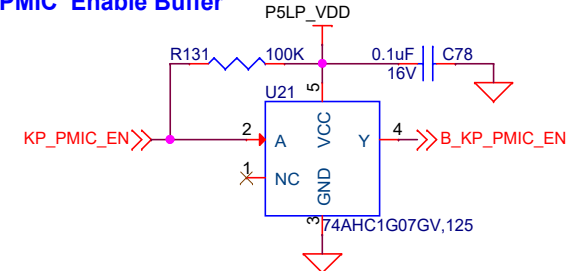
Voltage Monitoring



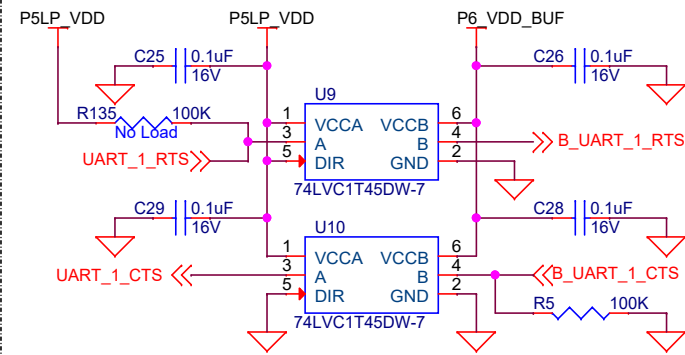
KitProg3 H/W Revision



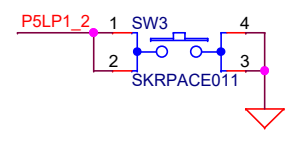
PMIC Enable Buffer



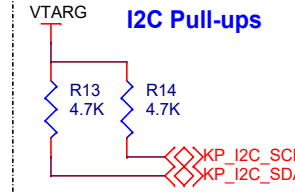
Primary UART H/W Flow Control Level Translator



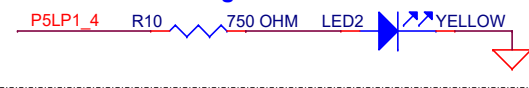
Mode Switch



I2C Pull-ups



KitProg3 Status LED



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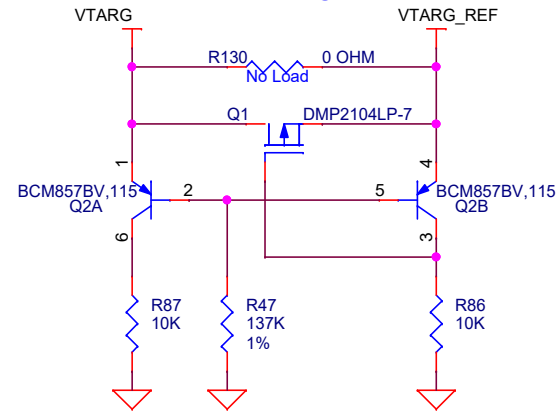
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Page Title : KitProg3 Controller

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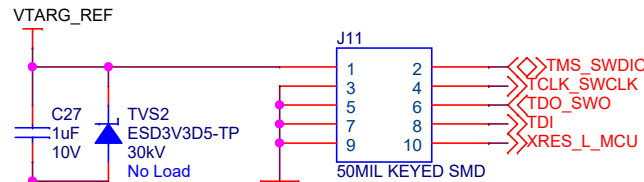
VTARG Reverse Voltage Protection



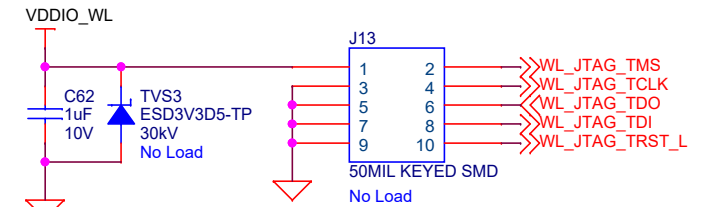
Note: VTARG_REF is only output voltage sense line for external debuggers. PSoC 6 can't be powered using external debugging headers J11 and J12

Note: If R130 is loaded and external power is used, make sure to remove 0 ohm R82 to prevent reverse voltage to on-board regulator

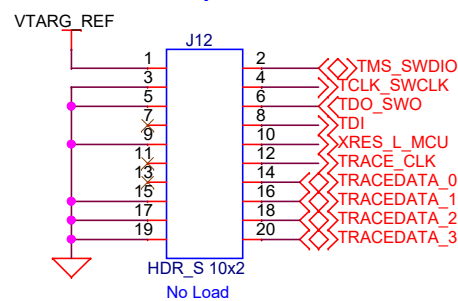
MCU 10-pin SWD/JTAG Header



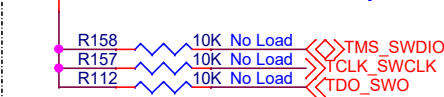
CYW43012 10-pin SWD/JTAG Header



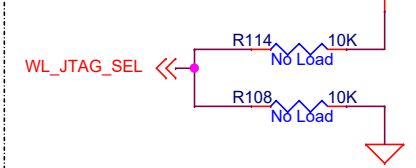
MCU 20-pin ETM Header



MCU JTAG Pull-ups



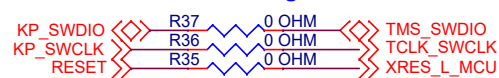
JTAG SEL Selection



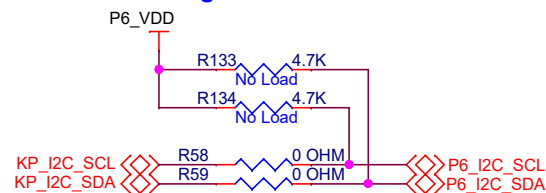
WL JTAG Pull-ups



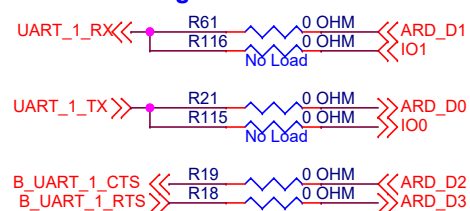
MCU SWD KitProg3 Interface



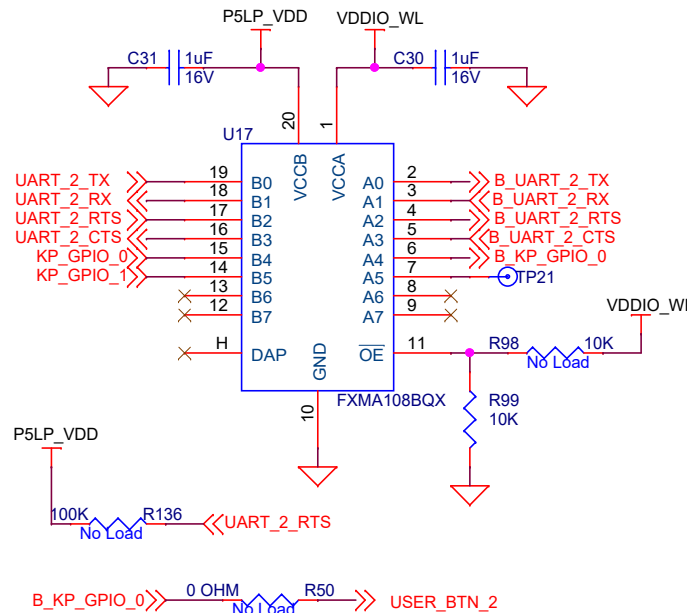
KitProg3 I2C Interface



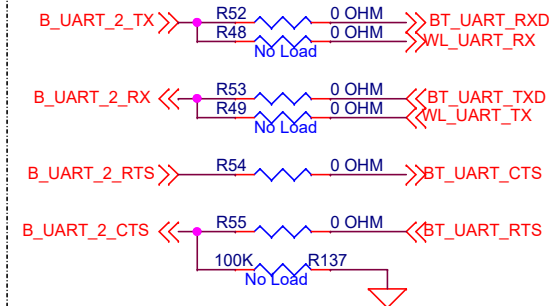
KitProg3 UART Interface



KitProg3 Level Translator for Secondary UART and GPIO



KitProg3 Secondary UART Multiplexing



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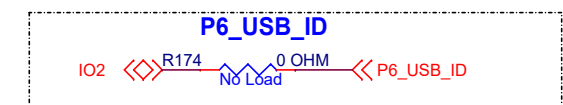
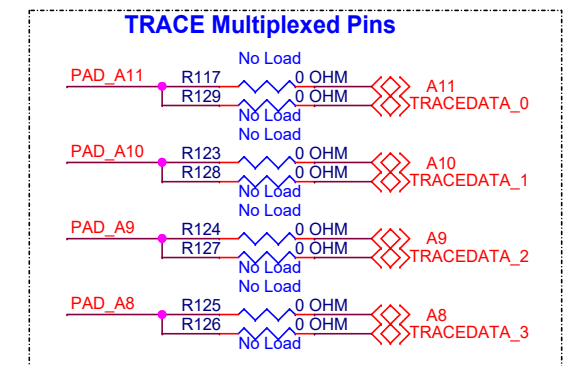
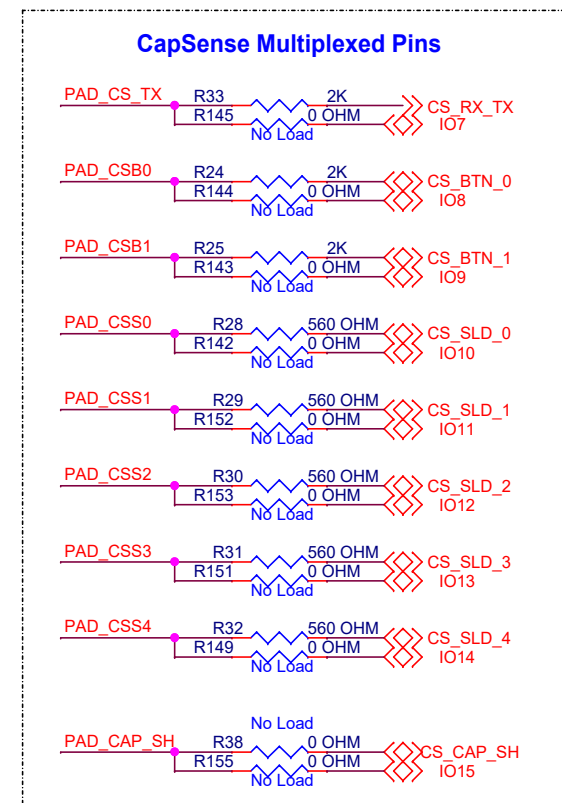
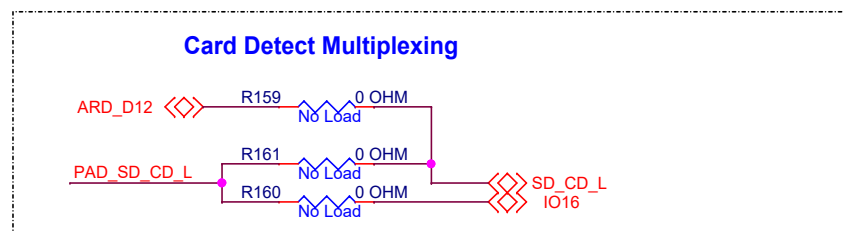
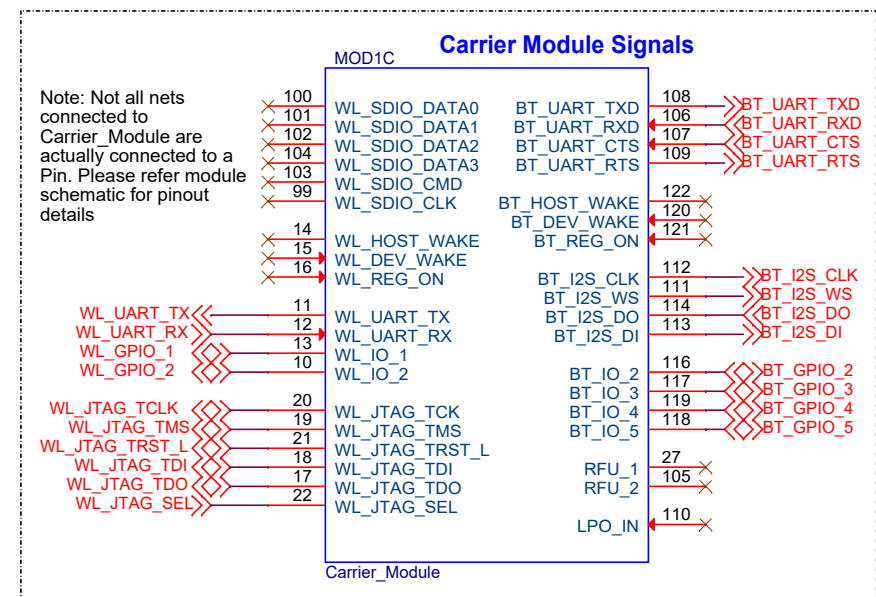
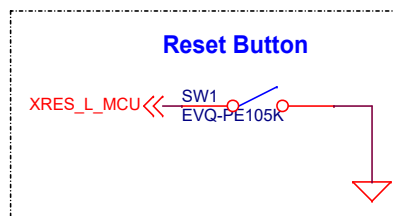
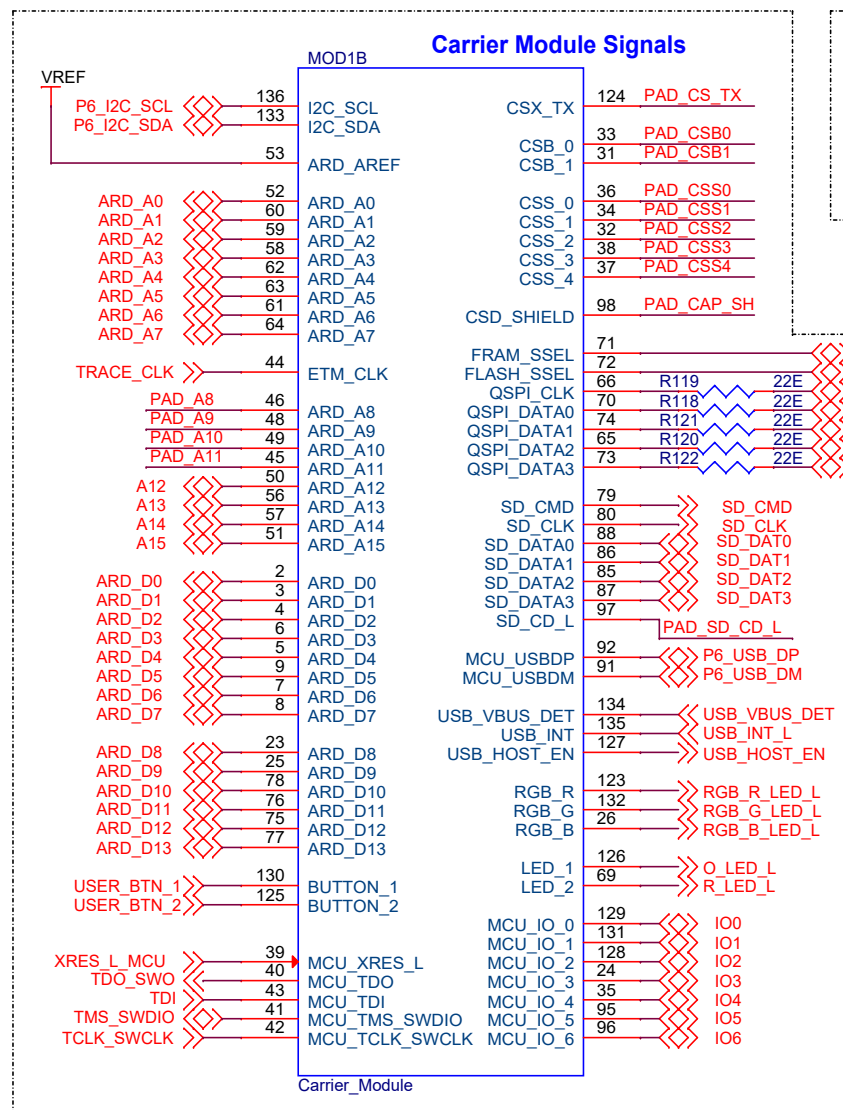
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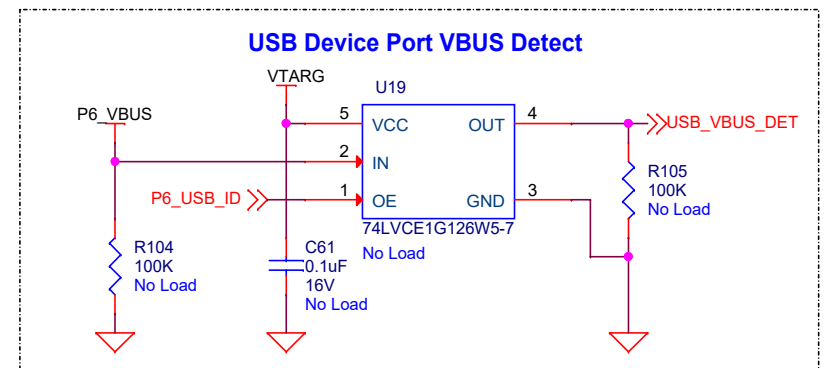
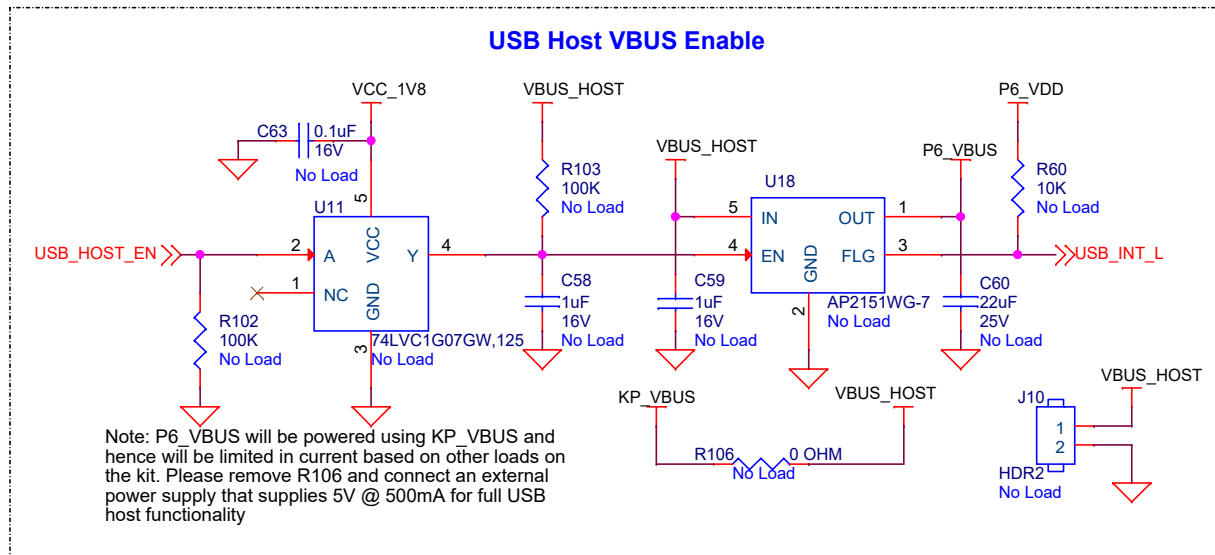
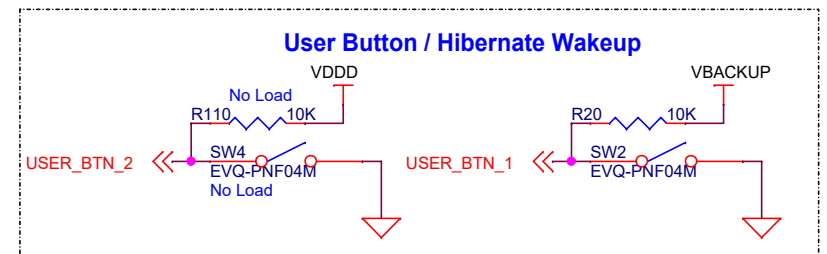
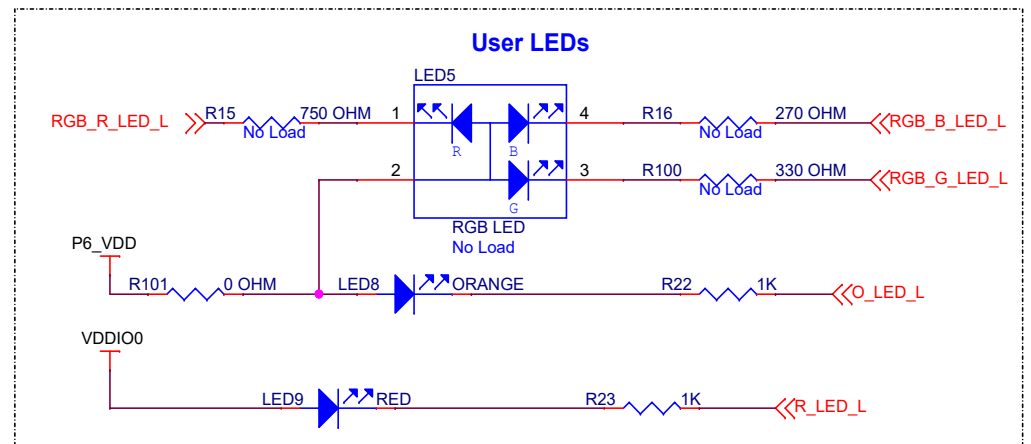
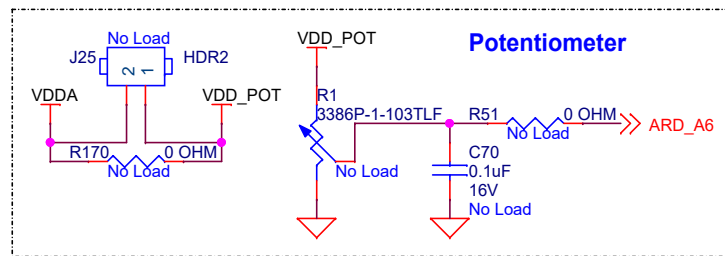
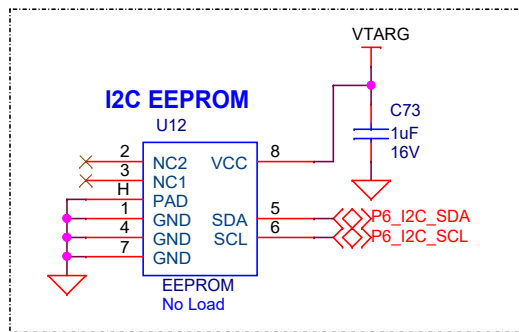
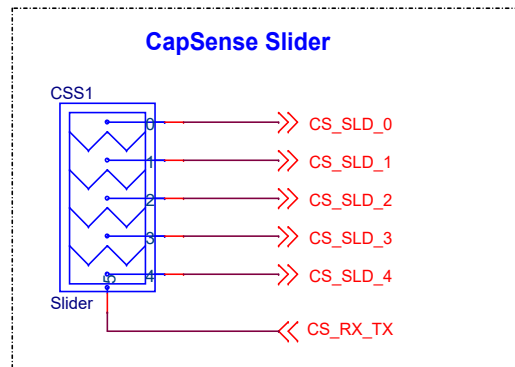
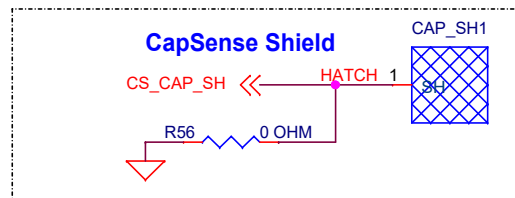
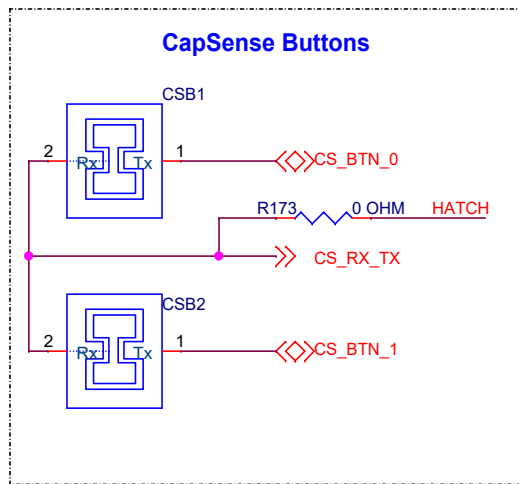
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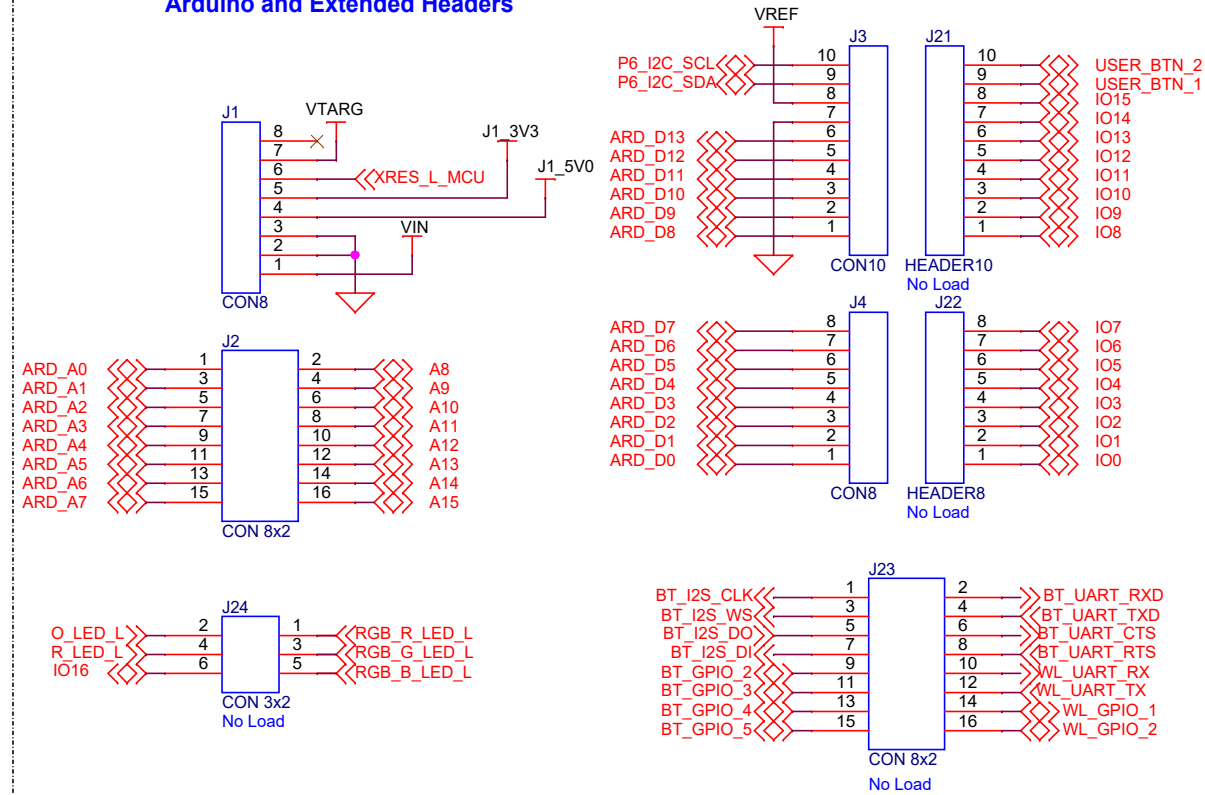
SCH Title : CYW9-BASE-01 Pioneer Baseboard

Page Title : Carrier Module Signals

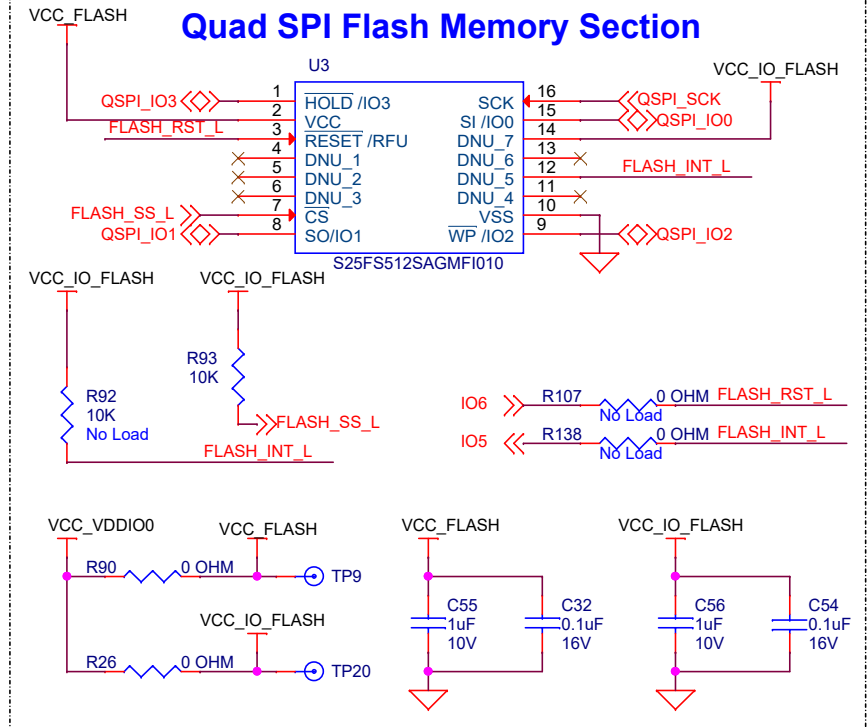
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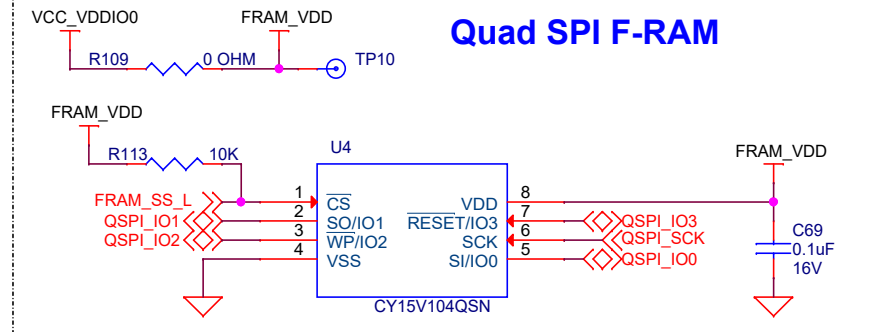
Arduino and Extended Headers



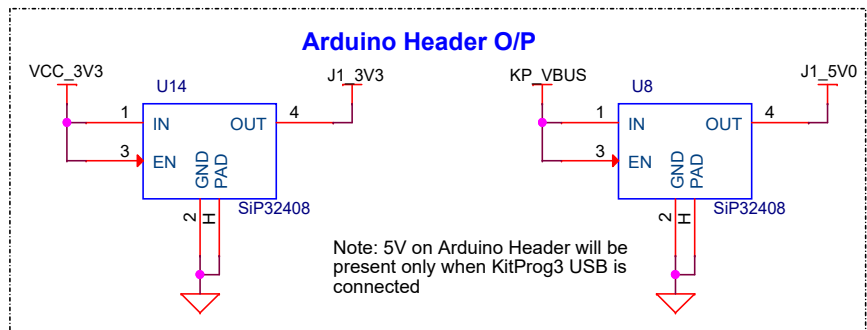
Quad SPI Flash Memory Section



Quad SPI F-RAM



Arduino Header O/P



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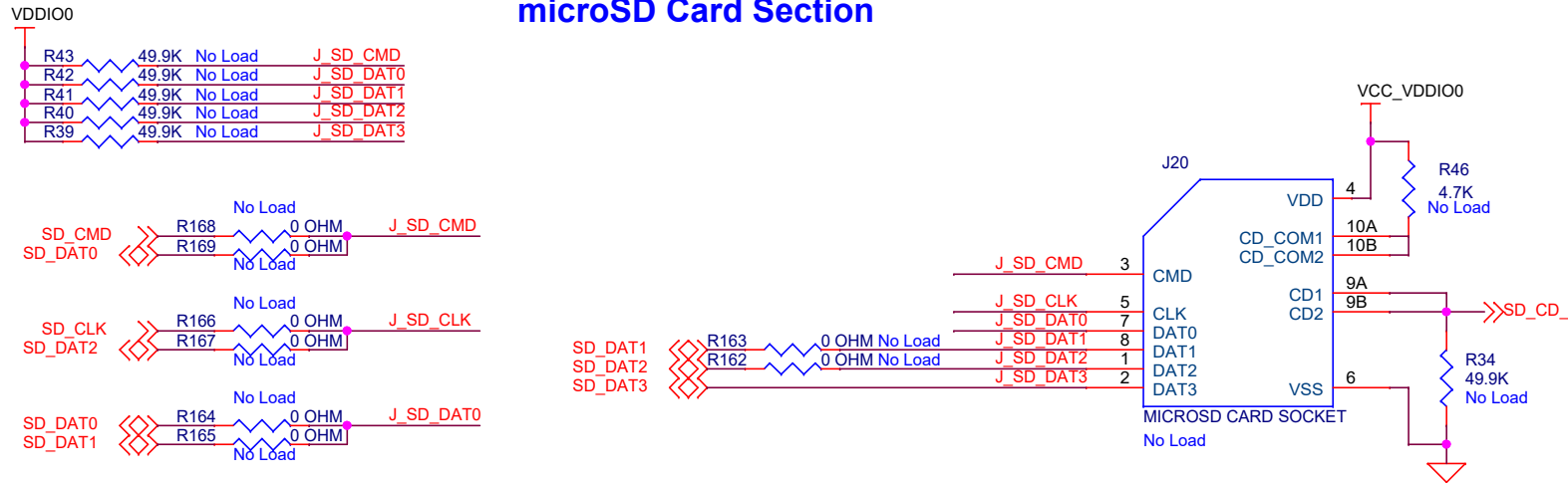
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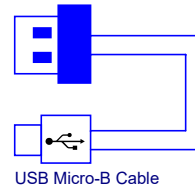
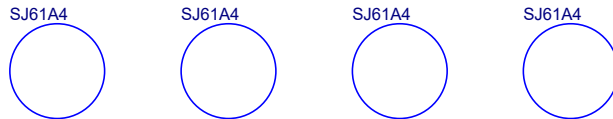
microSD Card Section



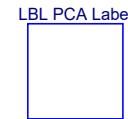
Note: Load R165, R167 and R169 and remove R162, R163, R164, R166 and R168 to change from SDHC to SPI interface for microSD card

Accessories

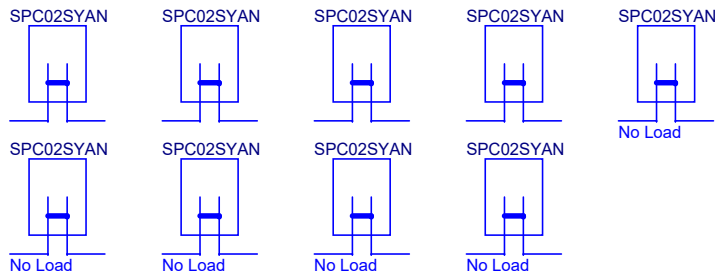
Cylindrical Bump-ons



PCBA label



Jumper Shunts



Acrylic Overlay

Overlay

Color: Clear, Transparent
 Finish: Matt

Dimensions:
 45.21mm - length
 36.93mm - height
 1mm - thickness



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Page Title : microSD card & Accessories

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REVISION HISTORY

REV	DESCRIPTION OF CHANGE	DATE
01	Initial Internal Release	2019/05/13
02	Internal Release	2019/05/21
03	Internal Release	2019/07/23
04	Internal Release	2019/08/08
05	Initial Release	2019/09/18



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