Product brief

FPGA mezzanine card for RadHard QDR®-II+ SRAM

CYDK-QDRII-NODT

Mezzanine card for testing the highest performing memory for space applications

This devkit board has been developed to work with the Infineon RadHard 72 & 144-Mbit QDR®-II+ SRAMs. The board is developed and tested to work with FPGA carrier cards through the FMC-HPC connector.

Key features

- 250 MHz frequency
- 72/144-Mbit density
- x18 or x36 data bus width
- Core $V_{DD} = 1.8$ V ($\pm 0.1$ V)
- I/O $V_{DDQ} = 1.4$ V to $V_{DD}$
- Socket for 165-pin CCGA (21 x 25 x 2.83 mm)
- FPGA mezzanine connector (FMC-HPC)
- Compatible with Microsemi Microchip RTG4- DEV-KIT
- Compatible with Alpha Data ADA-SDEV-KIT2

Advantages

- Supports all Infineon RadHard 72 & 144-Mbit QDR®-II+ SRAMs with onboard termination
- Only solution in the market that provides a FPGA mezzanine card supporting the full frequency of the Infineon RadHard QDR®-II+ SRAM when used with Infineon QDR®-II+ FPGA controller

For further details

To learn more about RH QDR®-II+ and all Infineon RH products, visit www.cypress.com/aero
To purchase RH QDR®-II+ parts, visit www.cypress.com/buyonline

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