Product brief

Logic analyzer mezzanine card for RadHard QDR®-II+ SRAM debug

CYDK-QDRII-LA

Mezzanine card for connecting a logic analyzer to debug the RadHard QDR®-II+ SRAM interface to the CYDK-QDRII-NODT

This LA card has been developed to work with the Infineon CYDK-QDRII-NODT Mezzanine card to provide a path to debug the interface between the FPGA and RadHard QDR®-II+ SRAM. The board is developed and tested to work with FPGA carrier cards through the FMC-HPC connector.

Key features

› 250 MHz frequency
› 72/144-Mbit density
› x18 or x36 data bus width
› Brings all SRAM interface signals to micitors
› Provides standard 40 pin Mictor connectors for LA single ended probes

Product overview

Advantages

› Supports all Infineon RadHard 72 & 144-Mbit QDR®-II+ SRAMs
› Ability to observe all QDR®-II+ SRAM signals at the same time on the logic analyzer

Additional information

For further information on technologies, our products, the application of our products, delivery terms and conditions and/or prices, please contact your nearest Infineon Technologies office (www.infineon.com).

Warnings

Due to technical requirements, our products may contain dangerous substances. For information on the types in question, please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by us in a written document signed by authorized representatives of Infineon Technologies, our products may not be used in any life-endangering applications, including but not limited to medical, nuclear, military, life-critical or any other applications where a failure of the product or any consequences of the use thereof can result in personal injury.

For further details

To learn more about RH QDR®-II+ and all Infineon RH products, visit www.infineon.com/hirelmemory
To purchase RH QDR®-II+ parts, visit www.cypress.com/buyonline

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