

## CY8CPROTO-064B0S3 PSoC 64 Secure Boot Prototyping Kit with 512K Flash

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### Drawing Numbers

PCBA	121-60577-01
PCB	600-60577-01
FAB DRW	610-60577-01
ASSY DRW	620-60577-01
SCH DRW	630-60577-01



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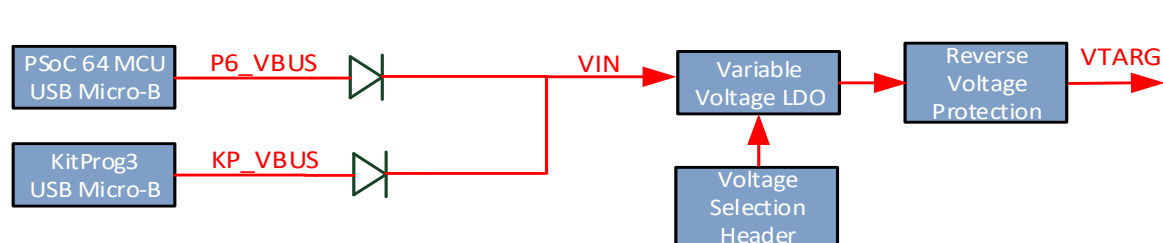
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**Page Title : Title Page**

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## Power Section

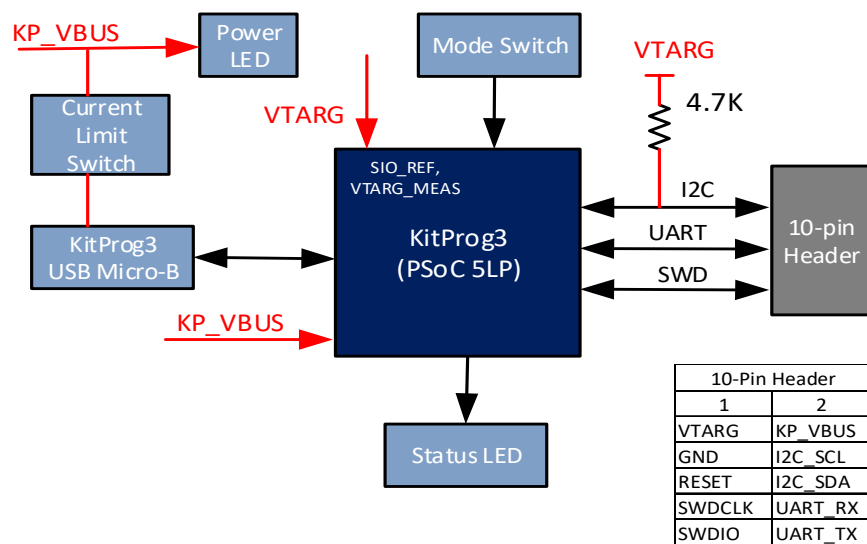


Cypress Parts

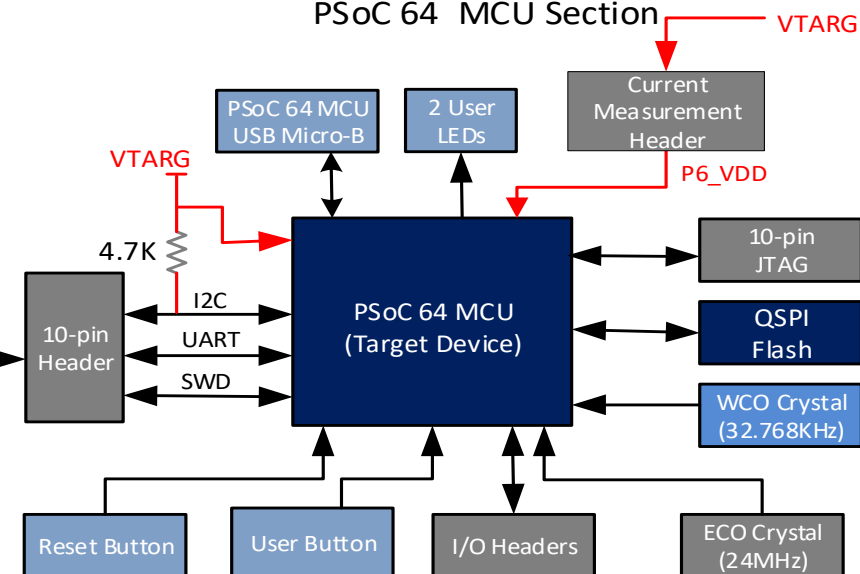
Loaded Parts

No Load Parts

## KitProg3 Section



## PSoC 64 MCU Section



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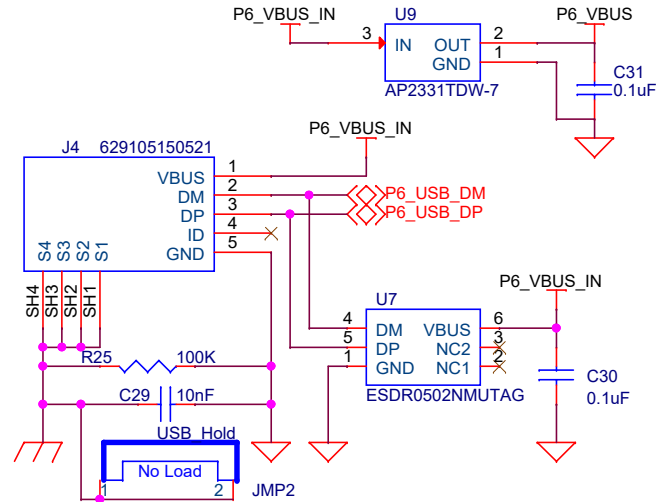
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**Page Title : Block Diagram**

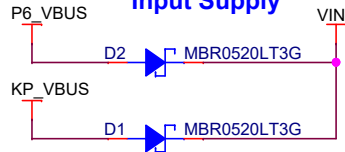
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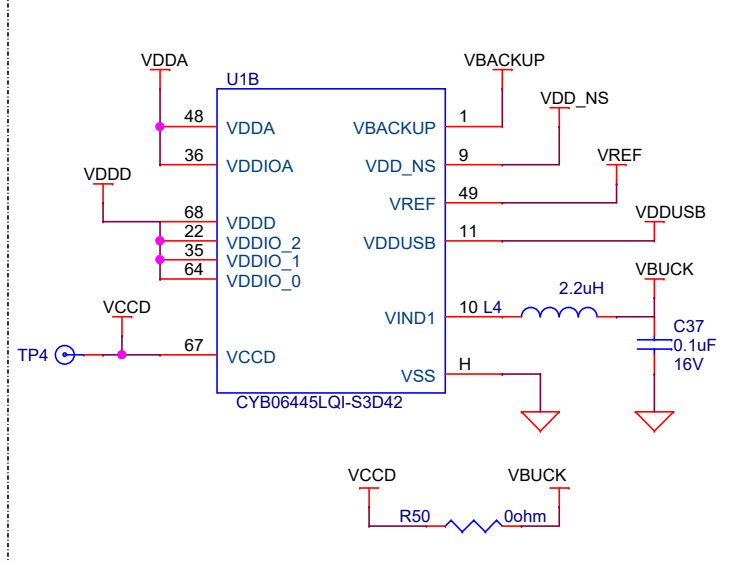
## PSoC 64 MCU USB Device Micro-B Connector



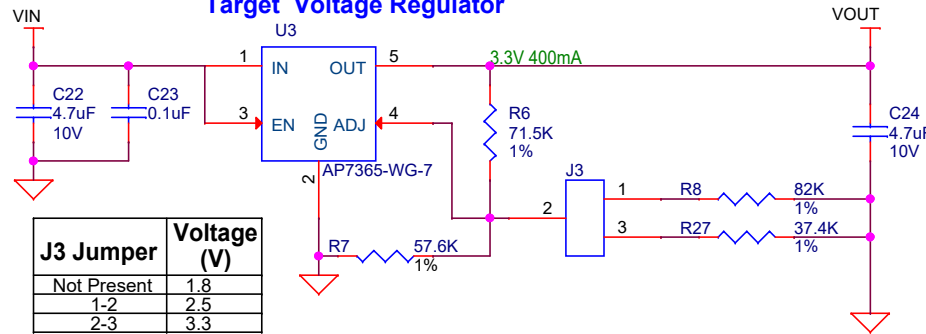
## Input Supply



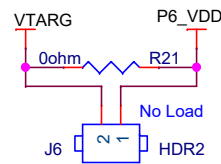
## PSoC 64 MCU Power



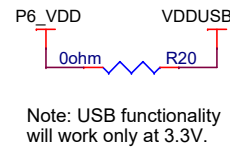
## Target Voltage Regulator



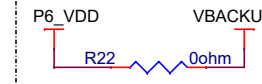
## Current Measurement Header



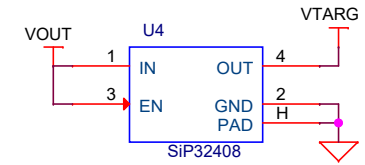
## PSoC 64 MCU USB Voltage



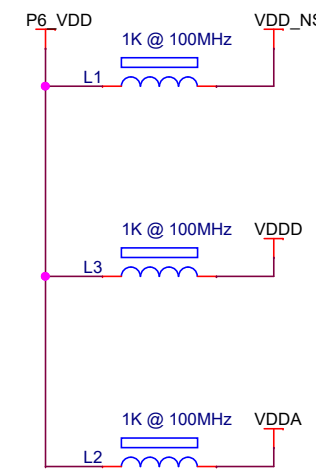
## VBACKUP



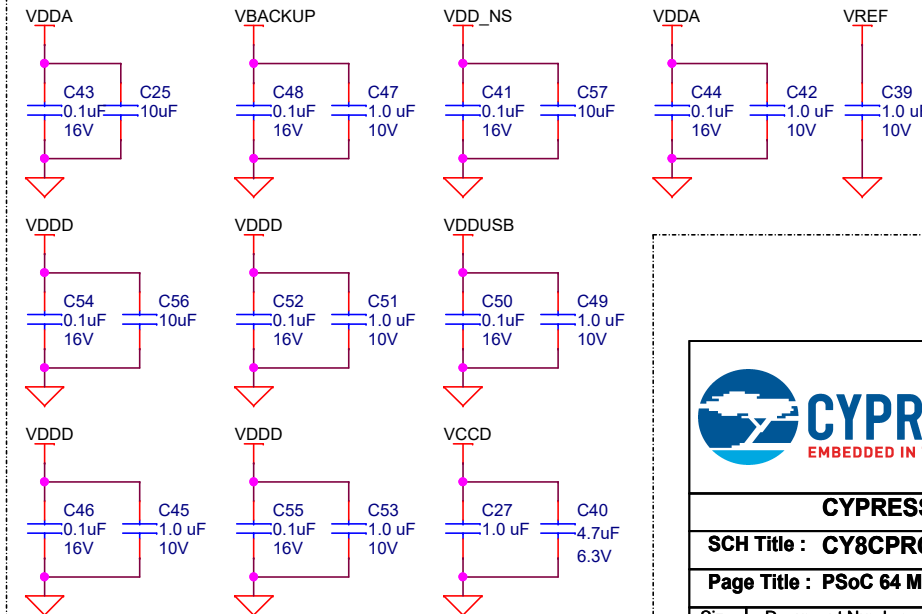
## Reverse Voltage Protection



## PSoC 64 MCU Power IN



## PSoC 64 MCU Decoupling Capacitors



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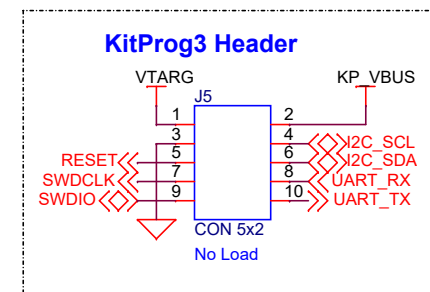
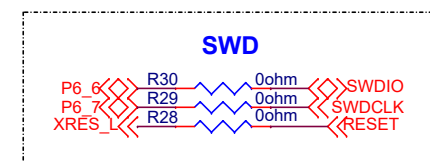
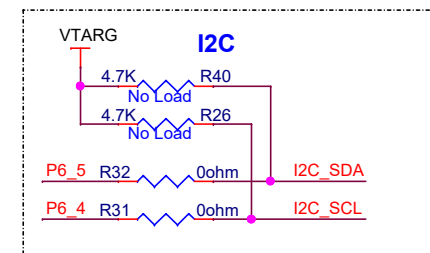
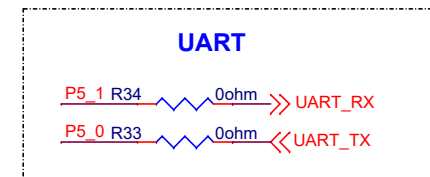
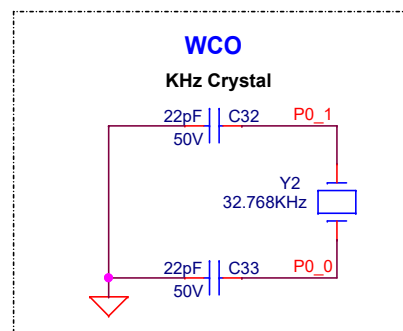
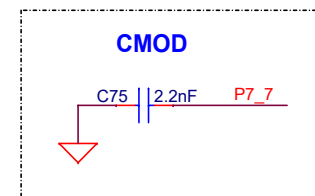
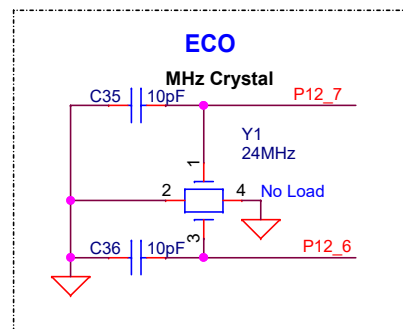
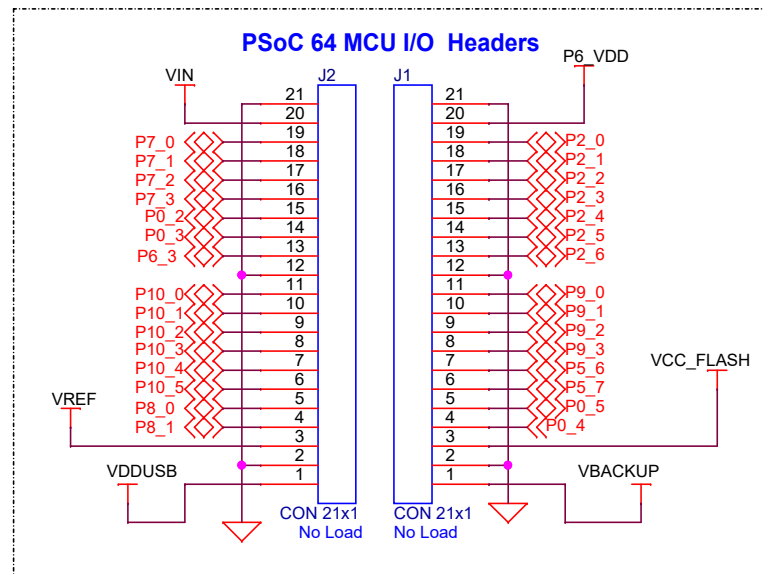
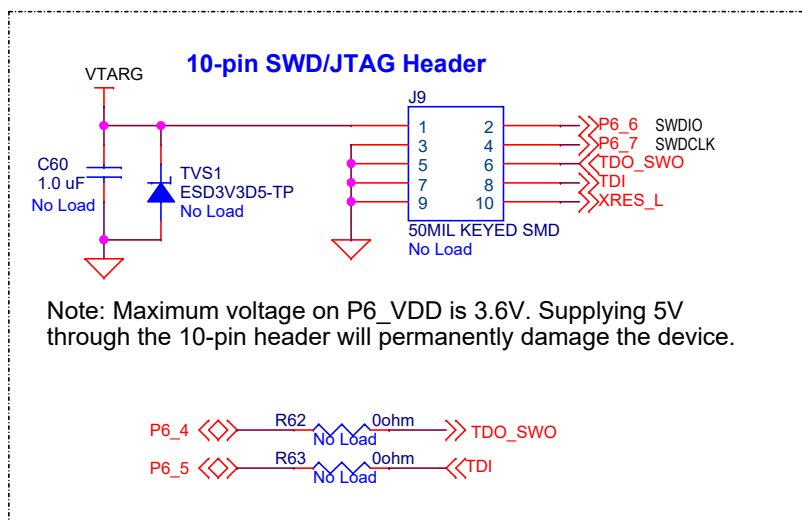
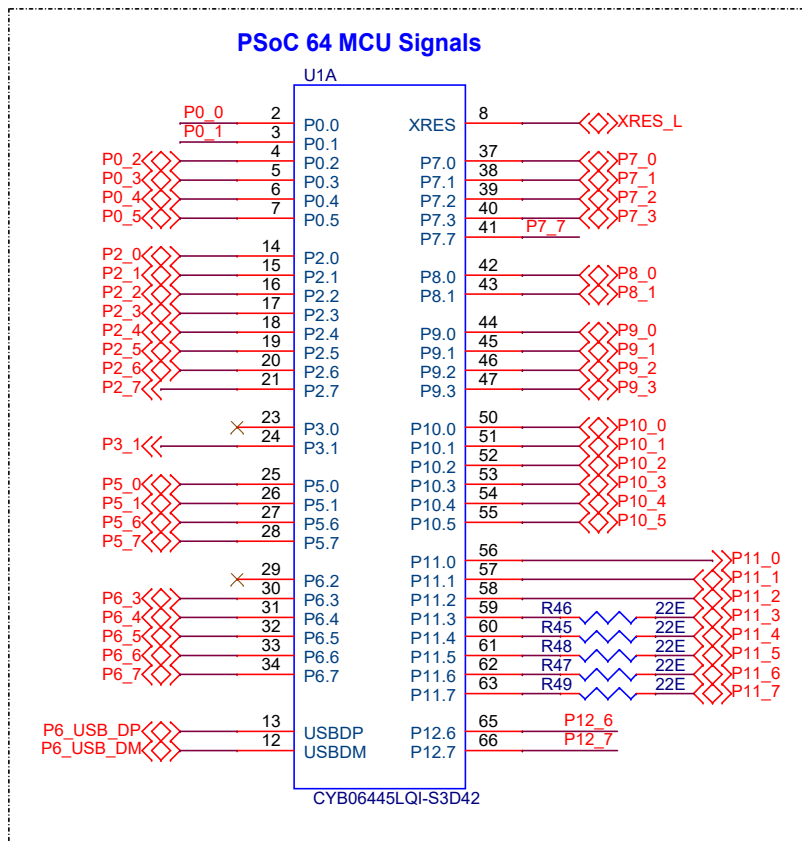
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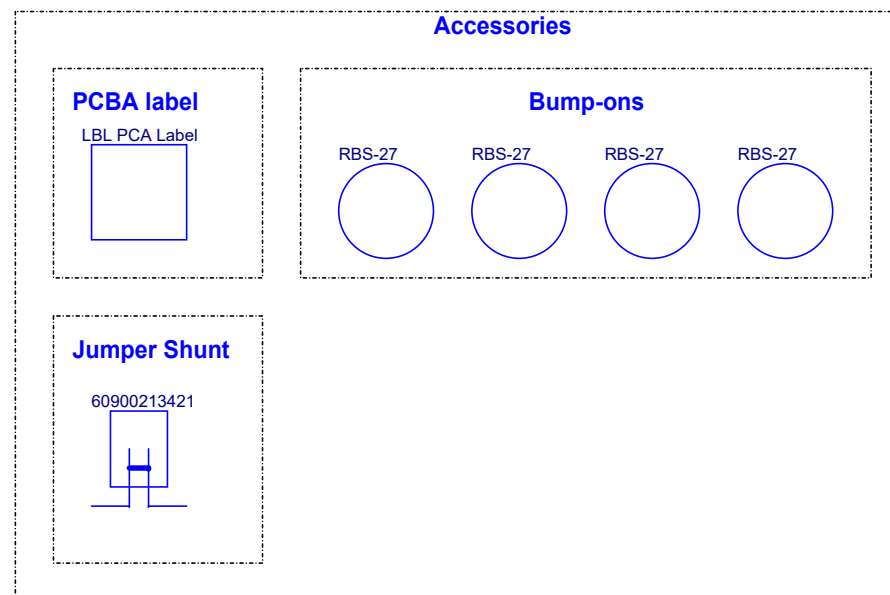
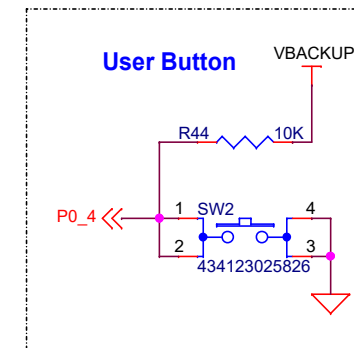
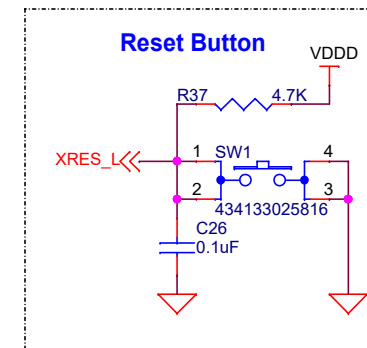
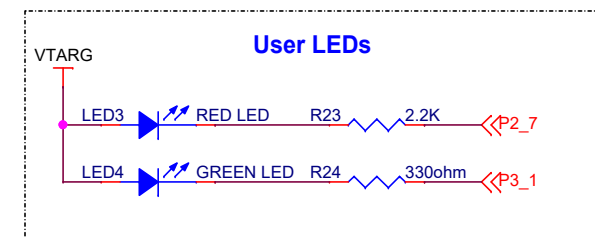
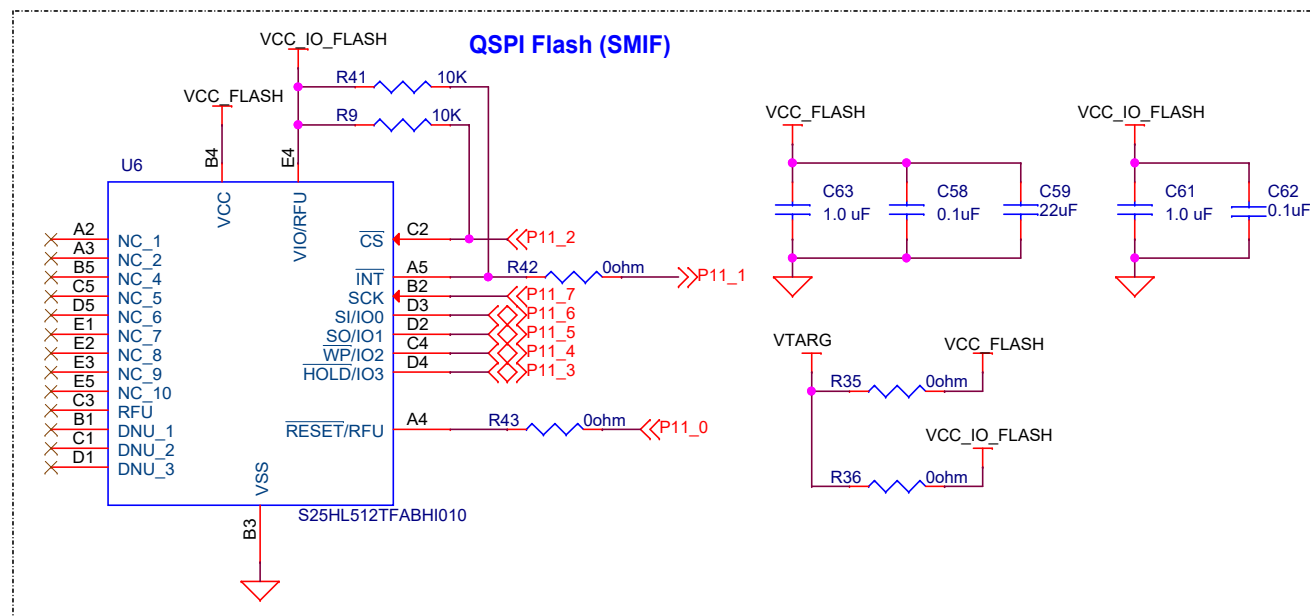
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REV	DESCRIPTION OF CHANGE	DATE
01	Initial Internal Release	2020/01/14
02	Initial Release	2020/01/20



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