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Continuity of document content

The fact that Infineon offers the following product as part of the Infineon product portfolio does not lead to any changes to this document. Future revisions will occur when appropriate, and any changes will be set out on the document history page.

Continuity of ordering part numbers

Infineon continues to support existing part numbers. Please continue to use the ordering part numbers listed in the datasheet for ordering.

CY8CPROTO-064B0S3 PSoC™ 64 “Secure Boot” prototyping kit quick start guide

Pinout description

KitProg3

KitProg3
USB connector (J8)

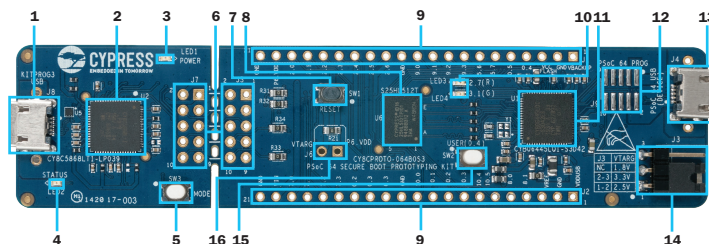
PSoC 64 SWD/JTAG
programming headers
(J7, J5)

PSoC 64 section

GND
VIN
P7_0
P7_1
P7_2
P7_3
P0_2
P0_3
P6_3
P10_0
P10_1
P10_2
P10_3
P10_4
P10_5
P8_0
P8_1
VREF
GND
VDDUSB
PSoC 64 I/O (J2)

GND
P6_VDD
P2_0
P2_1
P2_2
P2_3
P2_4
P2_5
P2_6
GND
P9_0
P9_1
P9_2
P9_3
P5_6
P5_7
P0_5
P0_4
VCC_FLASH
GND
VBACKUP
PSoC 64 I/O (J1)

- Claim the Root-of-Trust (RoT) of the PSoC 64 device. This provisions your unique keys and policies onto the device.
- See the kit guide for provisioning and programming this MCU for evaluation.



1. KitProg3 USB connector (J8)
2. KitProg3 (PSoC 5LP) programmer and debugger (CY8C5868LTI-LP039, U2)
3. Power LED (LED1)
4. KitProg3 status LED (LED2)
5. KitProg3 programming mode selection button (SW3)
6. KitProg3 10-pin interface headers (J7, J5)*
7. Reset button (SW1)
8. 512-Mbit Sempert™ NOR flash memory (S25HL512T, U6)
9. PSoC 64 I/O headers (J1, J2)*
10. PSoC 64 user LEDs (LED3, LED4)
11. PSoC 64 chip (CYB06445LQI-S3D42, U1)
12. PSoC 64 program and debug header (J9)*
13. PSoC 64 USB device connector (J4)
14. Power selection jumper (J3)
15. PSoC 64 user button (SW2)
16. PSoC 64 current measurement header (J6)*

*Not loaded by default

For more information, please visit: www.cypress.com/CY8CPROTO-064B0S3



PSoC Rocks!





CY8CPROTO-064B0S3

PSOC™ 64 “Secure Boot” prototyping kit

