CY8CPROTO-062S3-4343W PSoC® 62S3 Wi-Fi BT PROTOTYPING KIT QUICK START GUIDE

Power selection jumper (J3)

KitProg3 USB connector (J8)

Reset button (SW1)
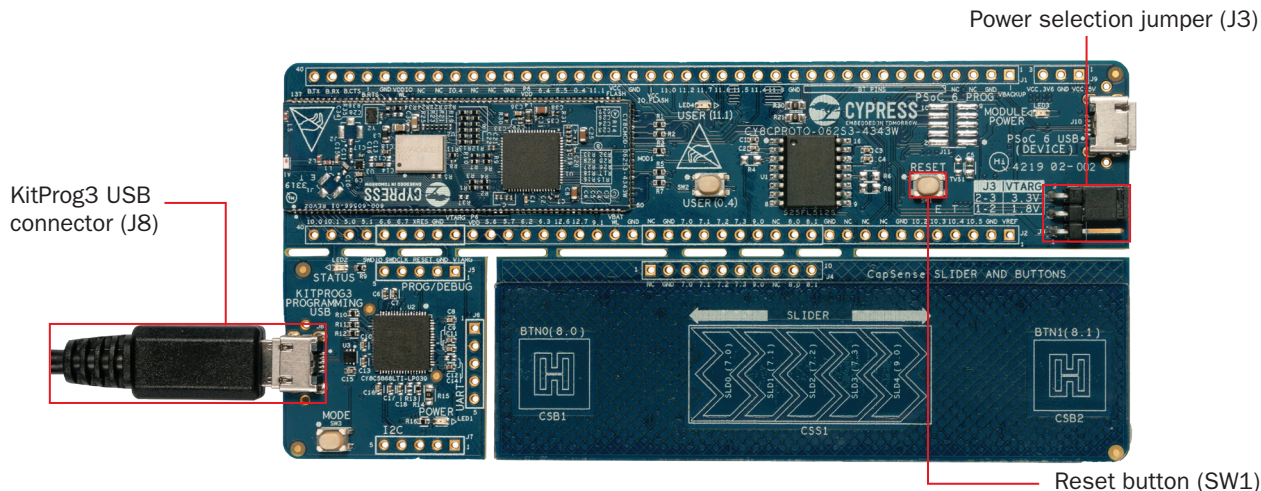
Before starting, ensure you have the following.

- PC with USB port
- UART terminal software such as Tera Term or Minicom

1) Visit the kit website to download and install the required software.

2) Ensure the jumper J3 is at position 2-3 to select 3.3V.

3) Connect the KitProg3 USB connector (J8) to your PC.

4) Wait for the drivers installation to complete.

5) Open the UART terminal software and connect to the KitProg3. USB-UART COM port with the settings – Baud rate: 115200, Data: 8 bit, Parity: None, Stop bit: 1 bit, Flow control: None.

6) Press the RESET switch (SW1) on the device.

7) Follow the instructions displayed on the UART terminal to use the pre-programmed code example.

   Refer to the Kit Guide available at the kit website for details.

For more information, please visit: **www.cypress.com/CY8CPROTO-062S3-4343W**

Contains Model: Type1DX
FCC ID: VPYLB1DX
IC: 772C-LB1DX
R 001-P00840

WÜRTH ELEKTRONIK

CYPRESS
EMBEDDED IN TOMORROW™

PSoC Rocks!

PEEL
HERE