

## CY8CPROTO-062-4343W PSoC 6 Wi-Fi BT Prototyping Kit

CONTENTS	
PAGE	DESCRIPTION
01	Title & Table of Contents
02	Block Diagram
03	KitProg3
04	PSoC 6 Module Power
05	PSoC 6 Module Signals
06	On-board Accessories
07	Accessories
08	Revision History

### Drawing Numbers

PCBA	121-60537-01
PCB	600-60537-01
FAB DRW	610-60537-01
ASSY DRW	620-60537-01
SCH DRW	630-60537-01



**CYPRESS**  
EMBEDDED IN TOMORROW™

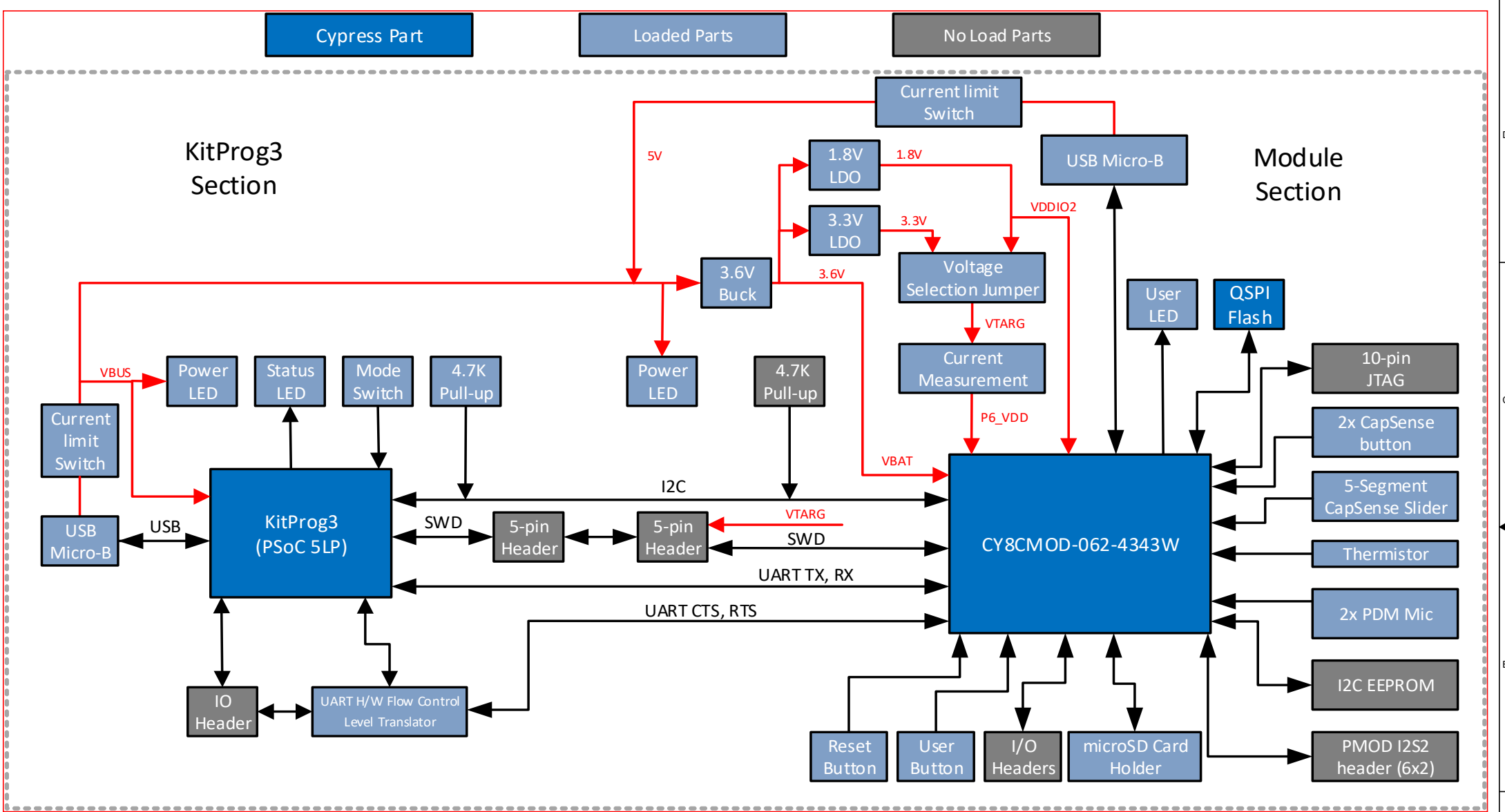
CYPRESS SEMICONDUCTOR  
198 CHAMPION COURT  
SAN JOSE, CA 95134  
(408) 943-2600

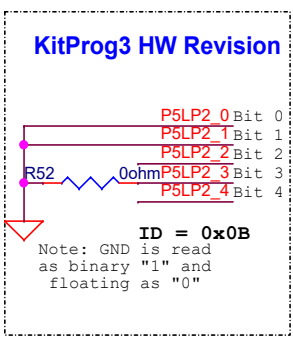
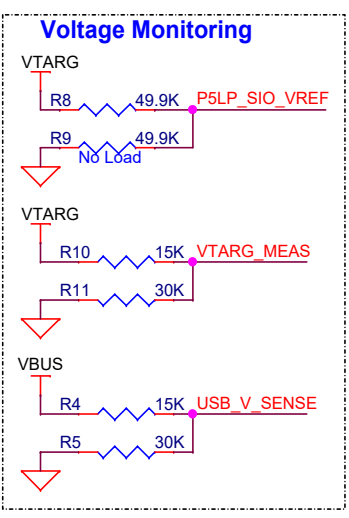
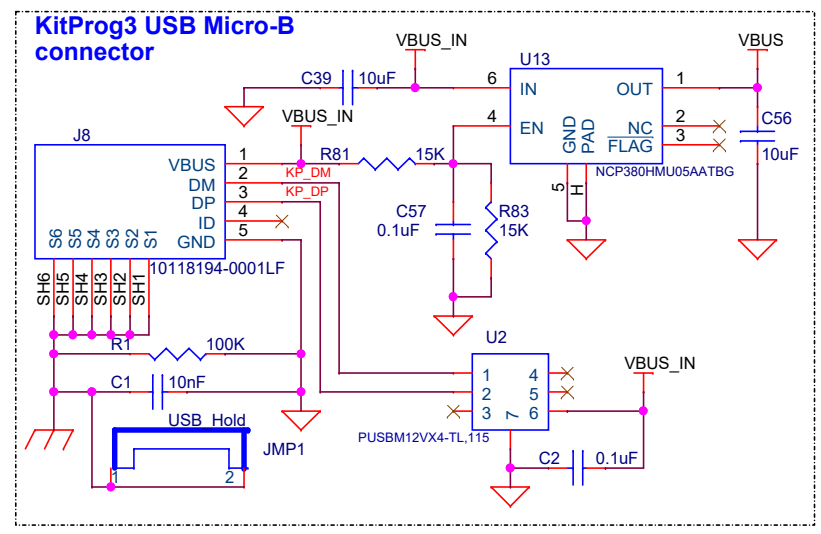
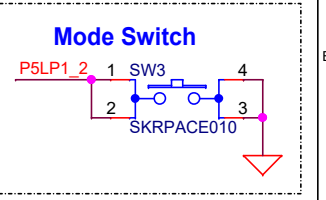
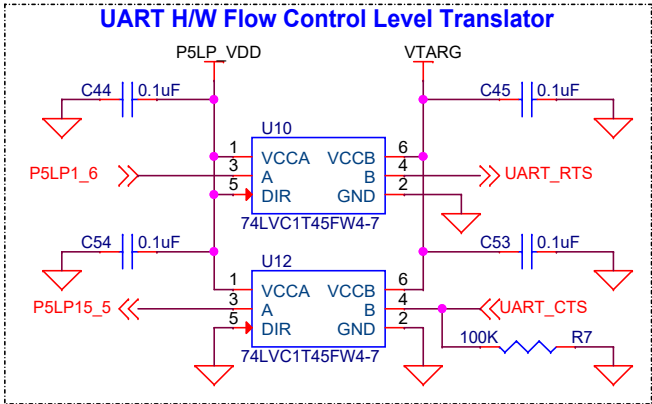
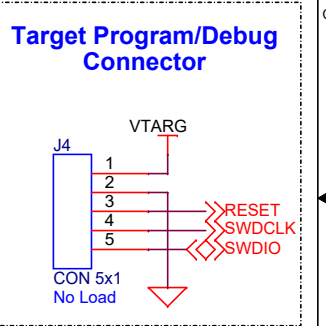
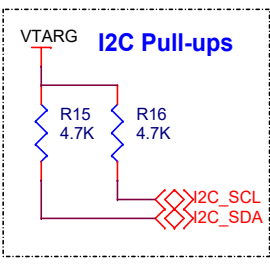
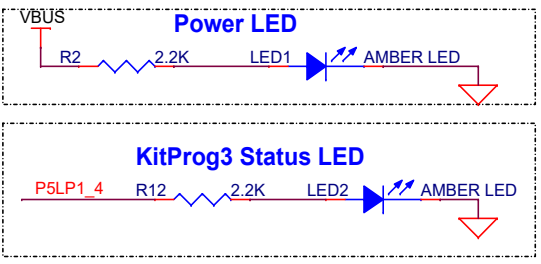
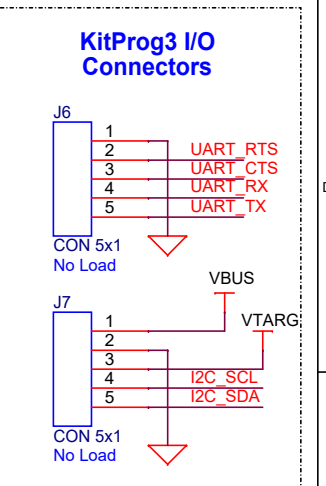
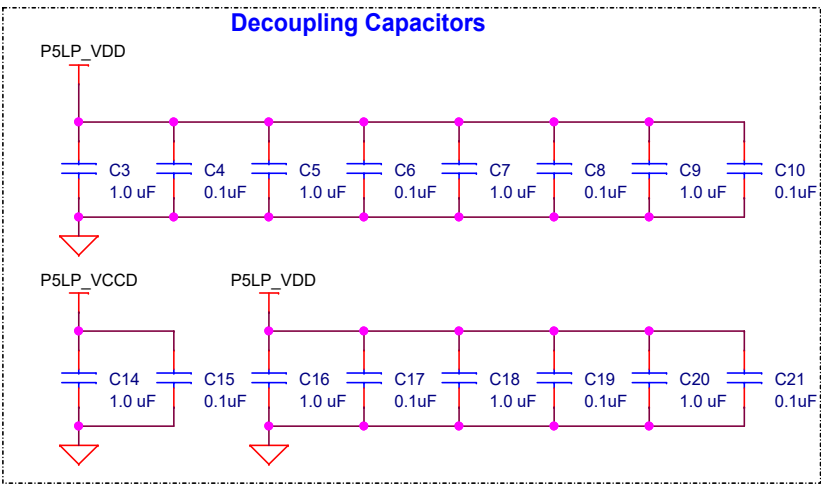
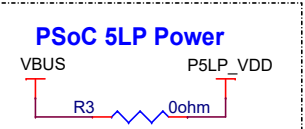
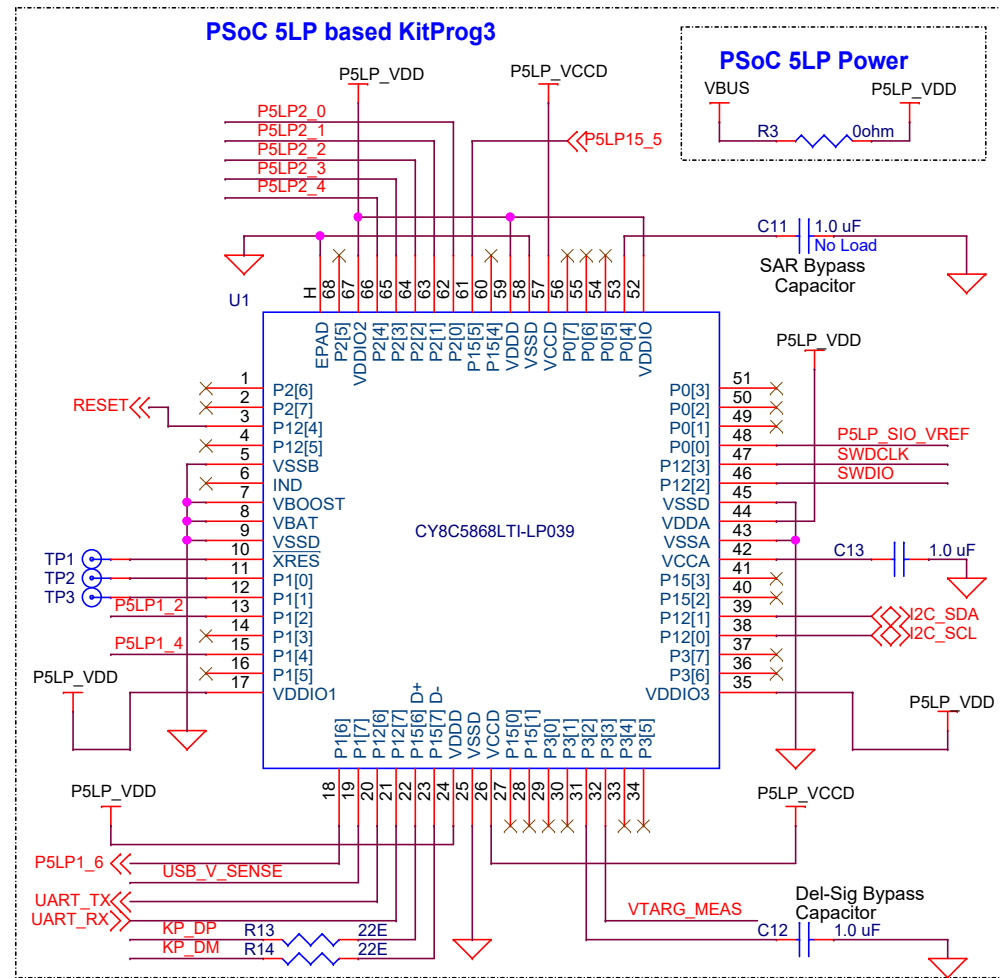
CYPRESS SEMICONDUCTOR © 2018


SCH Title : PSoC 6 Wi-Fi BT Prototyping Kit

Page Title : Title Page

Size A4	Document Number <b>630-60537-01</b>	Drawn By <b>TARE</b>	Approved By <b>RKAD</b>	Rev <b>02</b>
Date:	Friday, October 05, 2018		Sheet	1 of 8







**CYPRESS SEMICONDUCTOR**  
198 CHAMPION COURT  
SAN JOSE, CA 95134  
(408) 943-2600

---

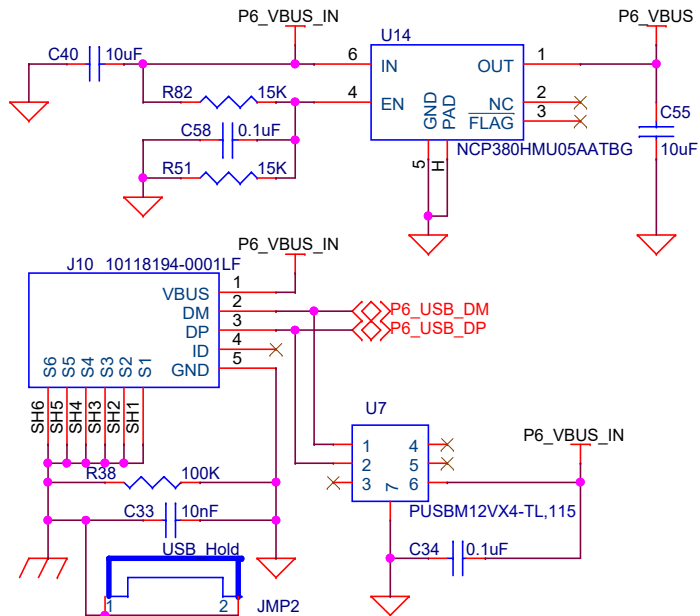
**CYPRESS SEMICONDUCTOR © 2018**

**SCH Title : PSoc 6 Wi-Fi BT Prototyping Kit**

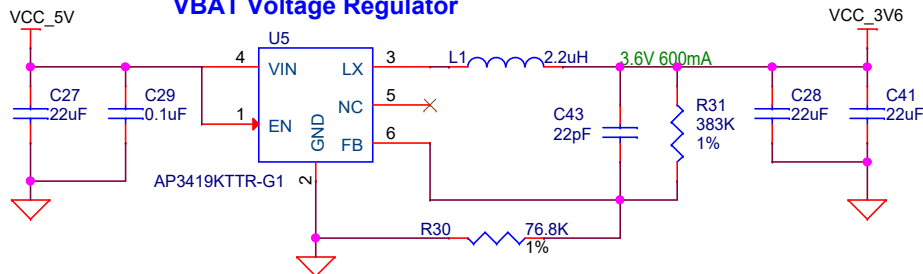
**Page Title : KitProg3**

Size A4	Document Number <b>630-60537-01</b>	Drawn By <b>TARE</b>	Approved By <b>RKAD</b>	Rev <b>02</b>
Date:	Monday, November 05, 2018		Sheet	3 of 8

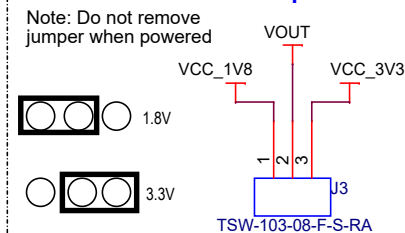
### PSoC 6 MCU USB Device Micro-B connector



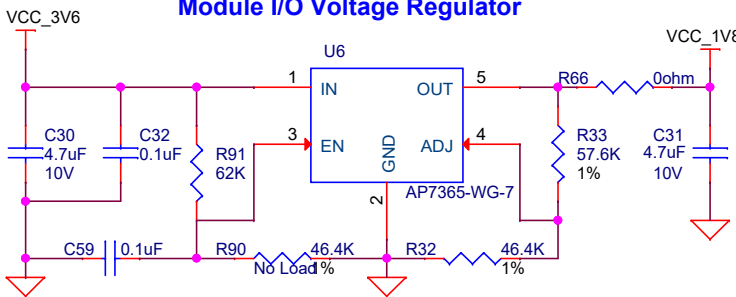
### VBAT Voltage Regulator



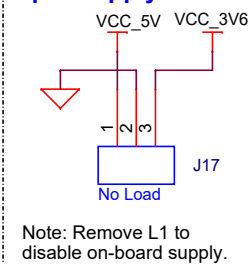
### System Power Selection Jumper



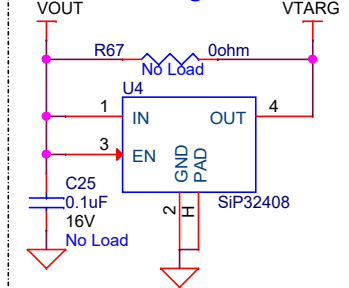
### Module I/O Voltage Regulator



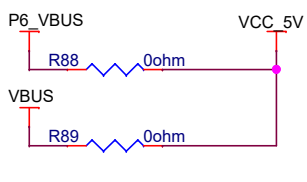
### Input Supply Header



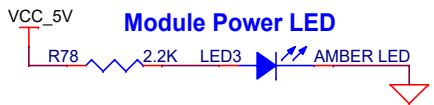
### Reverse Voltage Protection



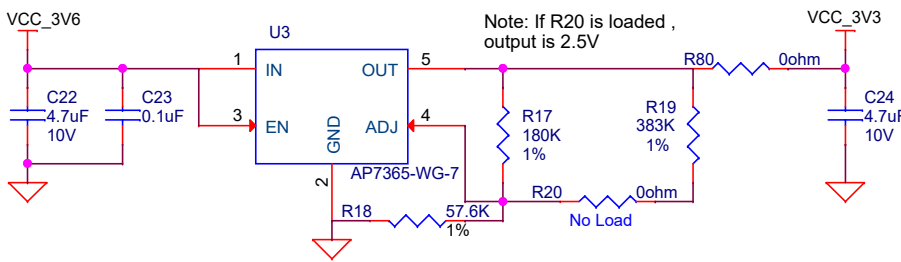
### Input Supply



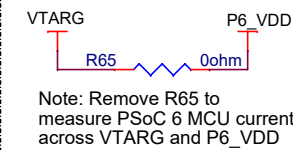
### Module Power LED



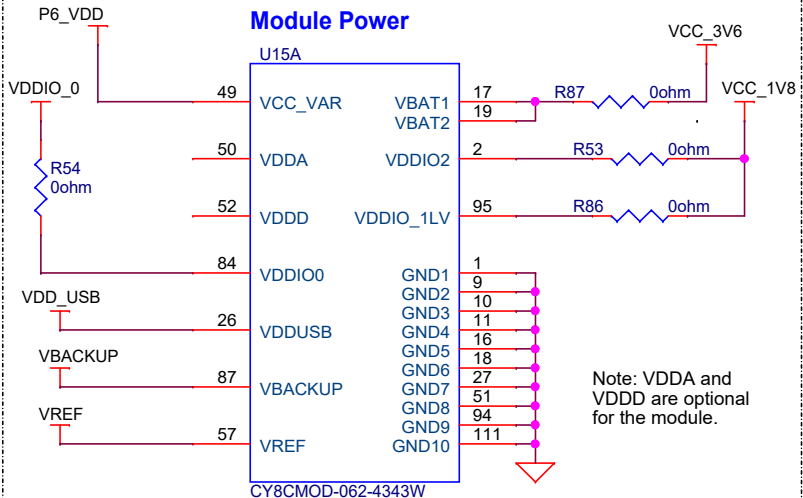
### Module Variable Voltage Regulator



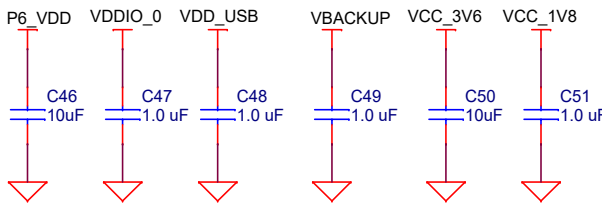
### Current Measurement



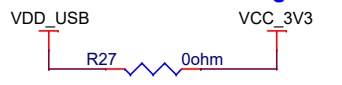
### Module Power



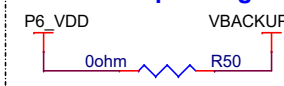
### Decoupling Capacitors



### PSoC 6 MCU USB Voltage



### Backup Voltage



**CYPRESS SEMICONDUCTOR**  
198 CHAMPION COURT  
SAN JOSE, CA 95134  
(408) 943-2600

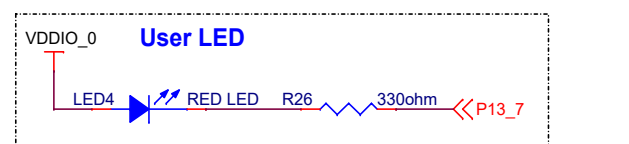
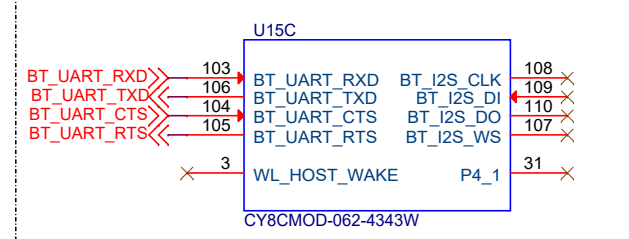
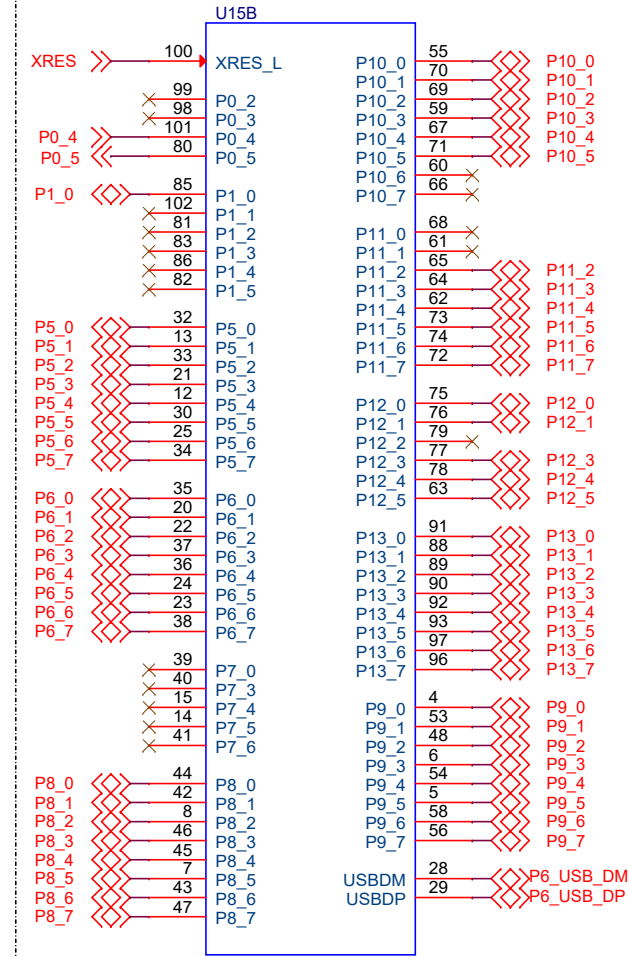
CYPRESS SEMICONDUCTOR © 2018

SCH Title : PSoC 6 Wi-Fi BT Prototyping Kit

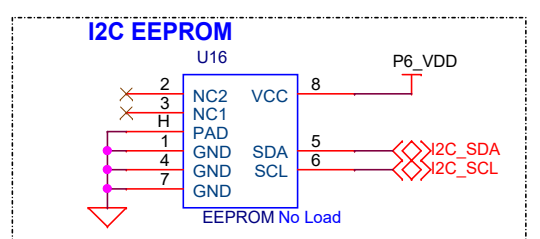
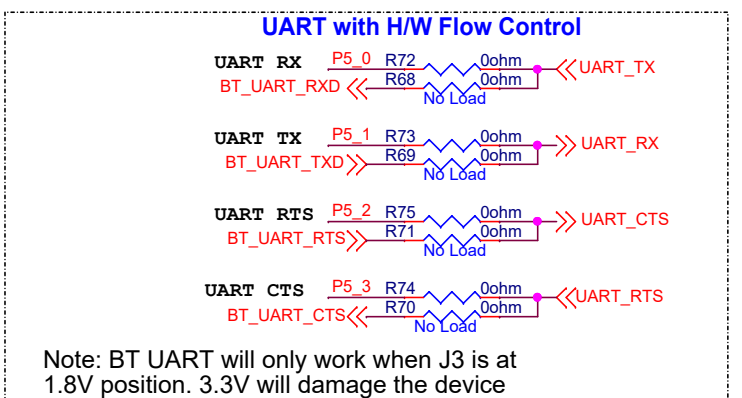
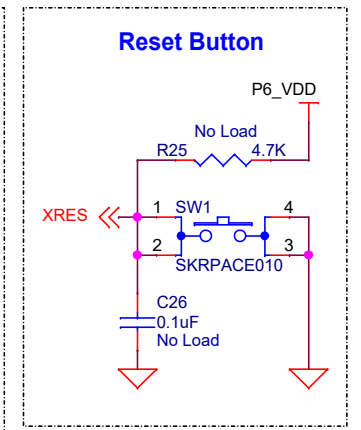
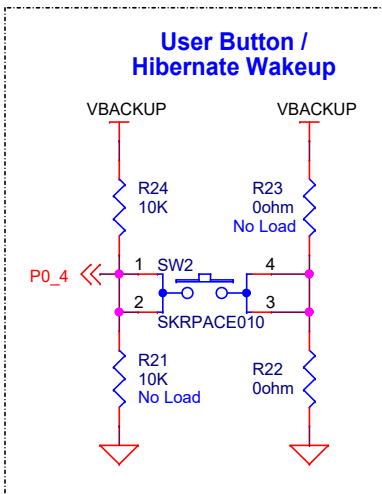
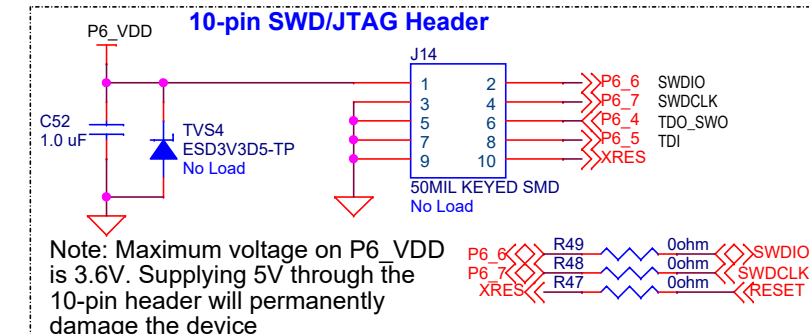
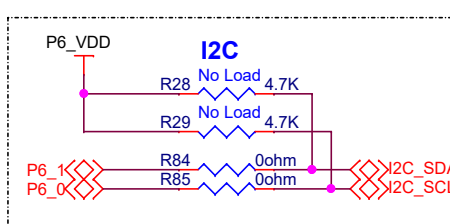
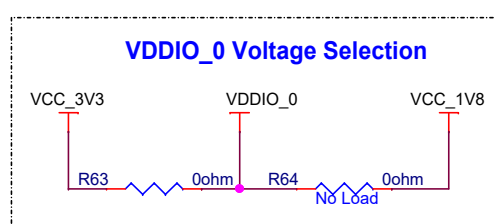
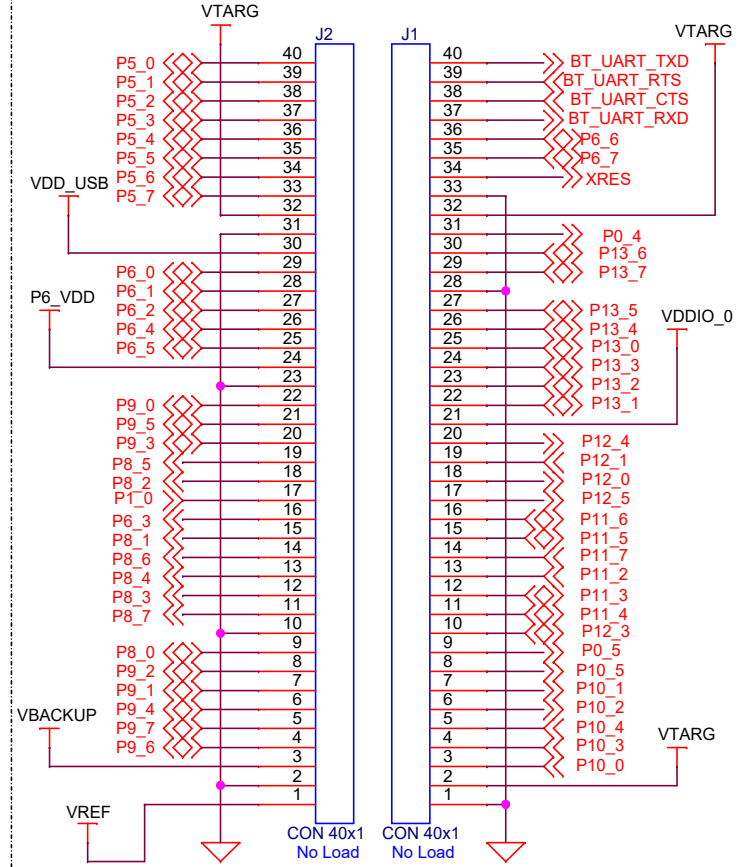
Page Title : PSoC 6 Module Power

Size A4	Document Number <b>630-60537-01</b>	Drawn By <b>TARE</b>	Approved By <b>RKAD</b>	Rev <b>02</b>
Date:	Monday, November 12, 2018		Sheet	4 of 8

### CY8CMOD-062-4343W Module Signals



### PSoC 6 MCU I/O Headers



**CYPRESS** EMBEDDED IN TOMORROW™  
**CYPRESS SEMICONDUCTOR**  
 198 CHAMPION COURT  
 SAN JOSE, CA 95134  
 (408) 943-2600

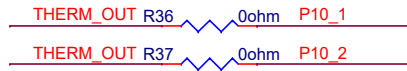
**CYPRESS SEMICONDUCTOR © 2018**

**SCH Title : PSoC 6 Wi-Fi BT Prototyping Kit**

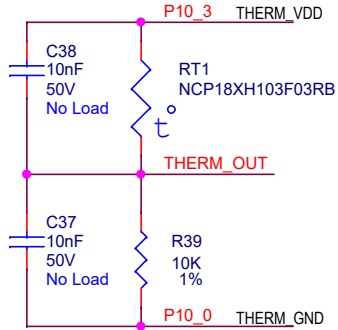
**Page Title : PSoC 6 Module Signals**

Size	Document Number	Drawn By	Approved By	Rev
A4	<b>630-60537-01</b>	<b>TARE</b>	<b>RKAD</b>	<b>02</b>
Date:	Thursday, November 08, 2018		Sheet	5 of 8

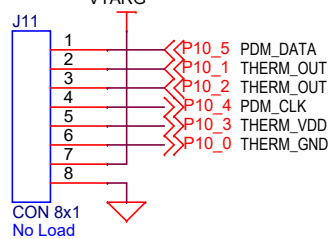
## Thermistor and PDM Mic Section



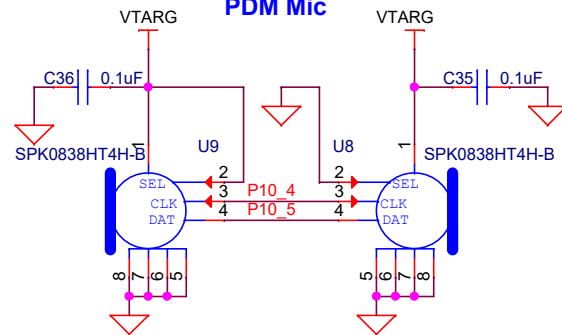
### Thermistor



### Header

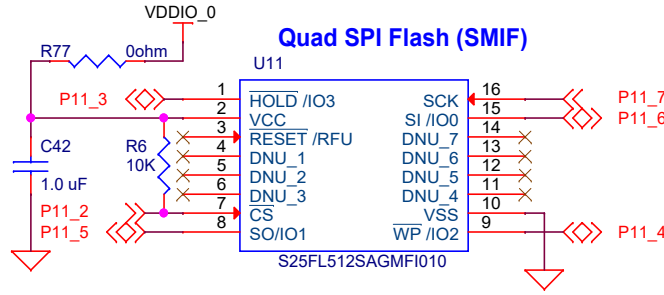


### PDM Mic

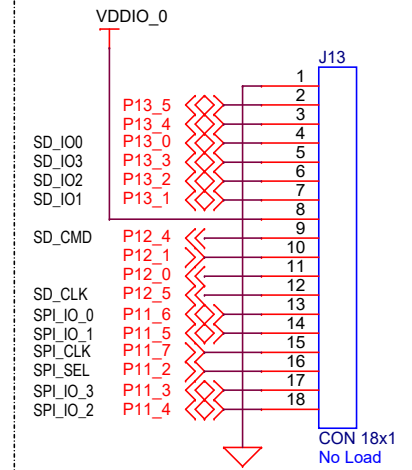


## Quad SPI Flash Memory and microSD Card Section

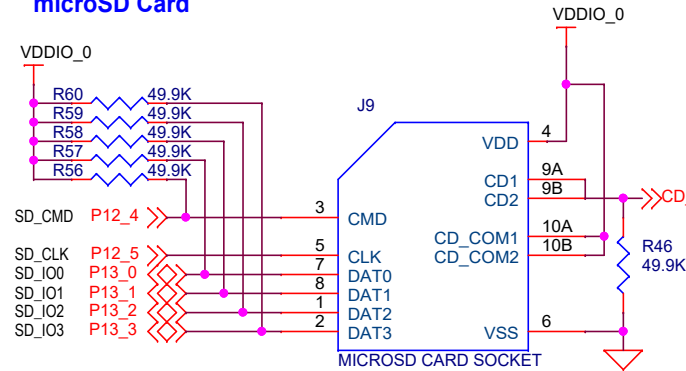
### Quad SPI Flash (SMIF)



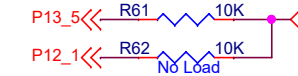
### Header



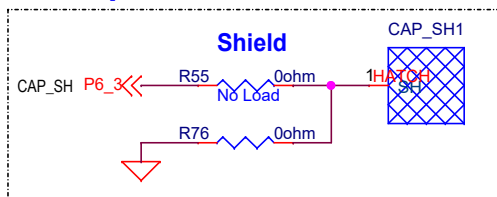
### microSD Card



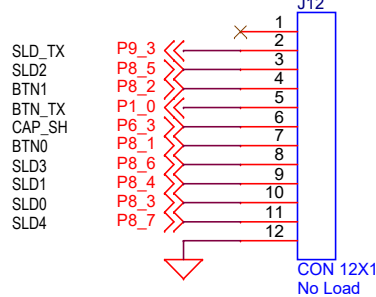
### Card Detect Multiplexing



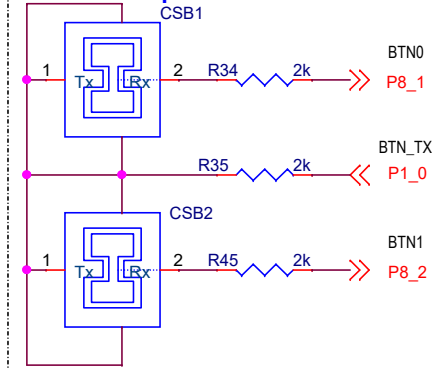
## CapSense Section



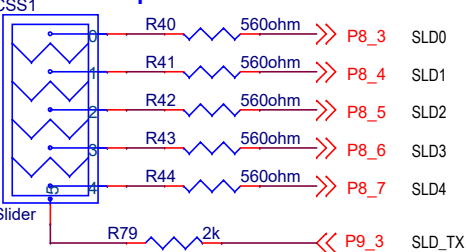
### Header



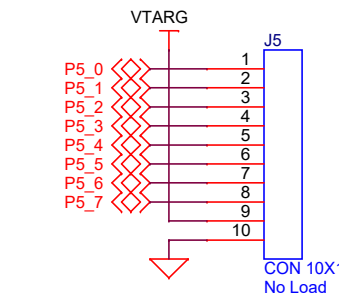
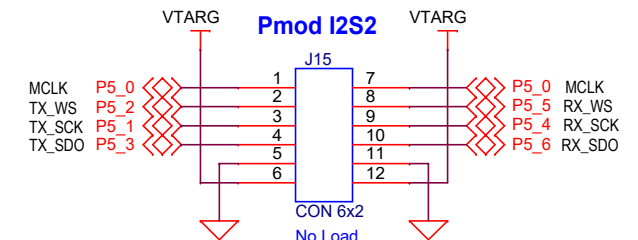
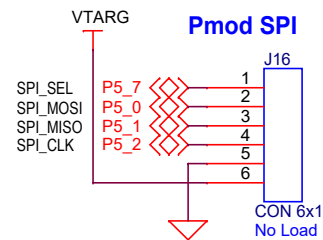
### CapSense Buttons



### CapSense Slider



## Pmod Header Section



Note: Pmod I2S2 will only work when VTARG is 3.3V



**CYPRESS**  
EMBEDDED IN TOMORROW™  
CYPRESS SEMICONDUCTOR  
198 CHAMPION COURT  
SAN JOSE, CA 95134  
(408) 943-2600

CYPRESS SEMICONDUCTOR © 2018

SCH Title : PSoC 6 Wi-Fi BT Prototyping Kit

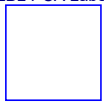
Page Title : On-board Accessories

Size A4	Document Number <b>630-60537-01</b>	Drawn By <b>TARE</b>	Approved By <b>RKAD</b>	Rev <b>02</b>
Date:	Thursday, January 03, 2019		Sheet	6 of 8

### Accessories

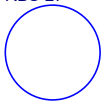
#### PCBA label

LBL PCA Label

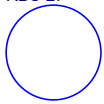


#### Bumper

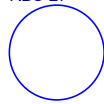
RBS-27



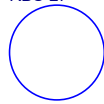
RBS-27



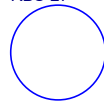
RBS-27



RBS-27

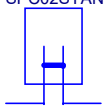


RBS-27



#### Jumper Shunt

SPC02SYAN

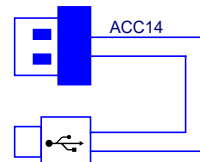


#### Acrylic Overlay

Acrylic Overlay

Color: Clear, Transparent  
Finish: Matt

L = 89.26mm  
W = 20.62mm  
Thickness = 1mm



USB Micro-B Cable



**CYPRESS**  
EMBEDDED IN TOMORROW™

**CYPRESS SEMICONDUCTOR**  
198 CHAMPION COURT  
SAN JOSE, CA 95134  
(408) 943-2600

**CYPRESS SEMICONDUCTOR © 2018**

**SCH Title : PSoC 6 Wi-Fi BT Prototyping Kit**

**Page Title : Accessories**

Size A4	Document Number <b>630-60537-01</b>	Drawn By <b>TARE</b>	Approved By <b>RKAD</b>	Rev <b>02</b>
------------	--	-------------------------	----------------------------	------------------

Date: **Monday, November 05, 2018** Sheet **7** of **8**

# REVISION HISTORY

REV	DESCRIPTION OF CHANGE	DATE
01	Initial Internal release	2018/08/13
02	Initial release	2018/08/21



**CYPRESS**  
EMBEDDED IN TOMORROW™

**CYPRESS SEMICONDUCTOR**  
198 CHAMPION COURT  
SAN JOSE, CA 95134  
(408) 943-2600

**CYPRESS SEMICONDUCTOR © 2018**

**SCH Title : PSoC 6 Wi-Fi BT Prototyping Kit**

**Page Title : Revision History**

Size A4	Document Number <b>630-60537-01</b>	Drawn By <b>TARE</b>	Approved By <b>RKAD</b>	Rev <b>02</b>
Date: Friday, October 05, 2018			Sheet <b>8</b> of <b>8</b>	