

CY8CPROTO-040T PSoC™ 4000T CAPSENSE™ Prototyping Kit

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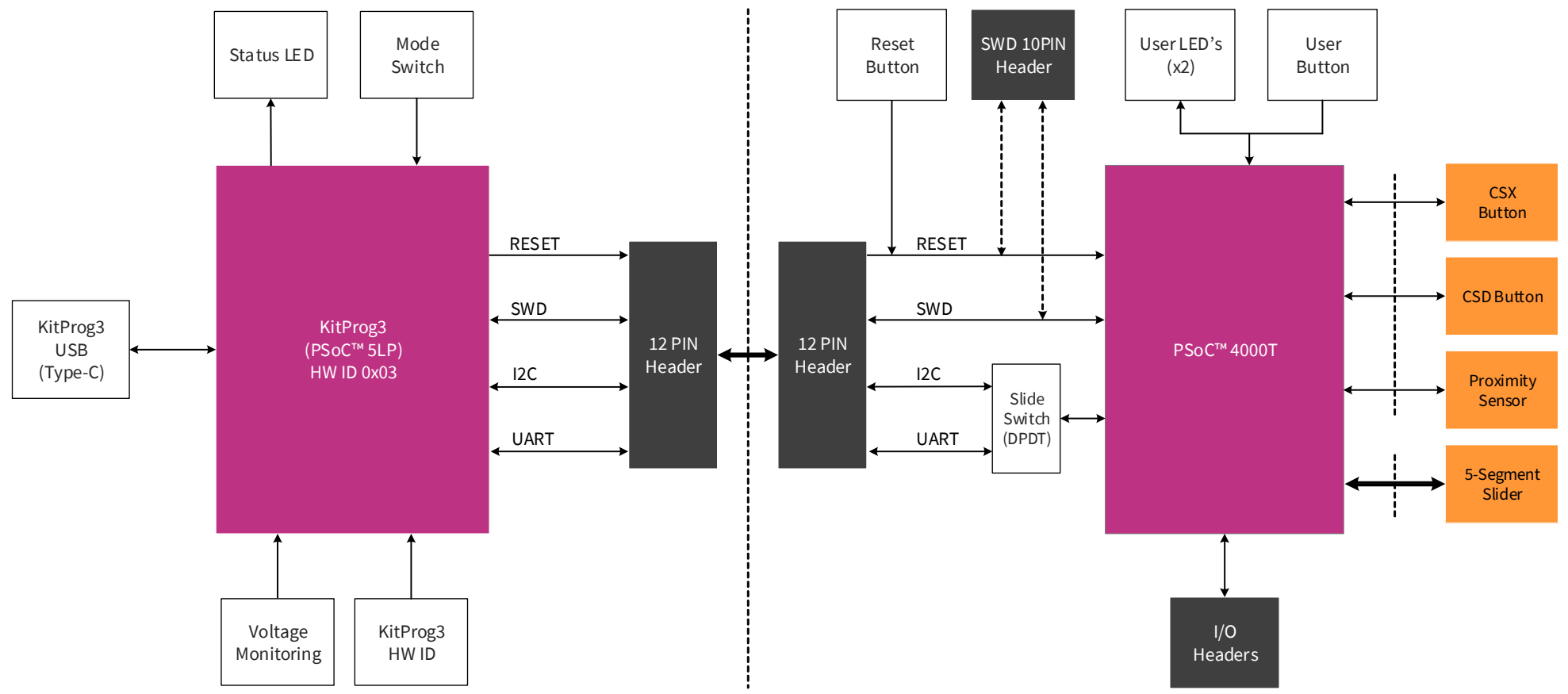
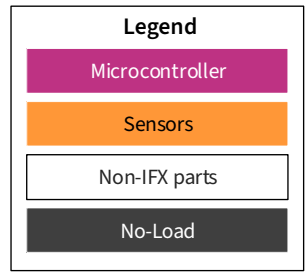
Drawing Numbers	
PCBA	121-60682-01
PCB	600-60682-01
FAB DRW	610-60682-01
ASSY DRW	620-60682-01
SCH DRW	630-60682-01

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Functional Block Diagram



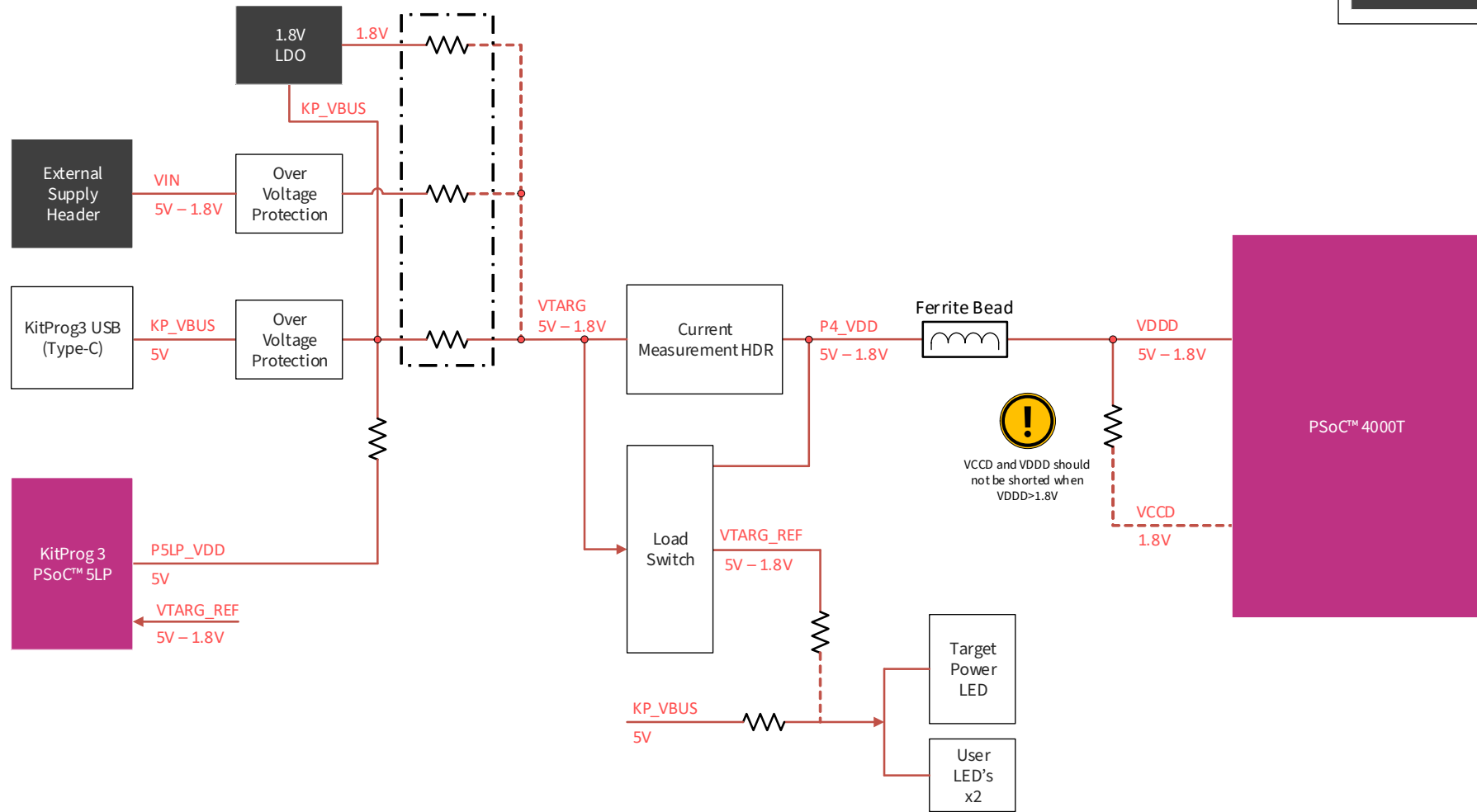
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Power Block Diagram

Legend	
	Microcontroller
	Non-IFX parts
	No-Load



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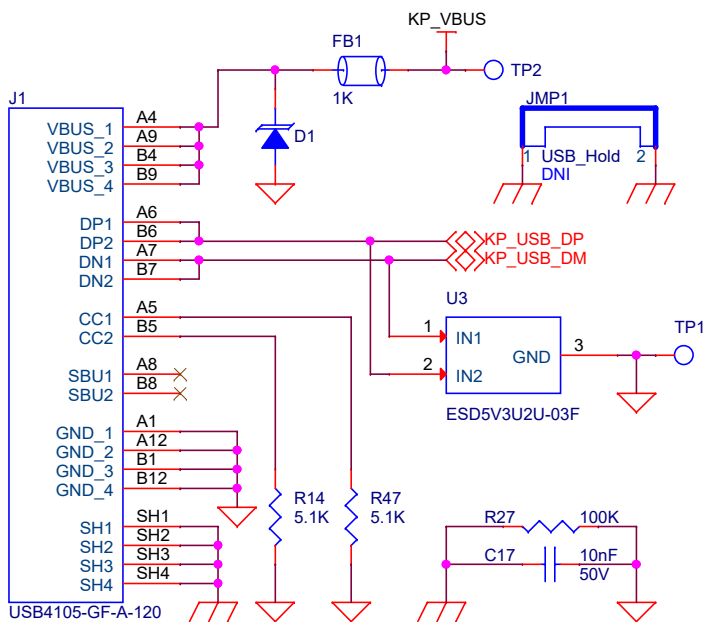


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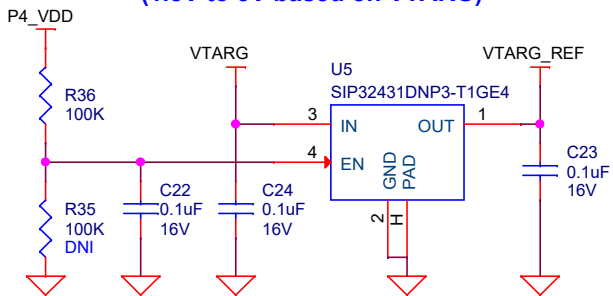
Page Title : Power supply Block Diagram

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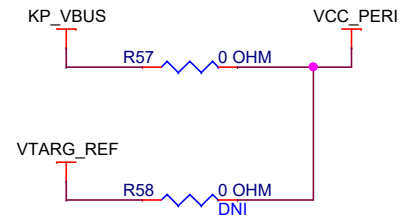
USB Power Supply and Interface



VTARG Reference Voltage (1.8V to 5V based on VTARG)

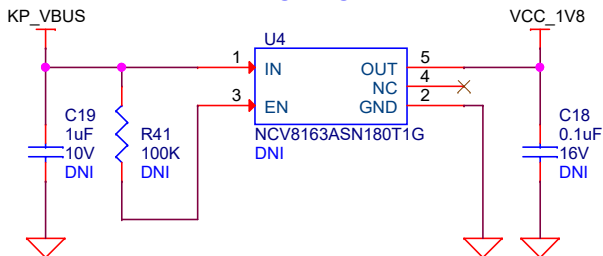


Voltage supply for Peripherals

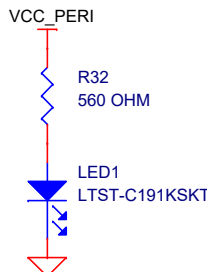


Note:
Unmount the R57 and mount R58 to select VTARG_REF as peripheral supply voltage

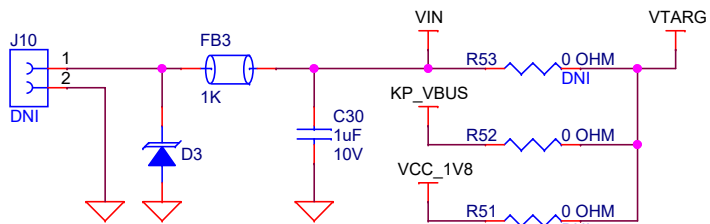
1.8V Voltage regulator



Power LED



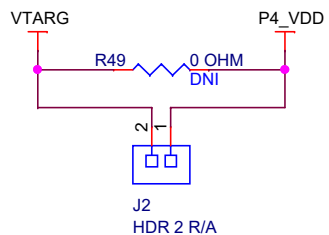
Target Voltage Supply



Note:
External supply should be minimum of 1.8V to maximum of 5V. For external supply of 1.8V, populate resistor R45 to short VDDD and VCCD

Note:
1. Unmount the R52 and mount R53 to select external supply as Target voltage
2. Unmount the R52 and mount R51 to select 1.8V supply from onboard LDO as Target voltage

Current Measurement Header



Note:
This header is used for measuring current consumed by the PSoC™ 4000T MCU

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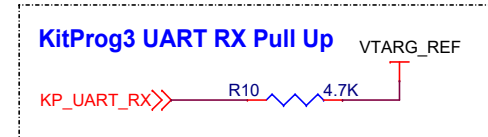
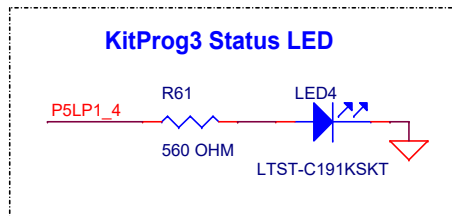
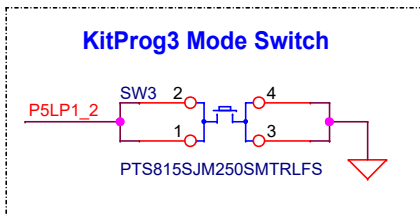
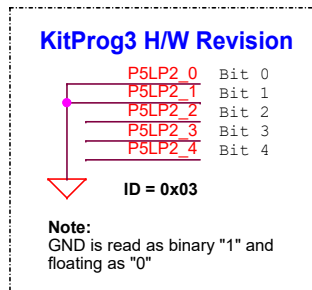
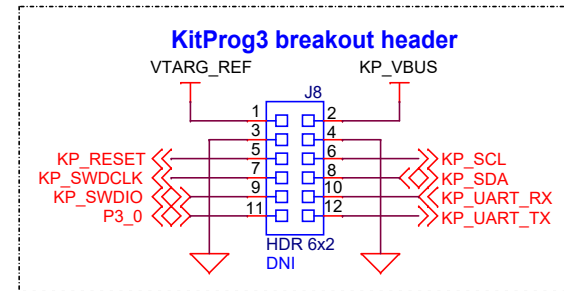
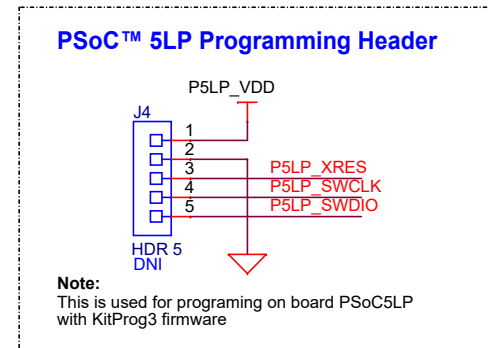
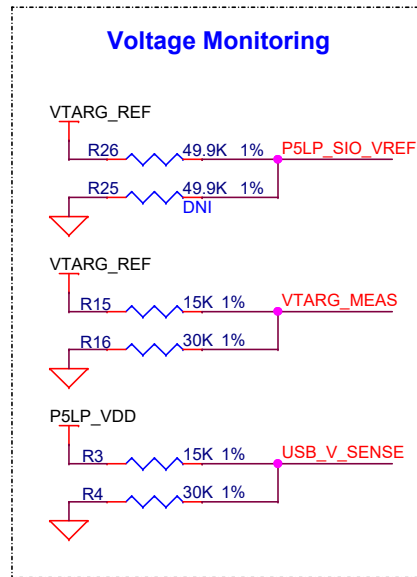
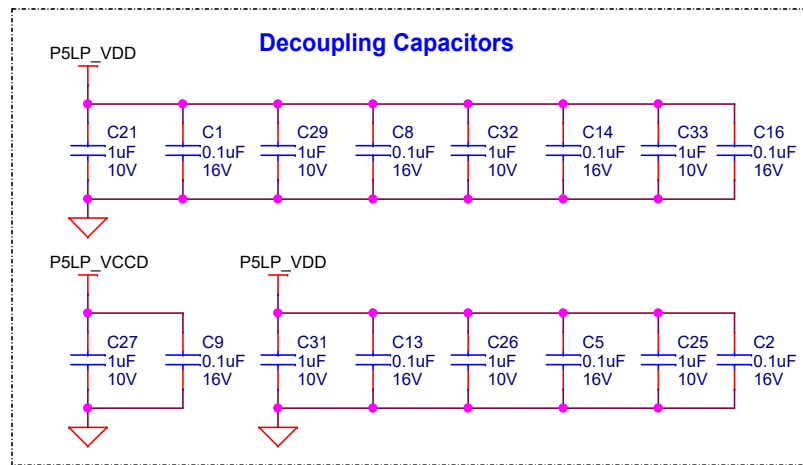
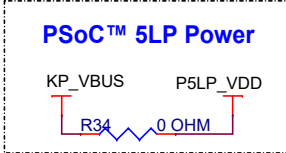
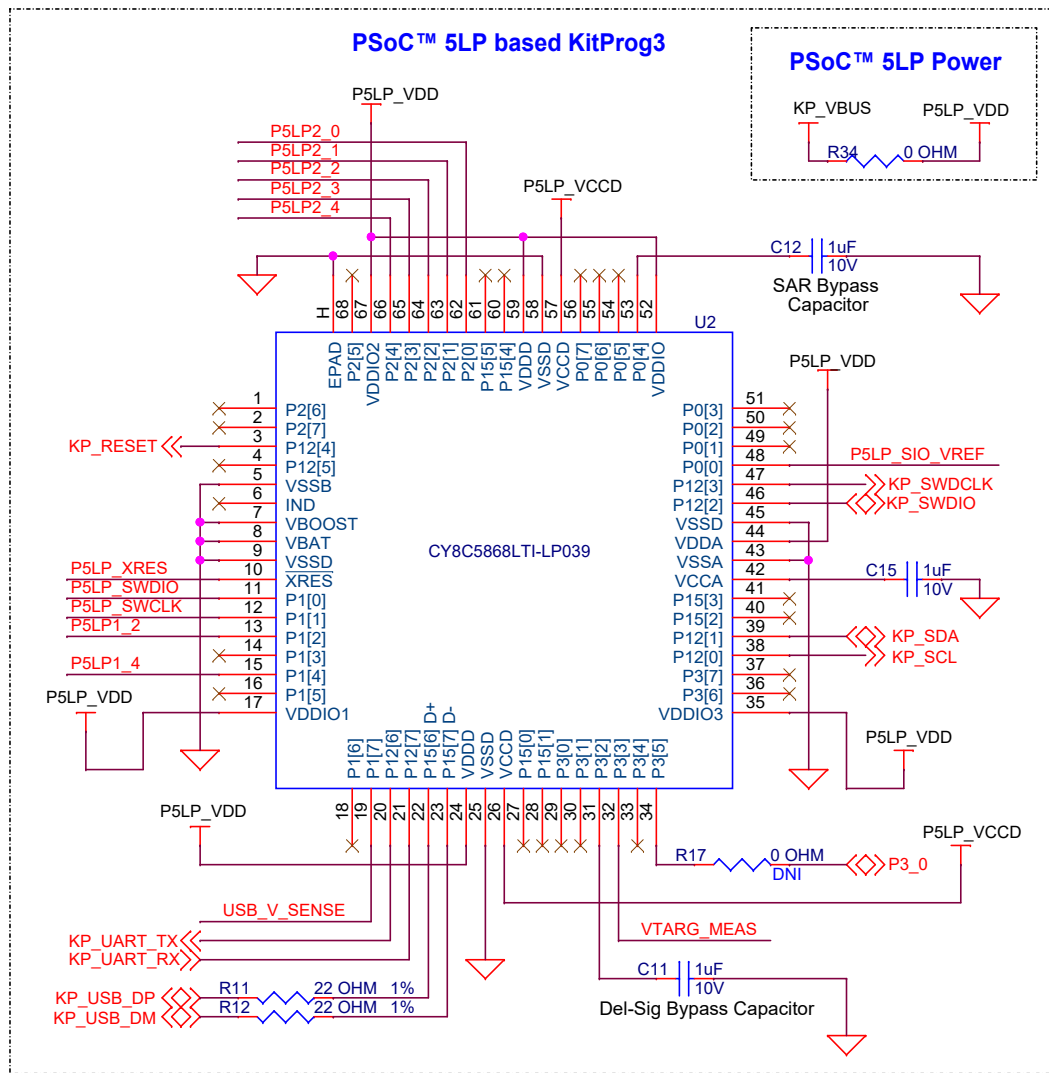
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Page Title : USB Interface and Power Supply

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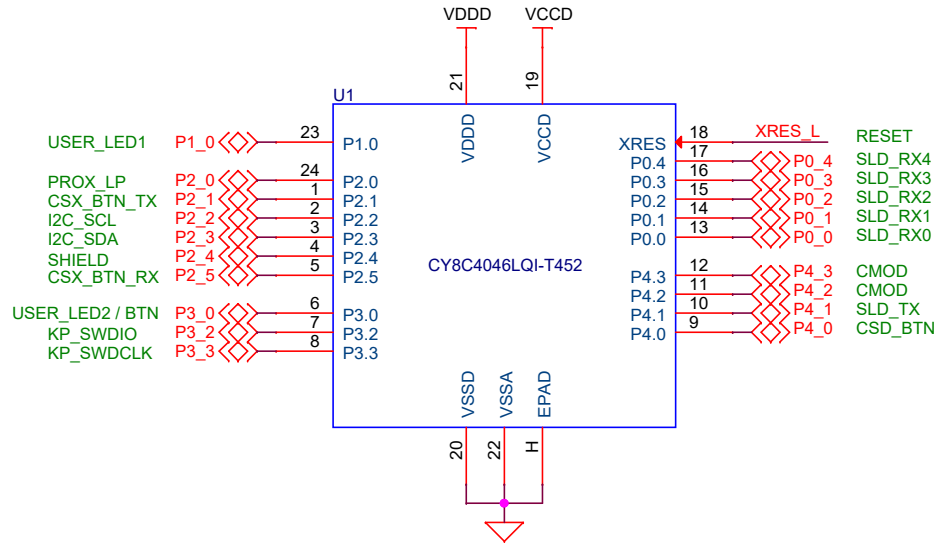


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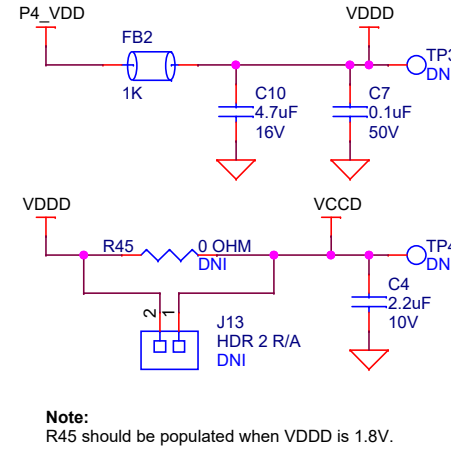
Page Title : KitProg3 Interface

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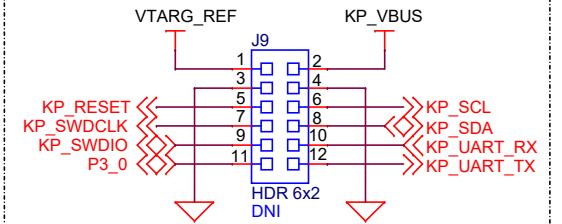
PSoC™ 4000T Device IOs



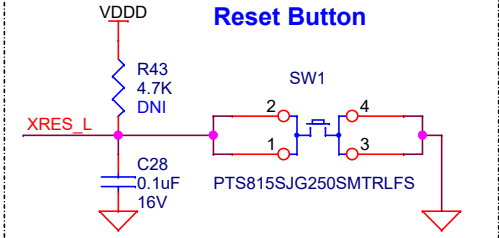
PSoC™ 4000T Device Power



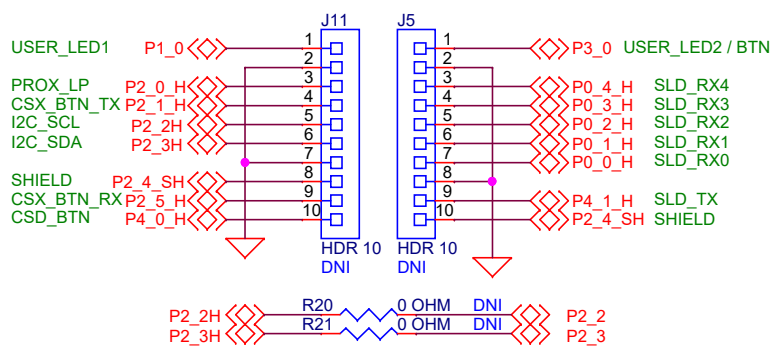
PSoC™ 4000T breakout header



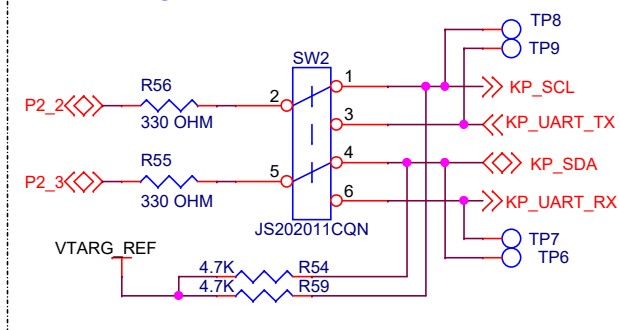
Reset Button



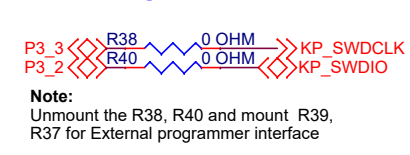
PSoC™ 4000T IO header



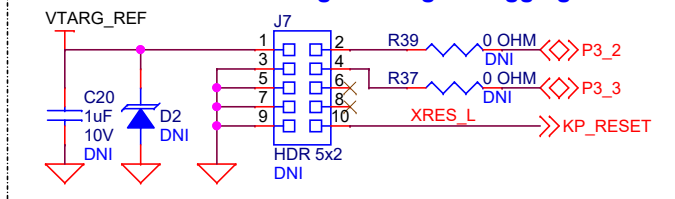
KitProg3 I2C and UART Interface Switch



KitProg3 SWD interface



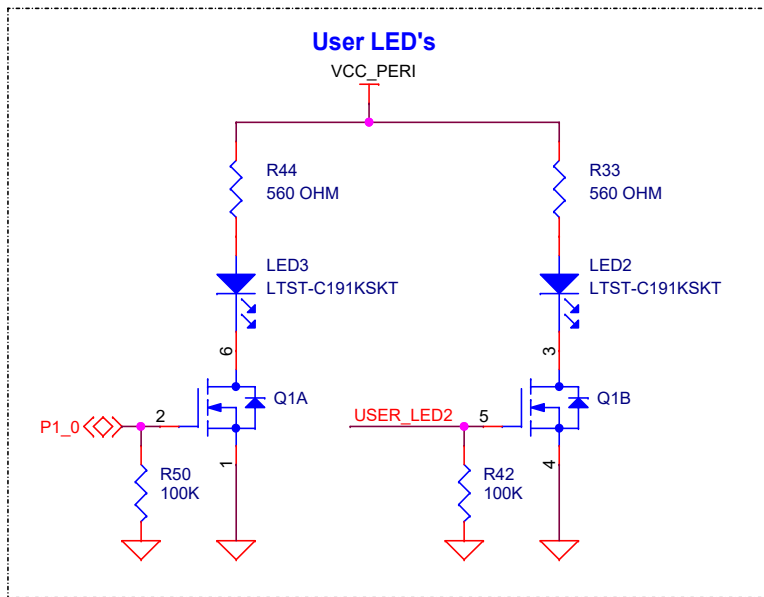
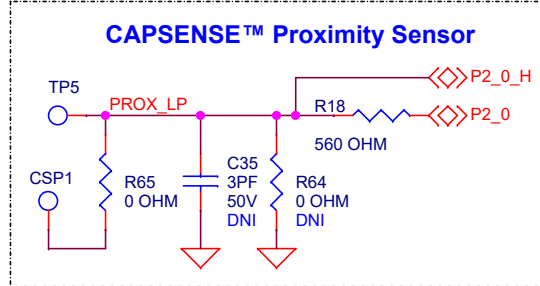
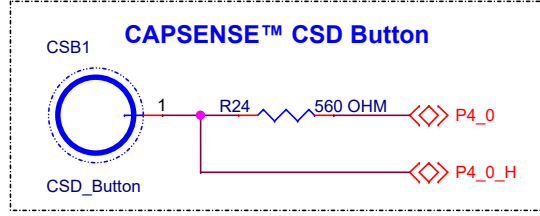
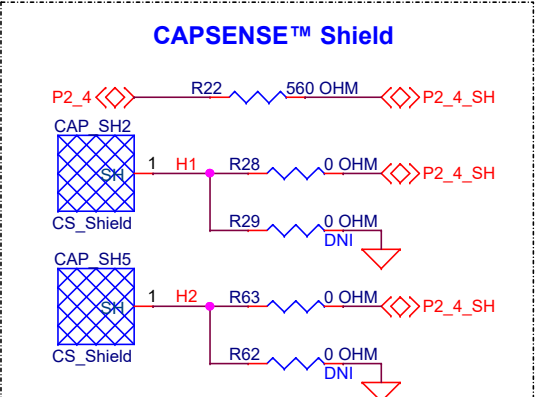
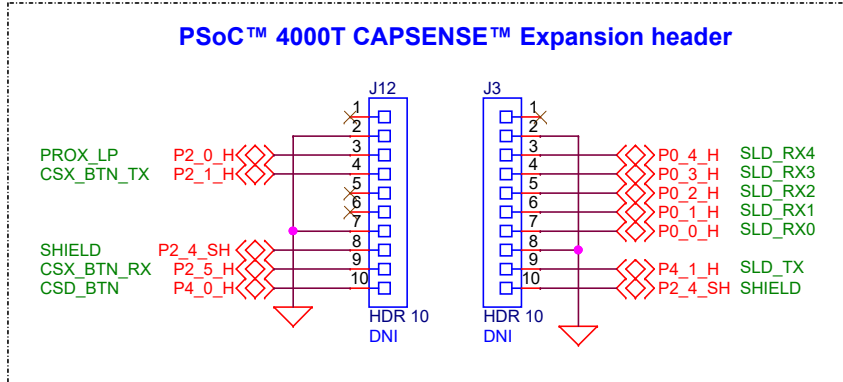
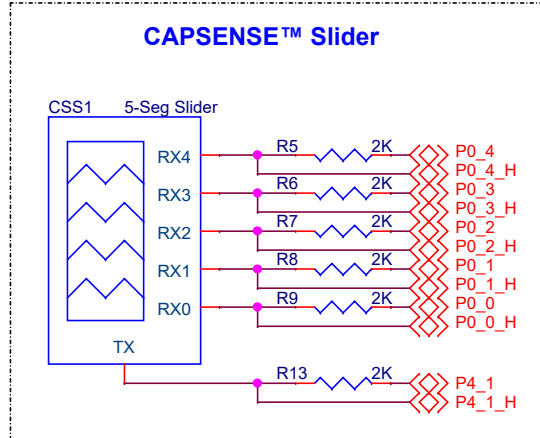
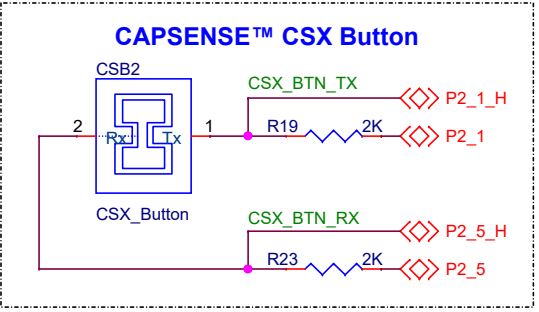
PSoC™ 4000T SWD Programming/Debugging Header



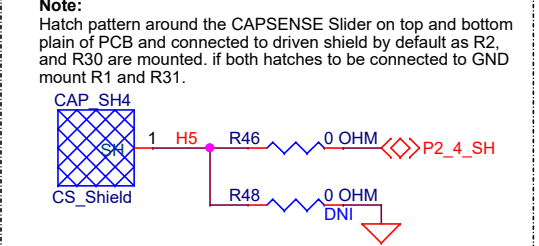
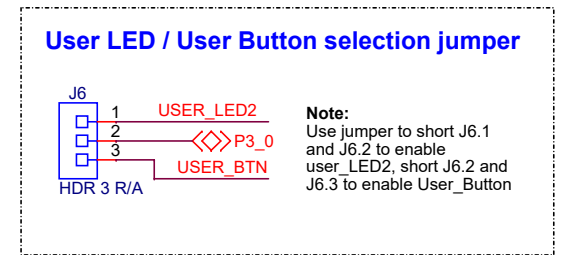
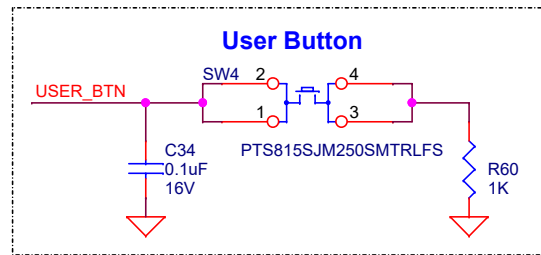
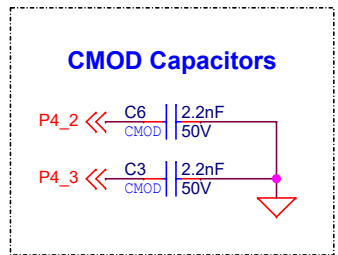
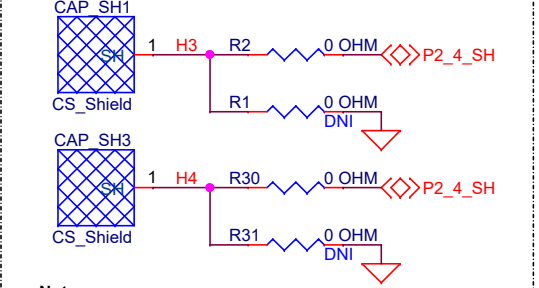
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Note:
Hatch pattern around the CAPSENSE button and Proximity sensor on top and bottom plain of PCB and connected to driven shield by default as R28 and R63 are mounted, if both hatches to be connected to GND mount R29 and R62.



Note:
Hatch pattern around the CAPSENSE sensor lines on main PCB and connected to driven shield by default as R46 is mounted, if this hatch to be connected to GND mount R48.

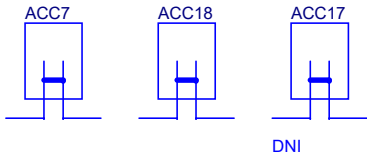
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Page Title : Capacitive sensor's and onboard Peripherals				
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Accessories

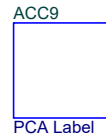
Jumper Shunt



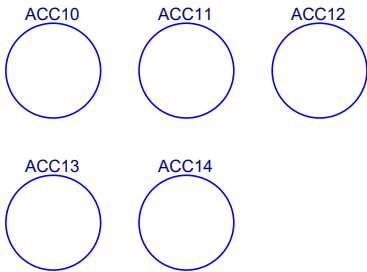
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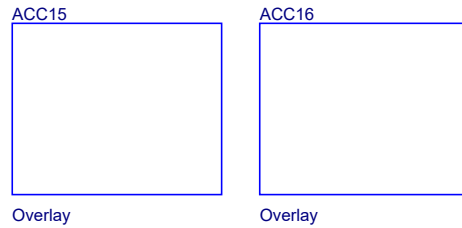
PCBA Label



Bumpons



CAPSENSE Overlay's



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Page Title: On-board Peripherals and Accessories

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