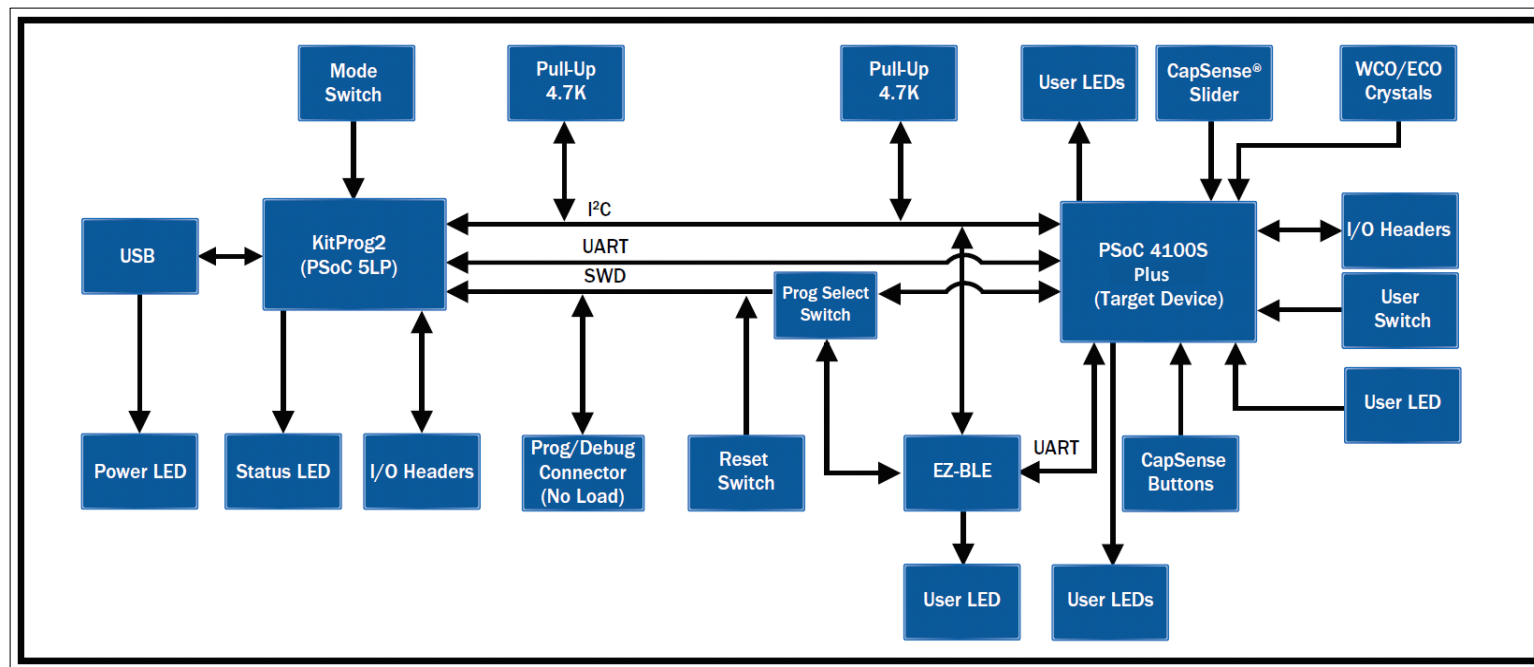


CY8CKIT-149 PSoC 4100S Plus Prototyping Kit

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01	Title, Table of Contents & Drawing Numbers
02	Block Diagram
03	KitProg2 Schematics
04	PSoC 4100S Plus
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06	IO Headers, BLE and Programming
07	Revision History

Drawing Numbers	
PCBA	121-60476-01
PCB	600-60476-01
FAB DRW	610-60476-01
ASSY DRW	620-60476-01
SCH DRW	630-60476-01

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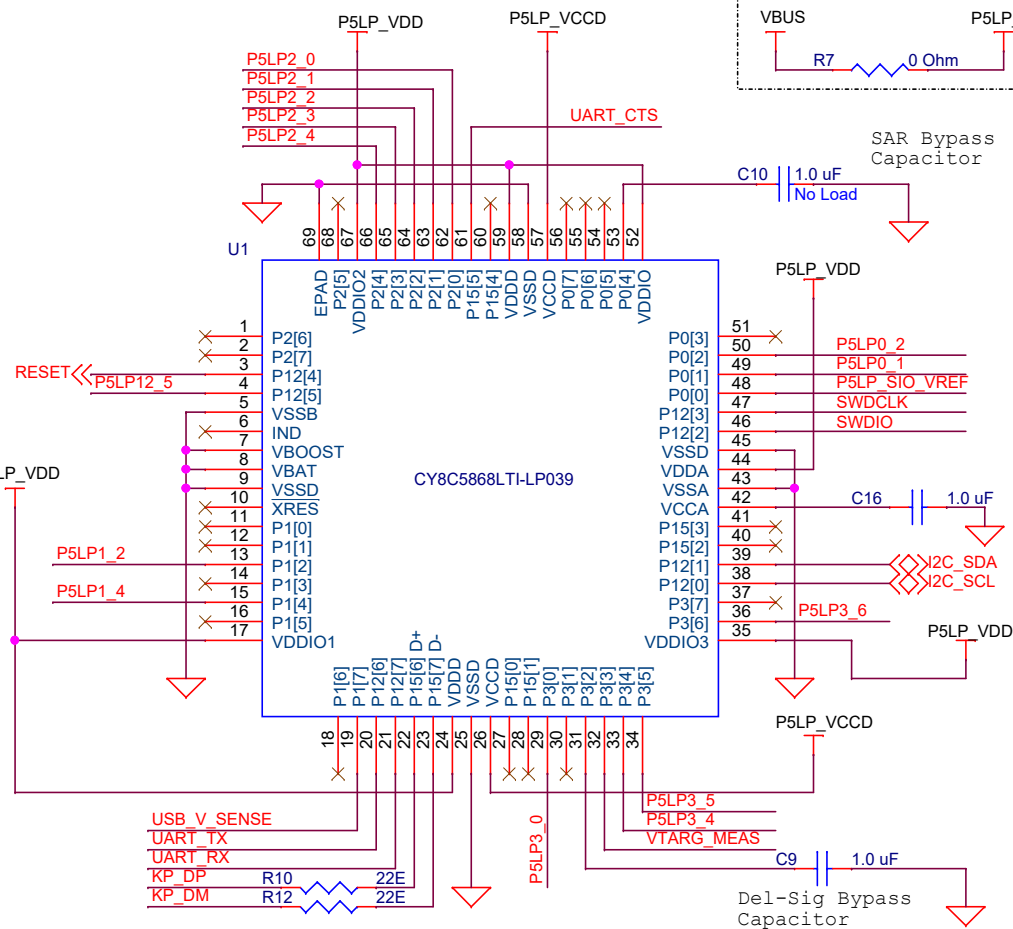
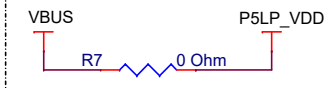
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Page Title : Block Diagram

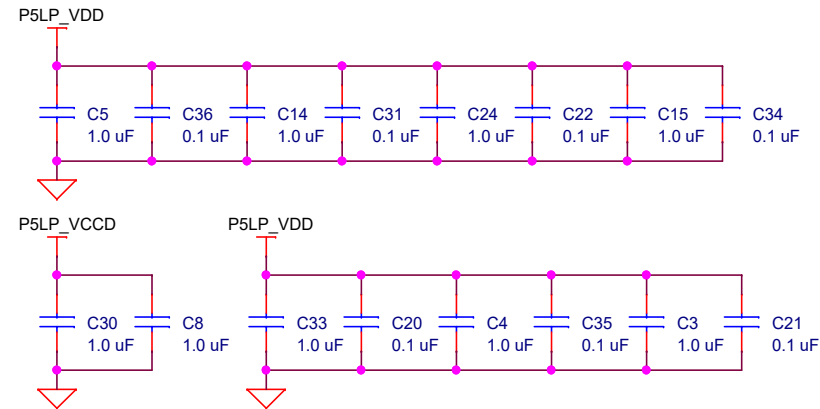
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PSoC 5LP based KitProg2

PSoC 5LP Power



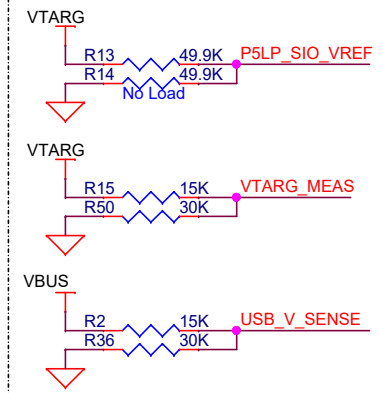
Decoupling Capacitors



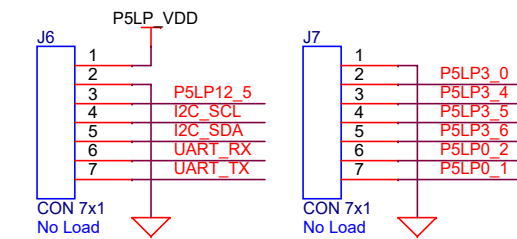
KitProg2 Status LED



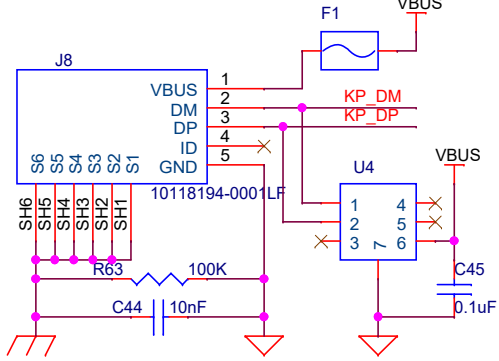
Voltage Monitoring



KitProg2 I/O headers



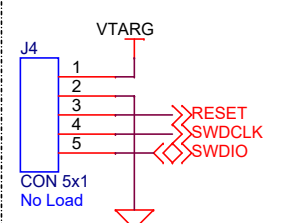
USB Microconnector



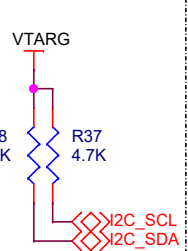
Target Power



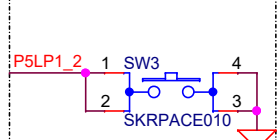
Target PSoC Program/Debug Connector



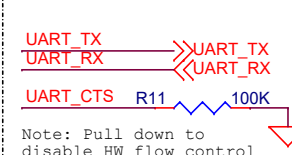
I2C Interface



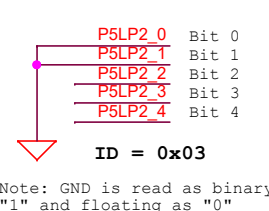
Mode Switch



UART Interface



KitProg2 HW Revision



Power LED



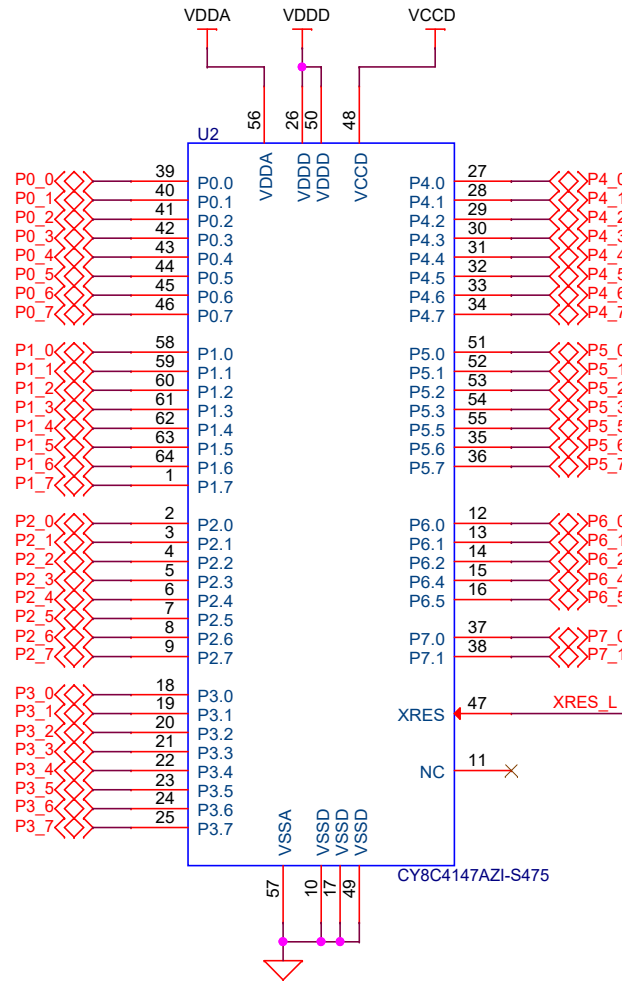
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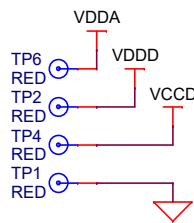
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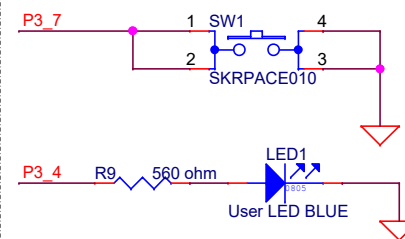
PSoC 4100S Plus Device



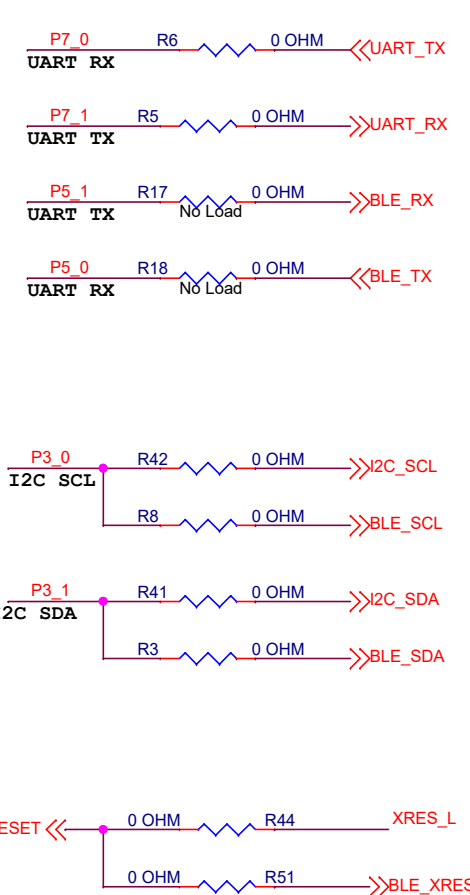
Test Points*



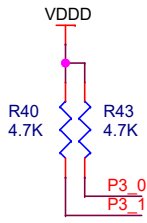
User Interfaces



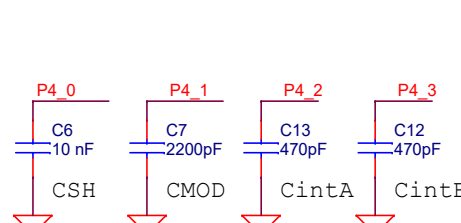
Communication Lines



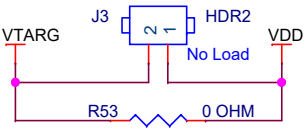
I2C Pull Ups



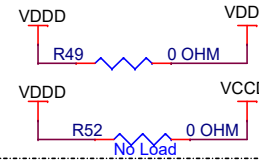
CapSense Capacitors



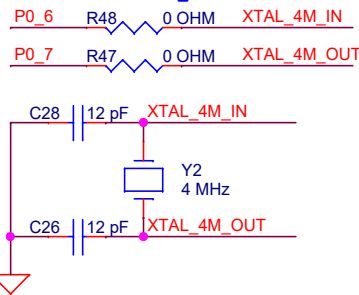
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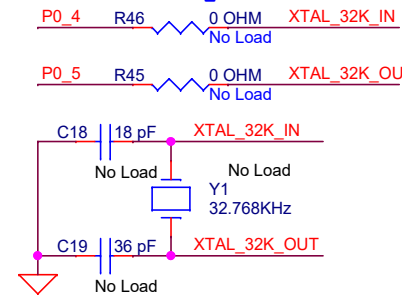
Voltage Distribution



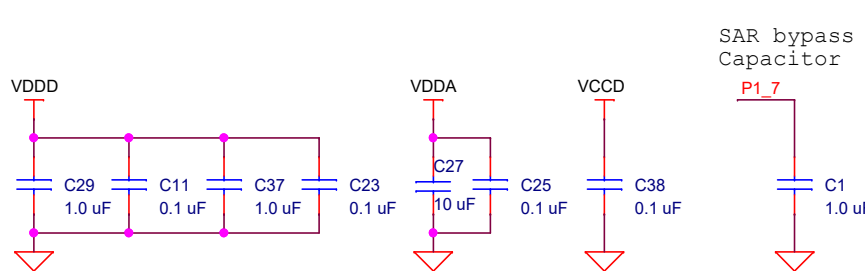
ECO Crystal



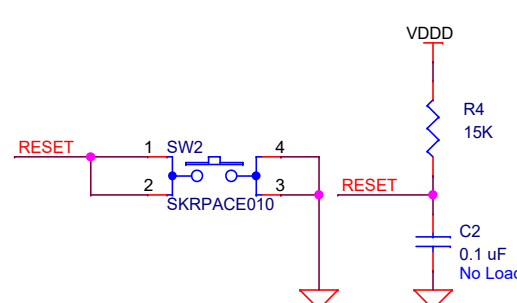
WCO Crystal



Decoupling Capacitors



Reset



*All Test Points are No Load

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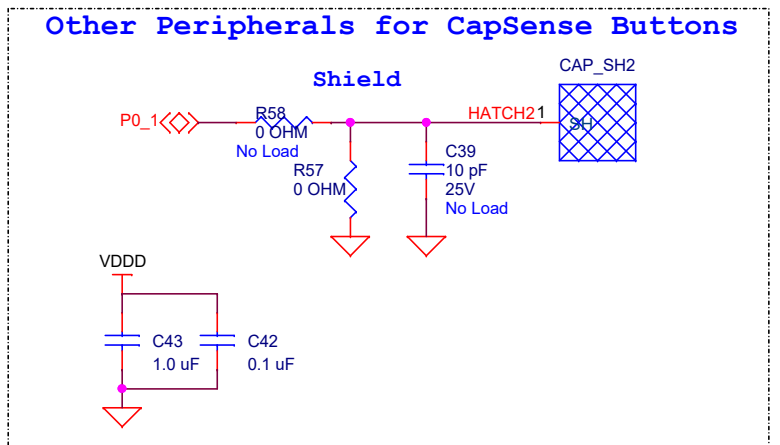
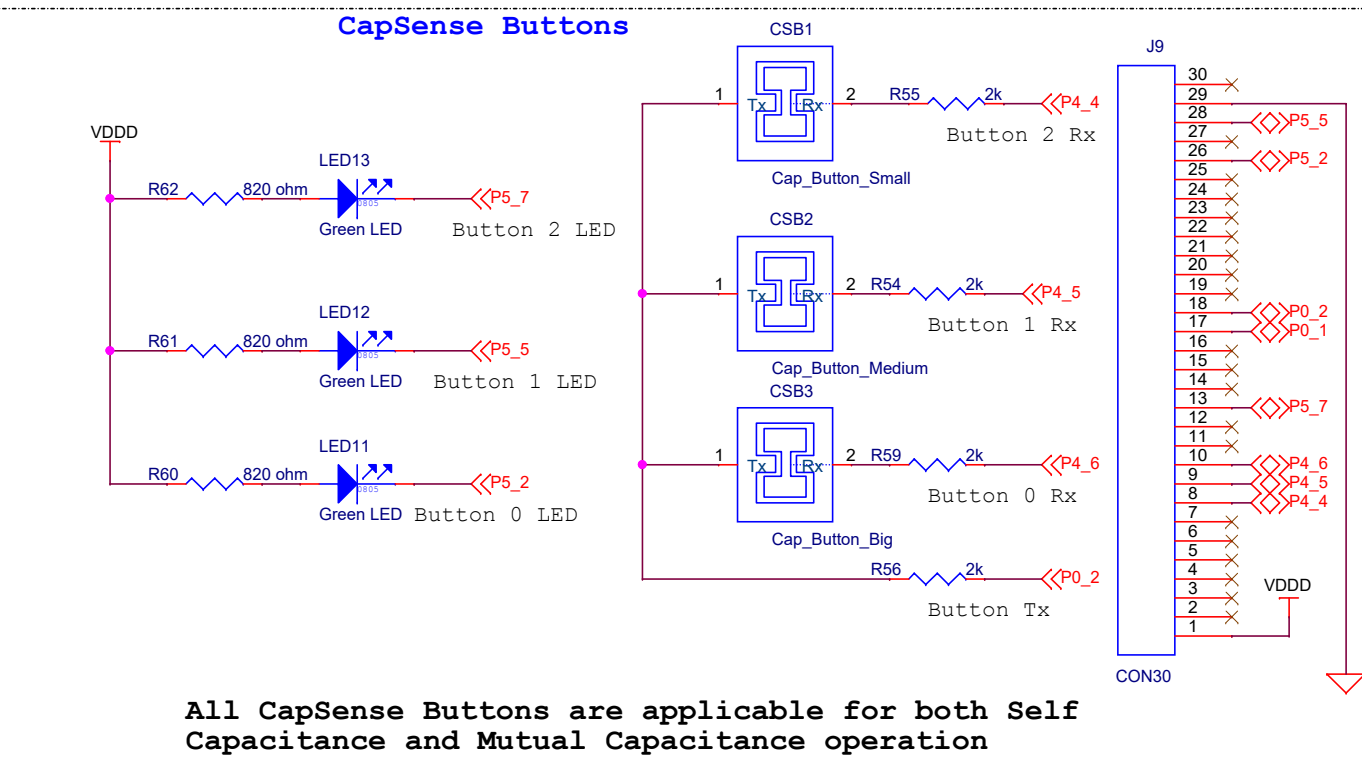
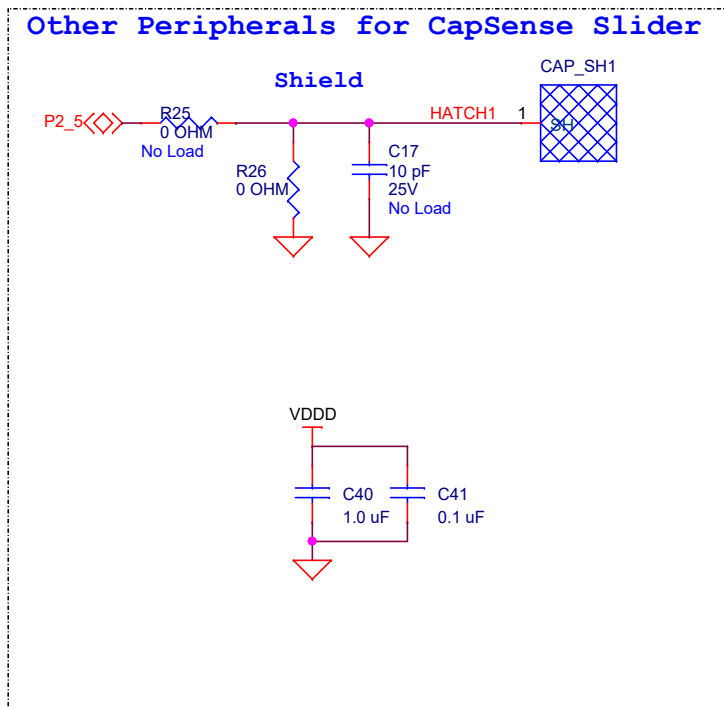
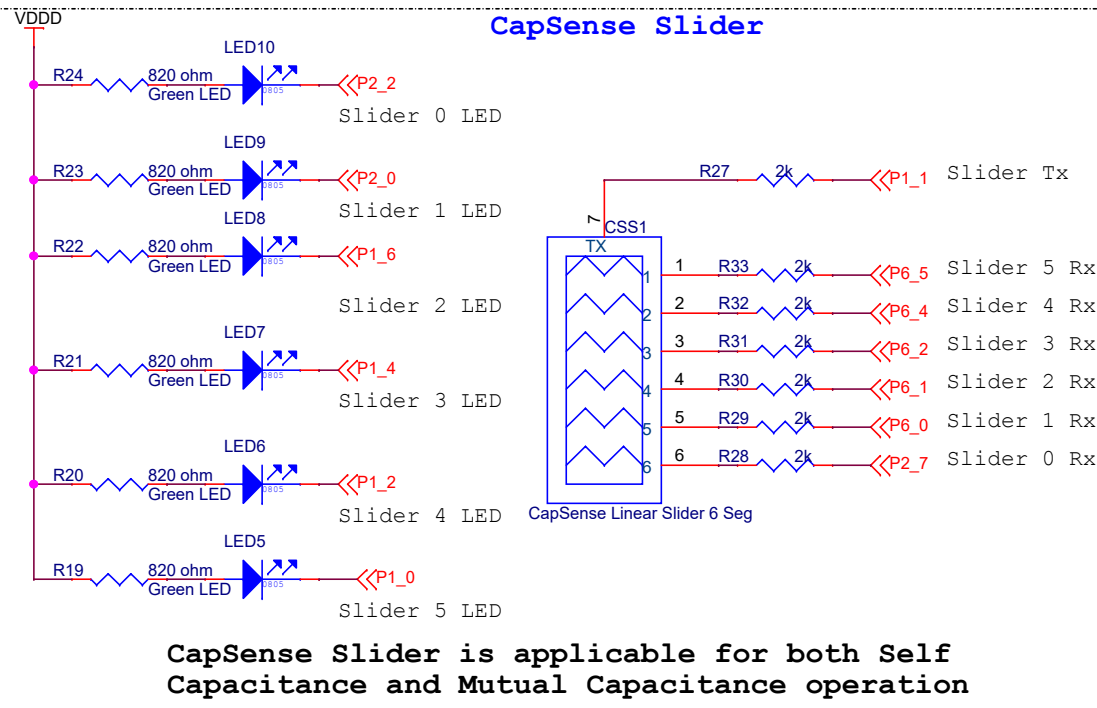
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REVISION HISTORY

Rev	DESCRIPTION	DATE
01	Testing	08/08/2017
02	Initial Release	08/14/2017
03	Alpha Release	09/08/2017
04	Beta Release	10/4/2017



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