

CY8CKIT-041S-MAX PSoC 4100S Max Pioneer Board

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Drawing Numbers

PCBA	121-60590-01
PCB	600-60590-01
FAB DRW	610-60590-01
ASSY DRW	620-60590-01
SCH DRW	630-60590-01

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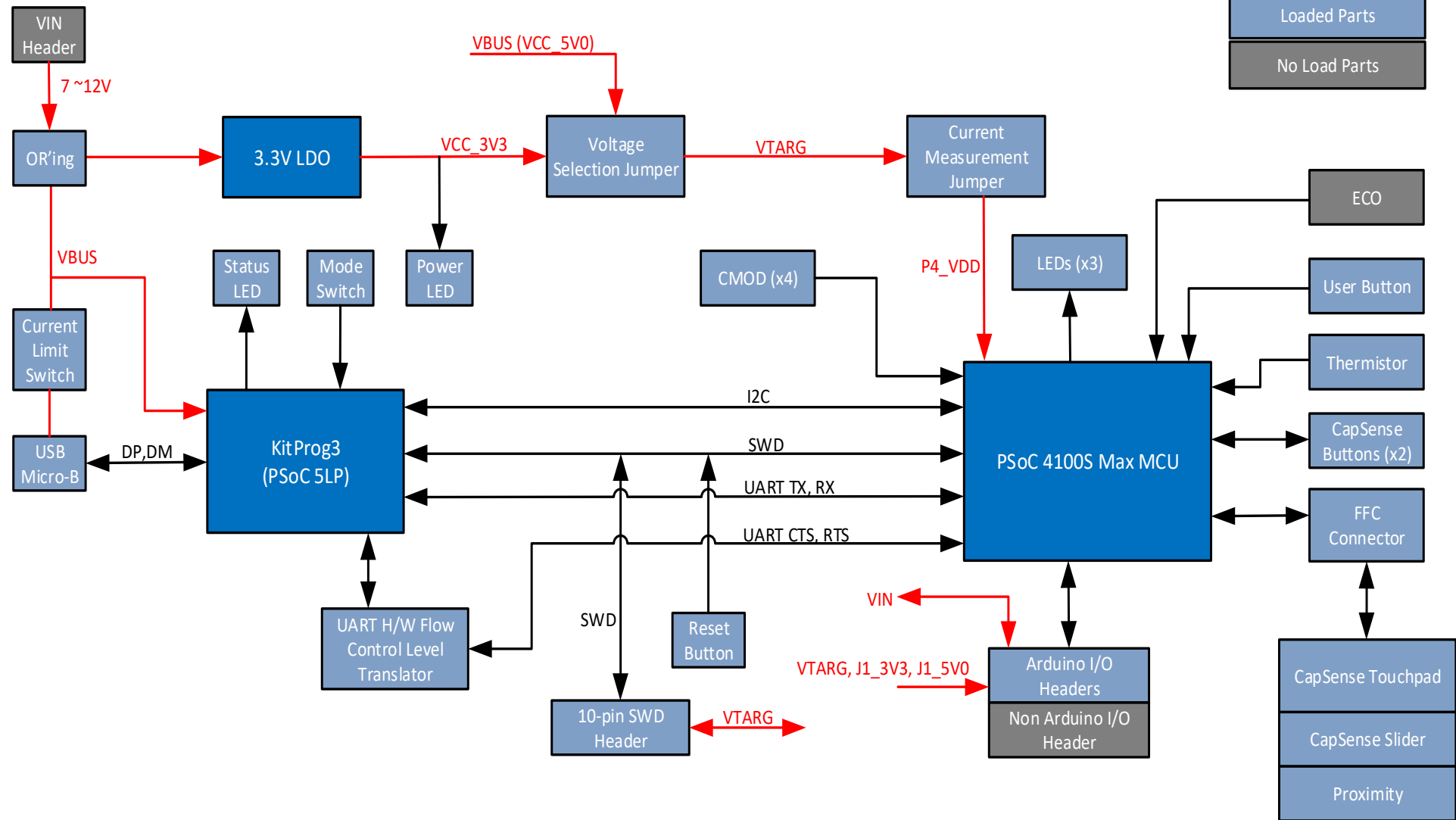


SCH Title : PSoC 4100S Max Pioneer Board

Page Title: Title Page

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CY8CKIT-041S-MAX PSoC 4100S Max Pioneer Kit Functional Block Diagram



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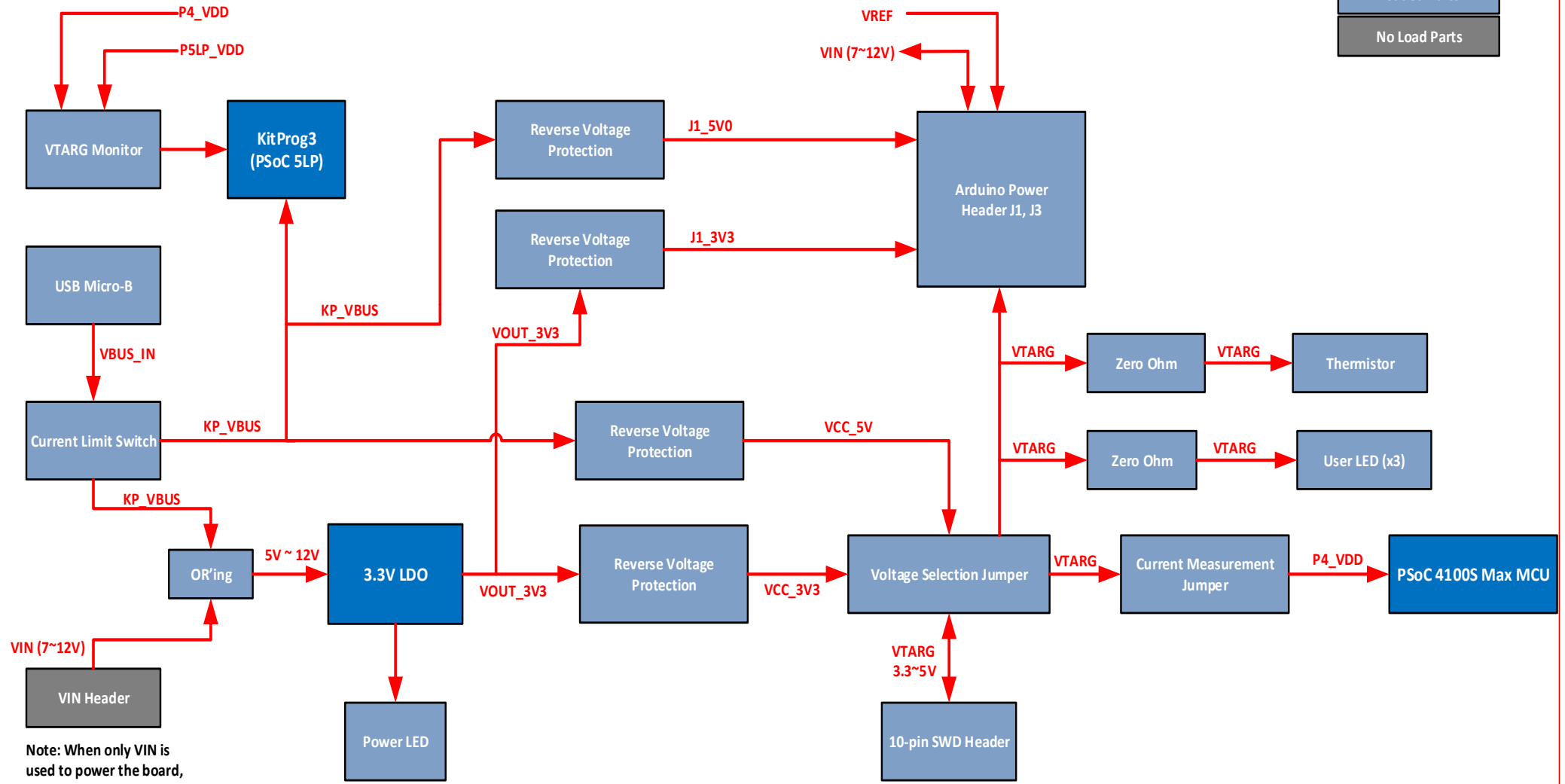
SCH Title : PSoC 4100S Max Pioneer Board

Page Title : Functional Block Diagram

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CY8CKIT-041S-MAX PSoC 4100S Max Pioneer Kit Power Supply Block Diagram

Infinion/Cypress Parts
Loaded Parts
No Load Parts



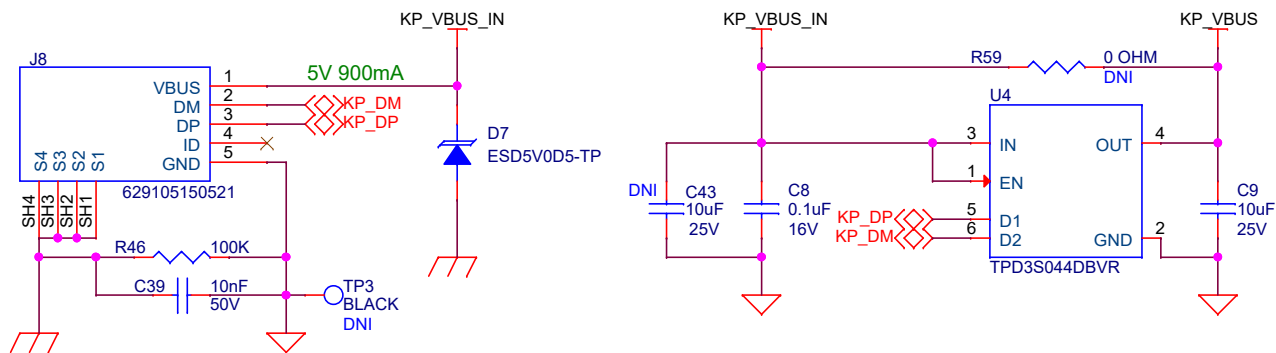
Note: When only VIN is used to power the board, on-board 5V will not be available.

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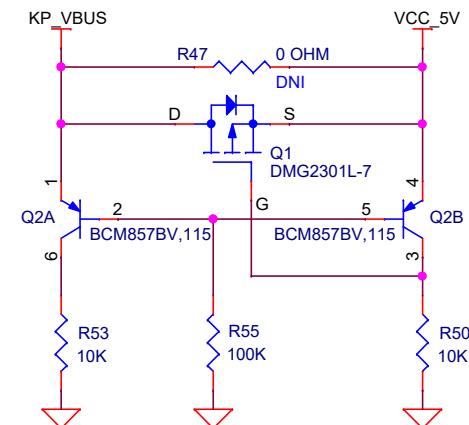


SCH Title : PSoC 4100S Max Pioneer Board				
Page Title: Power Supply Block Diagram				
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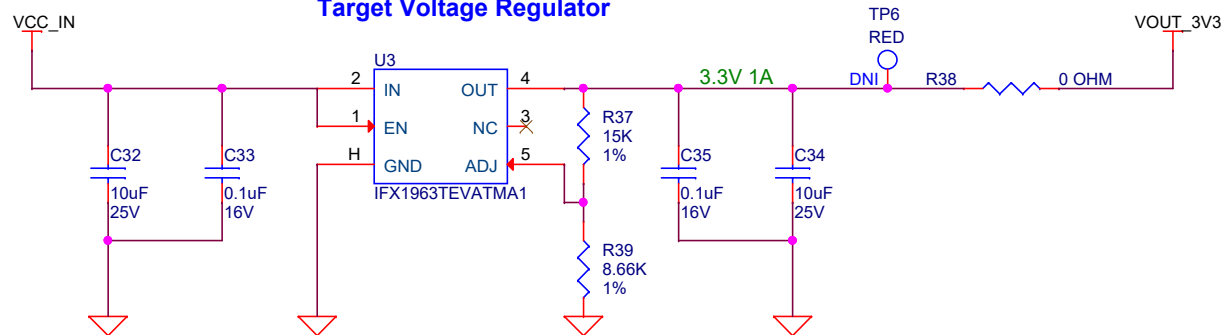
KitProg3 USB Micro-B Connector



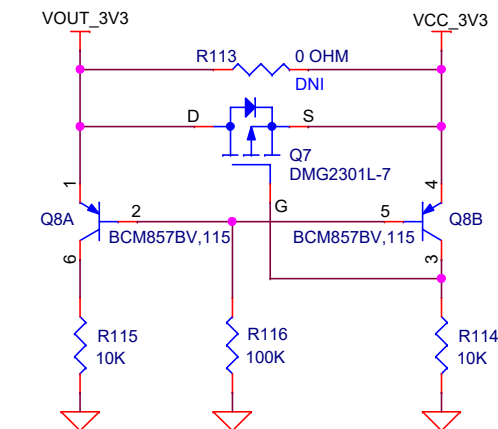
KP_VBUS Reverse Voltage Protection



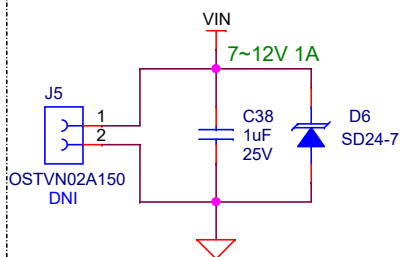
Target Voltage Regulator



VOUT_3V3 Reverse Voltage Protection

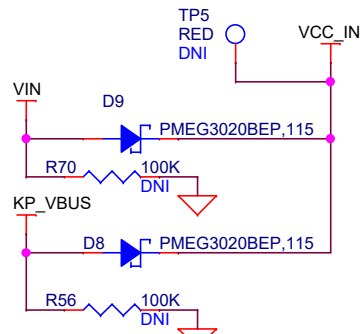


VIN Header

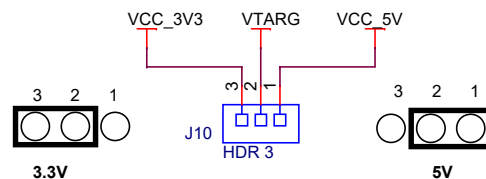


Note: VIN is supported upto max input voltage of 20V.

Power Supply 'OR'ing

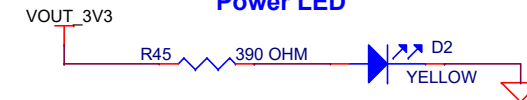


VTARG Voltage Selection Header



Note: Default Jumper setting is set to 5V. Do not remove jumper when powered.

Power LED



Note: LED does not light up when powered through external programmer.

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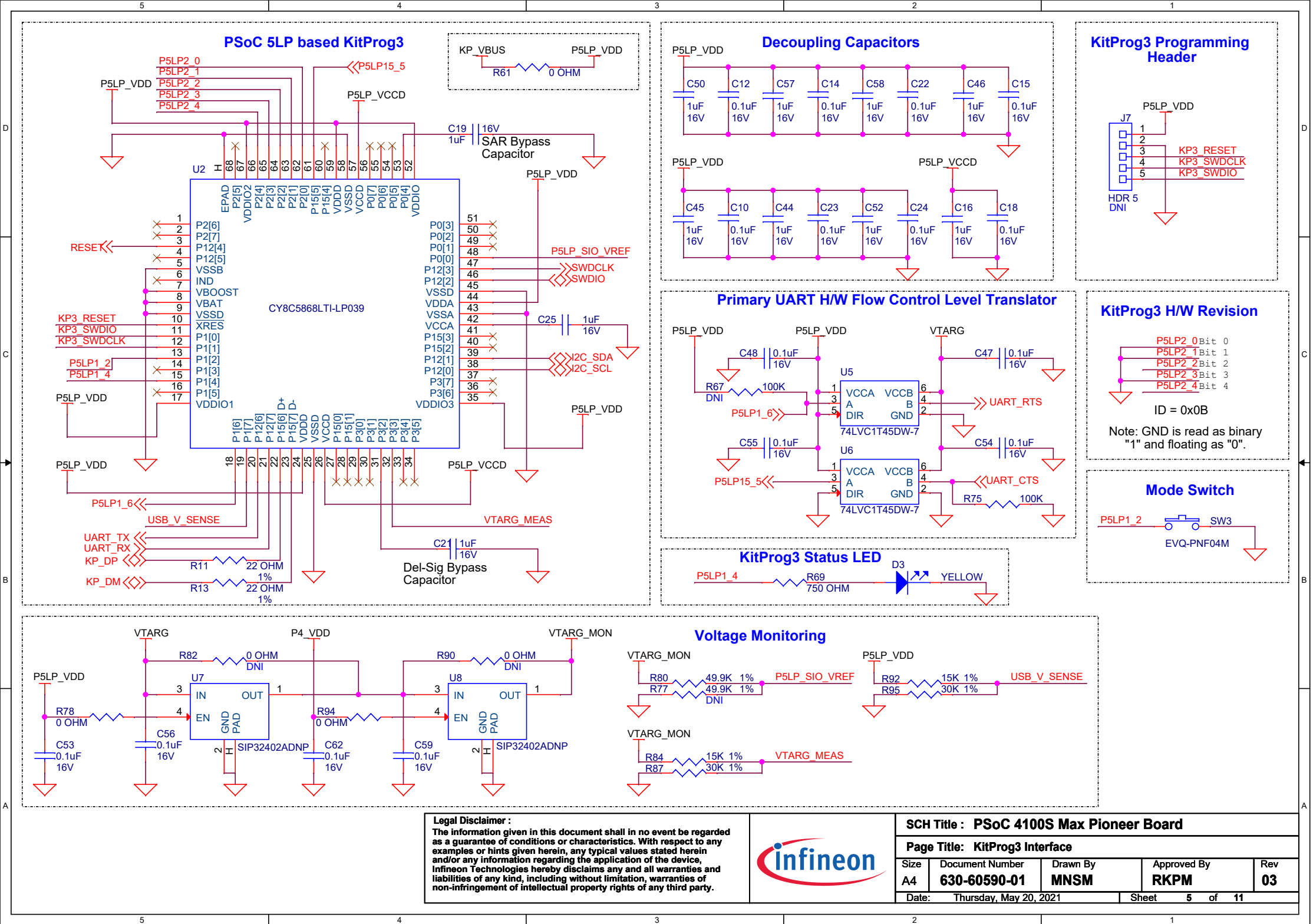
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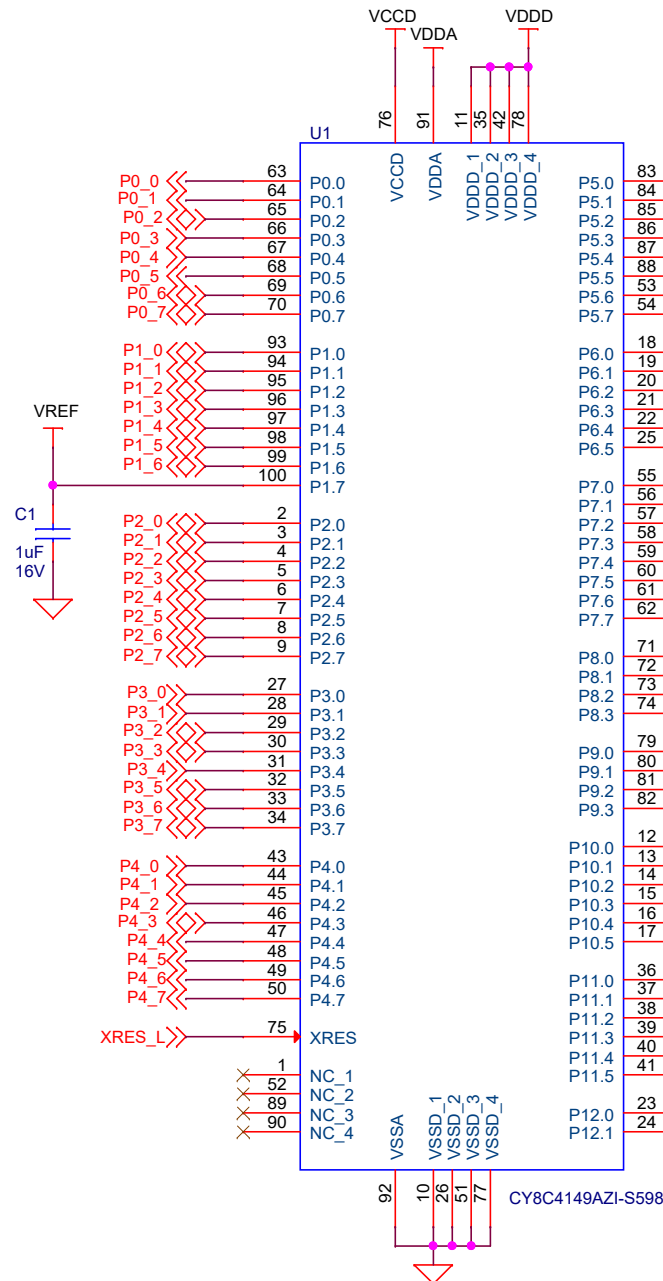
SCH Title : PSoC 4100S Max Pioneer Board

Page Title : Power Supply

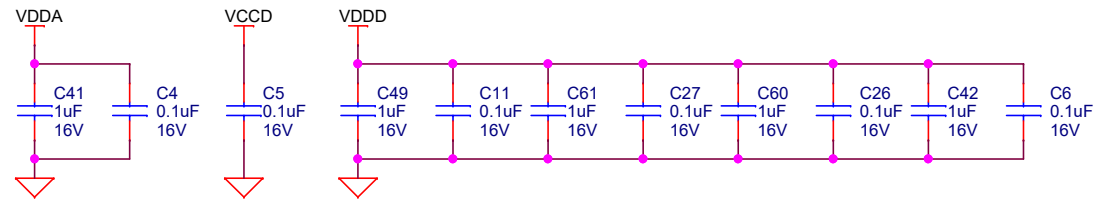
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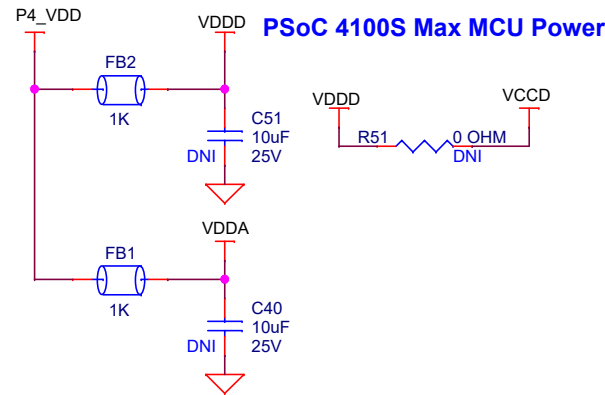
PSoC 4100S Max MCU I/Os



Decoupling Capacitors

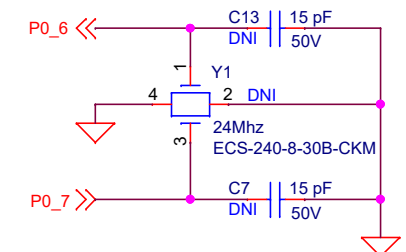


PSoC 4100S Max MCU Power



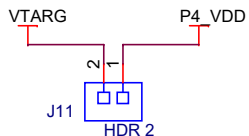
VDDD and VCCD pins should be shorted together and bypassed if 1.71 to 1.89V external supply is provided on VDDD.

ECO



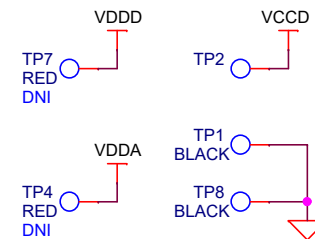
When using ECO, disconnect R9 and R10 resistors connected to UART H/W Flow Control.

Current Measurement Header



Note: Do not remove jumper when powered.

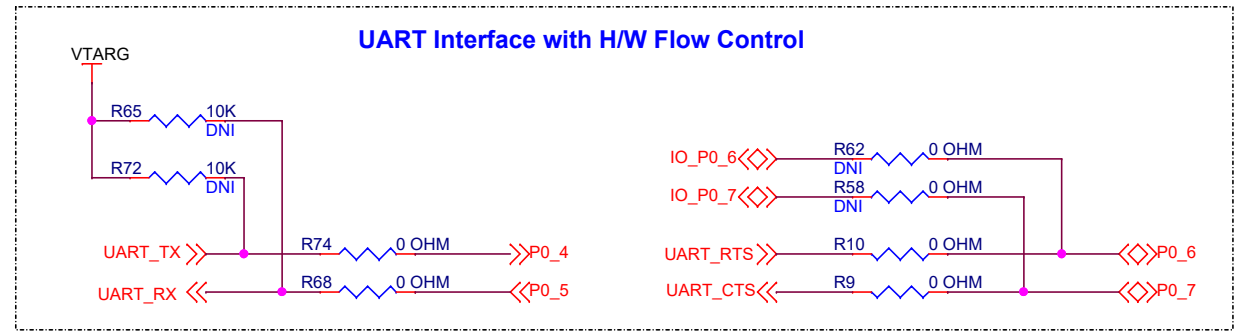
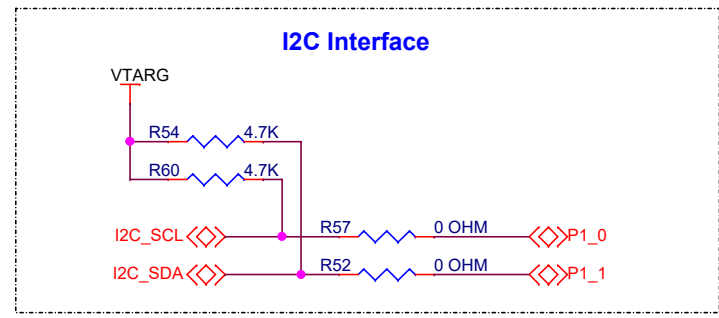
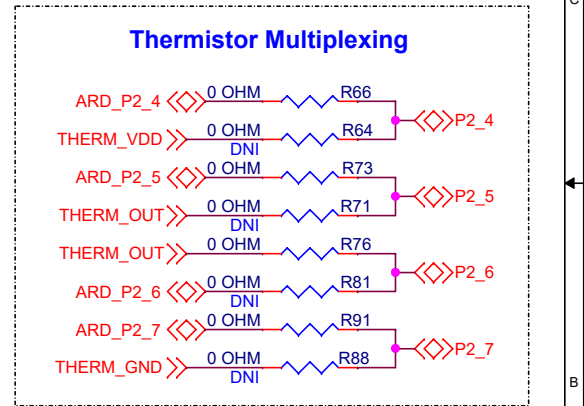
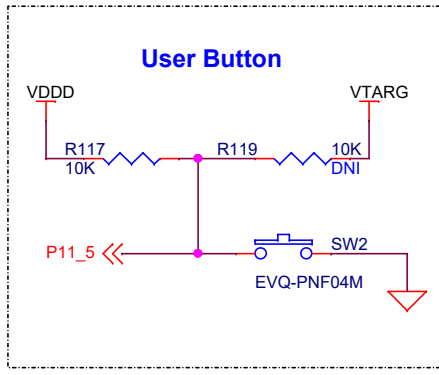
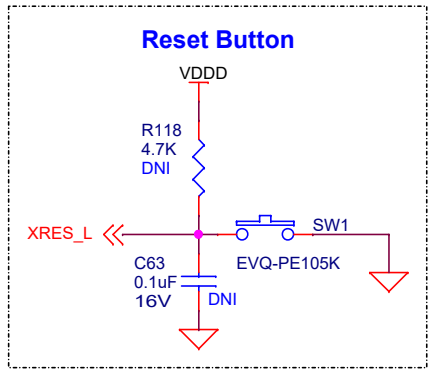
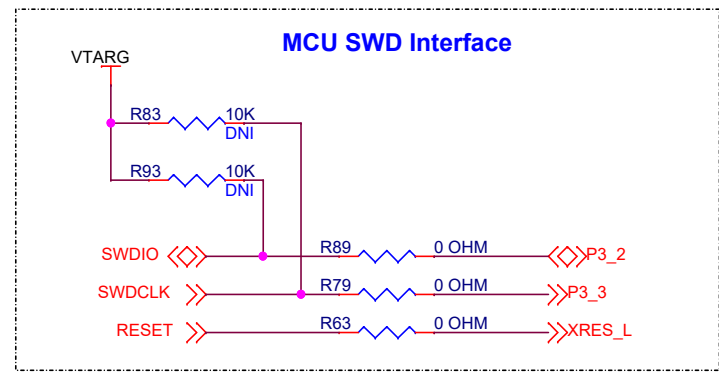
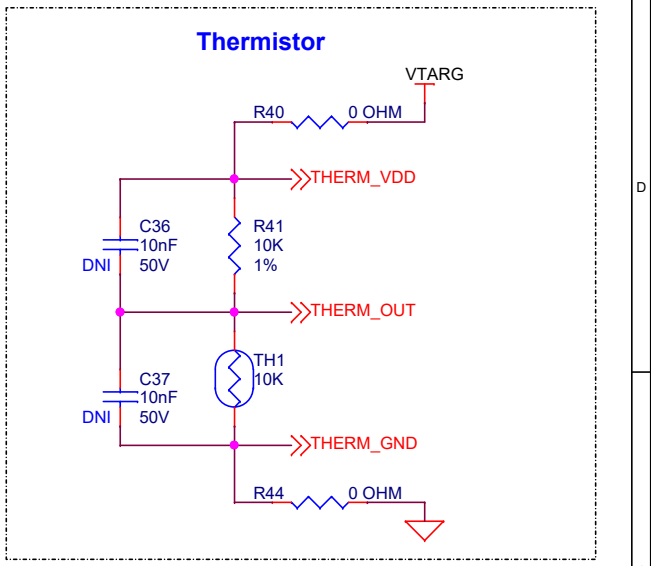
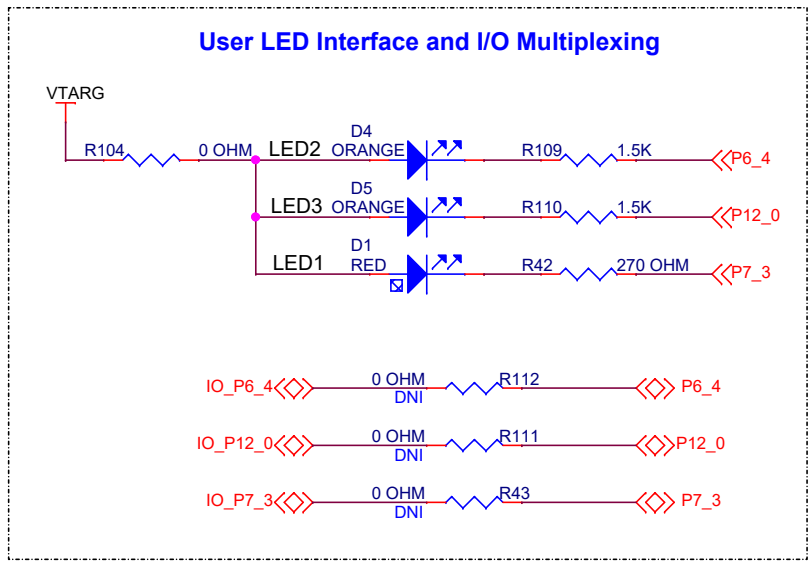
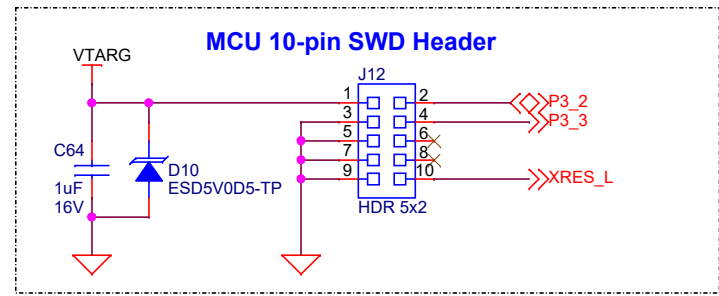
Test Points



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SCH Title : PSoC 4100S Max Pioneer Board				
Page Title : PSoC 4100S Max Interface				
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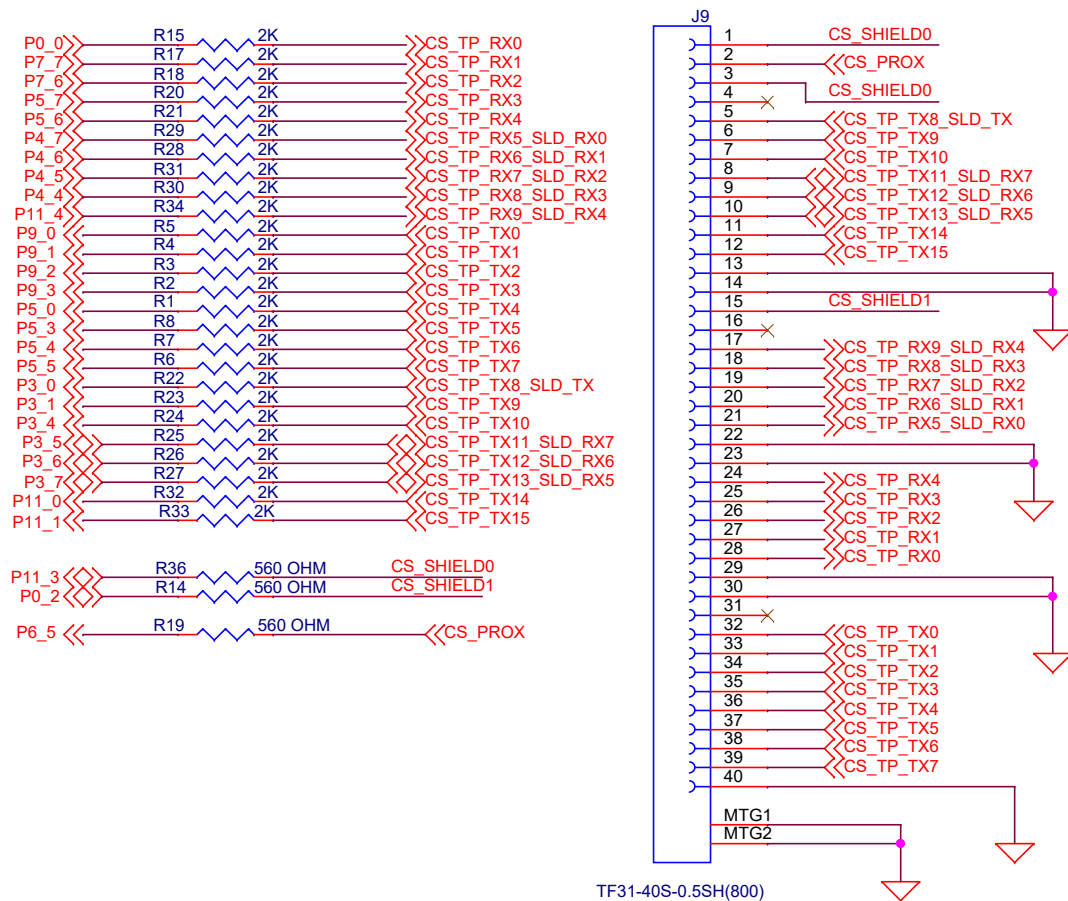


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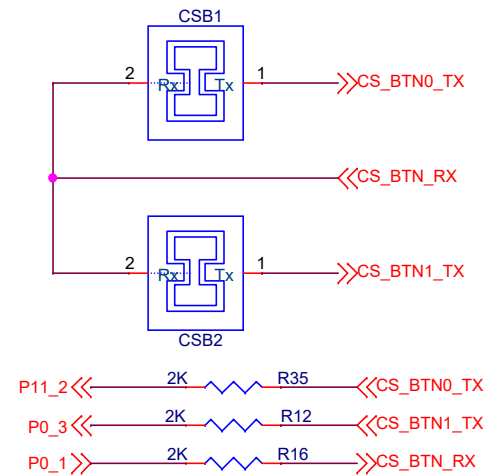


SCH Title : PSoC 4100S Max Pioneer Board				
Page Title : On-board Peripheral Interface				
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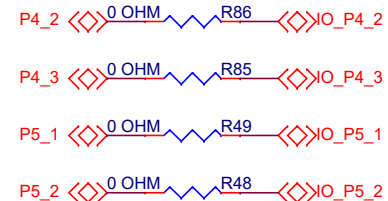
CapSense Touchpad, Slider and Proximity Interface



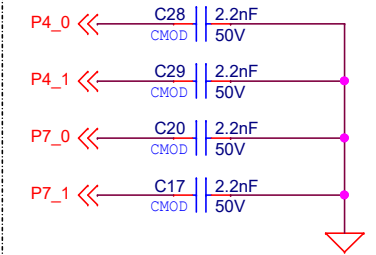
CapSense Buttons



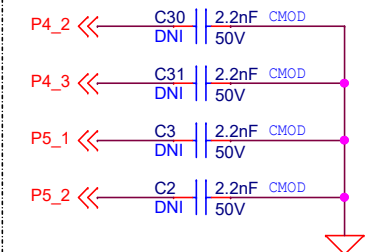
CMOD I/O Multiplexing



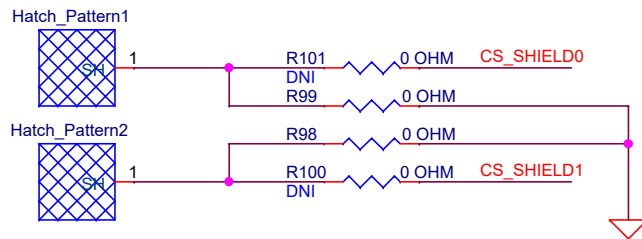
CMOD interface



Alternate CMOD interface



CapSense Buttons Shield



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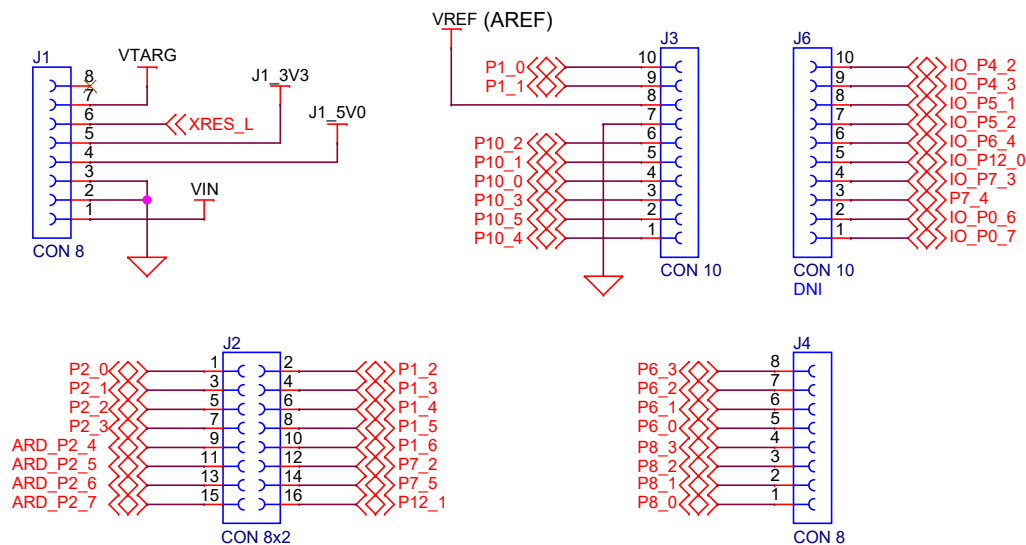


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Page Title : CapSense Interface

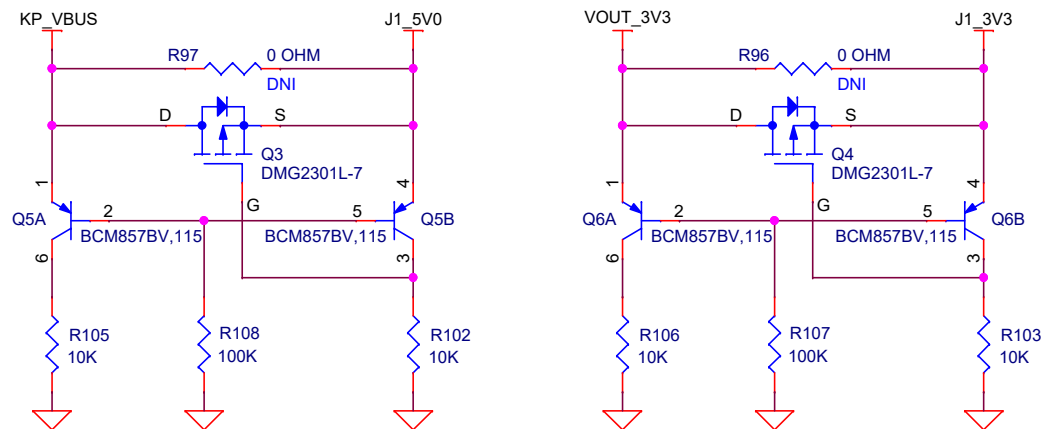
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Arduino & Expansion Headers



Note: 5V on Arduino Header will be present only when KitProg3 USB is connected.
Avoid external supply to J1.7 VTARG. This may cause permanent damage to the on-board components.

Reverse Voltage Protection for Arduino Header



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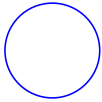
Page Title: Arduino & Expansion Headers

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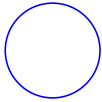
Accessories

Cylindrical Bump-ons

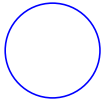
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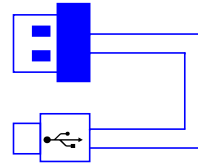
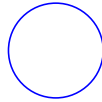
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SJ61A4



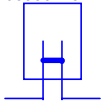
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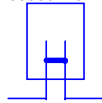
USB Micro-B Cable

Jumper Shunts

60900213421



60900213421



Acrylic Overlay

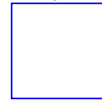
Overlay

Color: Clear, Transparent
Finish: Matt

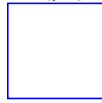
Dimensions:
Length = 43.19mm
Width= 19.43mm
Thickness = 3 mm

PCBA labels

LBL PCA Label



LBL QR Code



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Page Title: Accessories

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