Release Notes

CY3272 High Voltage Powerline Communication Evaluation Kit

Release Date: September 27, 2011

Thank you for your interest in the CY3272 High Voltage Powerline Communication (PLC) Evaluation Kit. This document lists kit contents, installation requirements, limitations, and known issues. It also provides links for updates, support, and additional information.

System Requirements and Recommendations

To evaluate this kit, a second high voltage PLC kit is required. The compatible kits are CY3272 (this kit) and CY3274 High Voltage PLC Development Kit. For information on these kits, go to: http://www.cypress.com/go/CY3272 and http://www.cypress.com/go/CY3274.

The following requirements are necessary to run the PLC Control Panel GUI for evaluating the PLC kits. They are also required to run the PSoC Designer™ development tool for developing a host application on an interfacing PSoC® device. PSoC Designer and PSoC Programmer are not required for evaluating these kits. If your computer does not have the minimum versions of .NET Framework and Windows Installer, the PLC Control Panel GUI installation automatically installs them for you.

Hardware/Operating System Requirements	**Minimum**	**Recommended**
Processor speed	1 GHz	2 GHz
RAM	1 GB	2 GB
Free hard drive space	800 MB	1 GB
Screen resolution	1024 x 768	1280 x 1024
CD/DVD drive	✓	✓
USB	Full Speed	2.0 Hi-Speed
XP (SP2 or higher), Vista, or Windows 7	✓	✓
Software Prerequisites	**Minimum**	**Recommended**
Microsoft Internet Explorer	7	
Adobe Reader	6	9+
Windows Installer	3.1	
.NET Framework	2.5	
PSoC Programmer	3.12	
PSoC Designer	5.1	

Kit Contents

The CY3272 High Voltage Powerline Communication Evaluation Kit includes:

- CY3272 PLC evaluation board
- Five CY8CPLC10-PVXI samples
- AC power cable
- USB-I2C Bridge

- Ribbon cable for I^2C communication, external reset, and powering external board
- Retractable USB cable
- CY3272 Quick Start Guide
- CD containing:
  - Packet test software – PLC Control Panel GUI Application
  - PLC Control Panel Release Notes
  - CY3272 Release Notes
  - CY8CPLC10 datasheet
  - CY3272 evaluation board user guide
  - Application note: AN52478 - Designing an External Host Application for Cypress's Powerline Communication IC CY8CPLC10
  - CY3272 board Altium design project
  - CY3272 board schematics, layouts and BOM.

Installation

To install the CD contents on your hard drive, insert the kit CD into your PC's CD-ROM drive. If the installer does not start automatically, run *cyautorun.exe* in the root directory of the CD. In the installer window, click 'Install CY3272 High Voltage Kit…' and follow the installation instructions.

To install the PLC Control Panel GUI, in the installer window, click 'Install PLC Control Panel GUI…' and follow the installation instructions.

Note If you have a previous installation of the PLC Control Panel GUI, uninstall it first. To uninstall the software, go to **Start** > **Control Panel** > **Add or Remove Programs** and click the **Remove** button adjacent to the particular software. Follow the instructions to uninstall.

Updates

Go to http://www.cypress.com/go/CY3272 for the latest software downloads and documents.

Limitations and Known Issues

The following table lists the known issue with the CY3272 Rev. ** board.

Defect	Workaround
The silkscreen labels for the I2C SDA and SCL pull-up headers are swapped. In typical situations, jumper shunts are placed across both headers or across neither, so this does not have an impact. The 5-pin header (J5) has the correct mapping of the I2C pins (SCL on pin 4, SDA on pin 5).	If you use these headers to probe the I2C lines, note that the SDA header has the clock and the SCL header has the data.

Technical Support

For assistance, go to http://www.cypress.com/go/support or contact our customer support at +1(800) 541-4736 Ext. 8 (in the USA), or +1 (408) 943-2600 Ext. 8 (International).

Additional Information

- For more information on PLC (application notes, software, kits, and so on), visit http://www.cypress.com/go/plc
- For more information about PSoC Designer functionality and releases, review the user guide and release notes on the PSoC Designer web page: http://www.cypress.com/go/psocdesigner
- For more information about PSoC Programmer, supported hardware, and COM layer, visit the PSoC Programmer web page: http://www.cypress.com/go/psocprogrammer
- For a list of trainings on PSoC Designer, visit http://www.cypress.com/?rID=40543

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