

Release Notes

CY3218-CAEXP2 CapSense® Express™ Kit

Release Date: June 15, 2015

Thank you for your interest in the CY3218-CAEXP2 CapSense® Express™ Kit. This document lists installation requirements, software and hardware updates, limitations, and known issues with the kit.

System Requirements and Recommendations

Hardware/Operating System Requirements	Minimum	Recommended
Processor speed	1 GHz	2 GHz
RAM	1 GB	2 GB
Free hard drive space	800 MB	1 GB
Screen resolution	1024 x 768	1280 x 1024
CD/DVD drive	✓	✓
USB	Full Speed	2.0 Hi-Speed
Windows XP (SP3 or higher), Windows Vista, Windows 7	✓	✓
Software Prerequisites	Minimum	Recommended
Microsoft Internet Explorer (Internet Explorer 8 beta version is not supported)	7	Latest
Adobe Reader (for PDF documents)	6	9+
Windows Installer	3.1	Latest
.NET Framework	2.0 SP1	Latest
PSoC Designer	5.0 SP6	5.0 SP6
PSoC® Programmer	3.13.3	Latest*

Installation

To install, insert the kit CD into your PC's CD-ROM drive. If the installer does not start automatically, run *cyautorun.exe* in the root directory of the CD/DVD. Follow the installation instructions.

Updates

Check <http://www.cypress.com/go/CY3218-CAEXP2> for the latest downloads of software and documentation.

Kit Revision and New Features

Code examples for the CY3218-CAEXP2 kit are supported in PSoC Designer 5.0 SP6. The onboard mechanical button can be used as input in both resistive pull-up and open drain GPIO drive modes, based on the fix provided in this revision of the kit. See the errata provided in the kit CD for more details.

Limitations and Known Issues

If the drive mode of GPIO, which is connected to the mechanical button, is set to resistive pull-up, the mechanical button input does not function as intended. The button is expected to provide logic 0 during inactive state (not pressed) and logic 1 during active state (pressed). In the original design (121R_45900 Rev**), the button provides logic 1 in both states.

All projects using the mechanical button input in combination with resistive pull-up drive mode are affected. The kit circuit is modified with manual rework to fix this defect. With this fix, the mechanical button can be used as input in both resistive pull-up and open drain drive mode of GPIO.

Due to this defect, the mechanical button input cannot be used to 'wake up the CapSense Controller from sleep'. With this fix, the mechanical button can be used as 'wake up from sleep' input signal.

Documentation

Kit documents are located in the CD, in the following location: <CD_Root>\Documentation

Documents include:

- CY3218-CAPEXP2 Kit_User_Guide.pdf
- CY3218-CAPEXP2 Quick_Start_Guide.pdf
- CY3218-CAPEXP2 Release_Notes.pdf
- CY3218-CAPEXP2 Errata.pdf

Silicon Errata

The most up-to-date versions of the silicon errata are available in the device datasheets on the web site at <http://www.cypress.com>.

For assistance go to <http://www.cypress.com> or contact our Applications team at +1(425) 787-4814

Technical Support

For assistance, go to <http://www.cypress.com/go/support> or contact our customer support at +1(800) 541-4736 Ext. 2 (in the USA), or +1 (408) 943-2600 Ext. 2 (International).

Additional Information

- For more information about PSoC Designer functionality and releases, review the user guide and release notes on the PSoC Designer web page: <http://www.cypress.com/go/psocdesigner>
- For more information about PSoC Programmer, supported hardware, and COM layer, visit the PSoC Programmer web page: <http://www.cypress.com/go/psocprogrammer>
- For getting started with CY8C201xx Configuration tool, PSoC Designer 5.0 SP6, refer to [AN42137 - CapSense™ Express Software Tool](#)



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