Code Example for SmartSense™ User Module in PSoC® 1

Project Name: CE85976
Programming Language: C
Associated Part Families: CY8C22x45, CY8C21x45
Software Version: PSoC® Designer™ 5.4
Related Hardware: CY3280-22x45 UCC Board,CY3240-I2USB Bridge or MiniProg3, MiniProg1 or MiniProg3
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Project Objective

This project demonstrates the operation of the SmartSense2X User Module (UM) of the PSoC 1 device.

Overview

The proper operation of capacitive sensors requires you to manually tune several parameters depending on the parasitic capacitance (C_P). The SmartSense UM improves and expands the basic architecture of the CSD UM. A key feature is SmartSense Auto-Tuning. The SmartSense UM optimizes the parasitic capacitance operational parameters during runtime based on the C_P of each sensor; you do not need to manually tune when you use SmartSense. This code example demonstrates how to use the SmartSense User Module. We use the EzI2Cs User Module to send CapSense® parameters, such as raw counts, difference counts, baselines of each button, and centroid position of slider, to the I^2C master.

User Module List and Placement

The following table lists the user modules used in this project and the hardware resources occupied by each user module.

User Module	Placement
EzI2Cs	No analog or digital block is used (A dedicated I^2C hardware resource is used)
SmartSense2X	Capsense0, ACE02 (SmartSense2X CMP)
LED_0	P2[5]
LED_1	P2[7]
LED_2	P0[1]
LED_3	P0[3]
LED_4	P1[2]

User Module Parameter Settings

The following tables show user module parameter settings for each of the user modules used in the project.

EzI2Cs		
Parameter	**Value**	**Comments**
Slave_Addr	0	This parameter assigns the Slave address. The value assigned can be any value from 0 to 127 (decimal)
Address_Type	Static	Slave address is not dynamically varied.
ROM_Registers	Disable	ROM read is not used in this example.
I2C_Clock	100K Standard	Master and slave are communicating at a speed of 100 KHz during Byte transfer phase.
I2C_Pin	P[1]0-P[1]1	P[1]1 → SCLK P[1]0 → SDATA

SmartSense2X		
Parameter	**Value**	**Comments**
Sensors Autoreset	Disabled	
Debounce	3	Sensor is ON only when it is active for 3 scans
Background Scanning	Enabled	Relieve the CPU when scanning is in progress
Buttons	5	SW0- P3[1]
		SW1- P1[3]
		SW2- P3[3]
		SW3- P2[1]
		SW4- P2[3]
Buttons Sensitivity	0.4(Med-Low)	
Sliders	1	
Slider Sensors Count	10	S1(0)-P1[4]
		S1(1)-P0[6]
		S1(2)-P0[4]
		S1(3)-P0[2]
		S1(4)-P2[6]
		S1(5)-P2[4]
		S1(6)-P2[2]
		S1(7)-P2[0],
		S1(8)-P3[2],
		S1(9)-P3[0]
Slider Resolution	50	
Diplex	False	
Slider Sensitivity Level	0.3(Medium)	
Radial Sliders	0	
Proximity Count	0	
Shield Electrode Output	GOO[6] to P1[6]	
Modulator Capacitor Pin	P0[5]	
Threshold Setting	Auto tune based on Sensitivity	

LED		
Parameter	**Value**	**Comments**
Drive	Active Low	
LED_0	P2[5]	
LED_1	P2[7]	
LED_2	P0[1]	
LED_3	P0[3]	
LED_4	P1[3]	

Global Resources

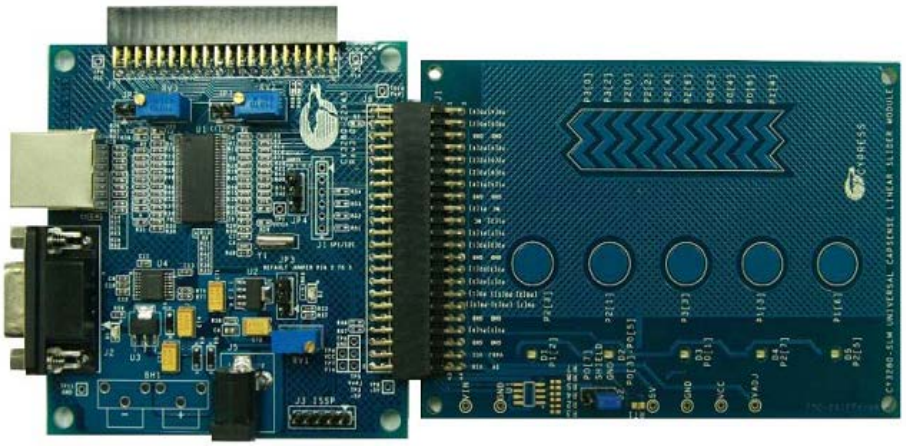
Important Global Resources		
Parameter	**Value**	**Comments**
Power Setting (VCC/Sys.Clock Frequency)	5.0 V/24 MHz	Sys.Clock = 24-MHz clock source accuracy is trimmed for 5-V power setting.
CPU Clock	SysClk/2	Sets the CPU frequency to 12 MHz
32K_Select	Internal	
PLL_Mode	Disable	

All other global resources are left at their default, as they are not specific to this project.

Hardware Connections

1. Connect the CY3280-SLM Universal CapSense Linear Slider Module board to the parent board as shown in Figure 1.

   Figure 1. Connecting the CY3280-SLM Universal CapSense Linear Slider Module Board to the Parent Board



2. Place shunts on pins 2 and 3 of JP3 and pins 1 and 2 of JP4 (default setting).
3. Connect your computer to the CapSense test board's in-system serial programming (ISSP) connector (J3) using the MiniProg1 or MiniProg3 and a USB cable. If you are using the MiniProg1 or MiniProg3 for the first time, install the driver using the following steps before proceeding:
   a. When the **Found New Hardwar**e Wizard opens, select the **Install the software automatically (Recommended)** option and click **Next**.
   b. A warning message may tell you the software you are trying to install has not passed Windows Logo testing. Click **Continue Anyway** each time it appears.
   c. When the installation is complete, click **Finish**.
4. Click **Start** > **All Programs** > **Cypress** > **PSoC Programmer <version xxx>** > **PSoC Programmer <version xxx>**.
5. Click **File Load**, navigate to and open the *CY3280_22x45_SmartSense_Example_Project.hex* file.
6. From the **Device Family** menu, select CY8C22X45.
7. From the **Device** menu, select CY8C22545-24AXI.
8. Click **Program**. **"Programming Succeeded..."** appears in the Actions pane when programming is complete.

Operation

Upon program execution, all hardware settings from the device configuration are loaded into the device and the code is executed.

The following operations are performed in *main.c*.

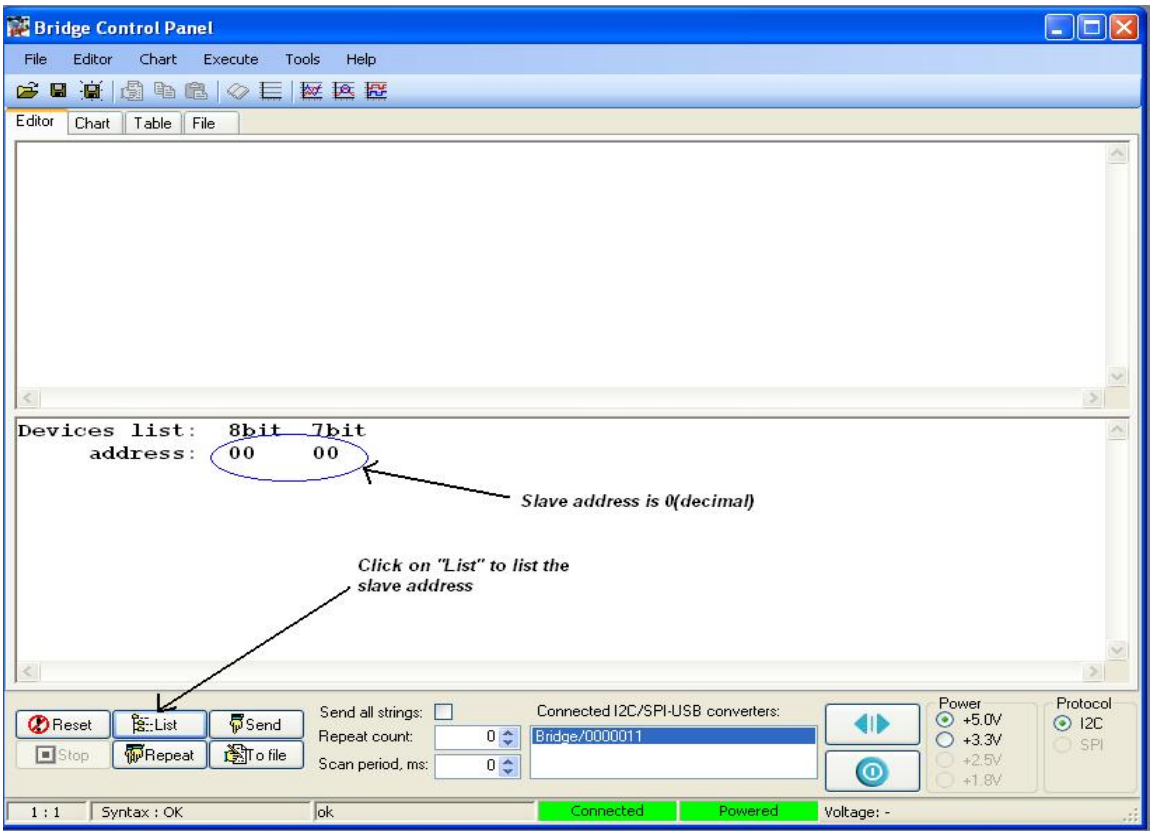
1. Enable Global interrupts.
2. Set EzI2Cs buffer - exposes 5 bytes of RAM locations to the I^2C master using EzI2Cs_SetRamBuffer API and starts EzI2Cs.
3. Start SmartSense2X and initialize baselines.
4. Set scanningFlag to scan first sensor.
5. If scanningFlag is scan first sensor, initialize capSensorNo to 0 (first sensor index), scan it, and set scanningFlag to first sensor scan done.
6. If scanningFlag is first sensor scan done and scanning is over, clear the scan complete flag, update sensor baseline, and check if sensor is active.
7. Increment capSensorNo.
8. If capsensorNo is less than total sensors, scan the next sensor; if not, get slider centroid, control LEDs and set scanningFlag to scan first sensor.
9. Load the fresh data (rawcount, baseline, diff, and centroid) in the I^2C buffer.
10. Go to step 5.

Testing the Project

1. Touch any button; the corresponding LED turns ON. When the finger is removed, the LED turns OFF.
2. Touch the slider. As the finger moves upwards, the number LEDs that are ON will increase. When the last segment of the slider is touched, all the LEDs are turned ON.
3. To test the SmartSense working, use the CY3240-I2USB Bridge or MiniProg3 with associated Bridge Control Panel software as the master. Make external jumper connections in the slave side as explained in the section Hardware Connections.
4. Connect the CY3240-I2USB Bridge or MiniProg3 between the PSoC device (using the ISSP header) and the PC (using the USB port). The following settings are required to enter communication commands in the Bridge Control Panel software.
    a. Open the Bridge Control Panel software from the path Start > All Programs > Cypress > Bridge Control Panel 1.6 > Bridge Control Panel 1.6.
    b. Select I^2C Speed of 100 K in the path Tools > Protocol Configuration > I2C in the Bridge Control Panel software.
    c. Select +5 V in the ‘Power Selection’ panel, which is the window below the ‘Results’ window, and click on the ‘Toggle Power’ button (This powers the target device with +5 V).

5. Click on **List** in the window that appears below the **Results** window. This lists the slave address in the **Results** window.

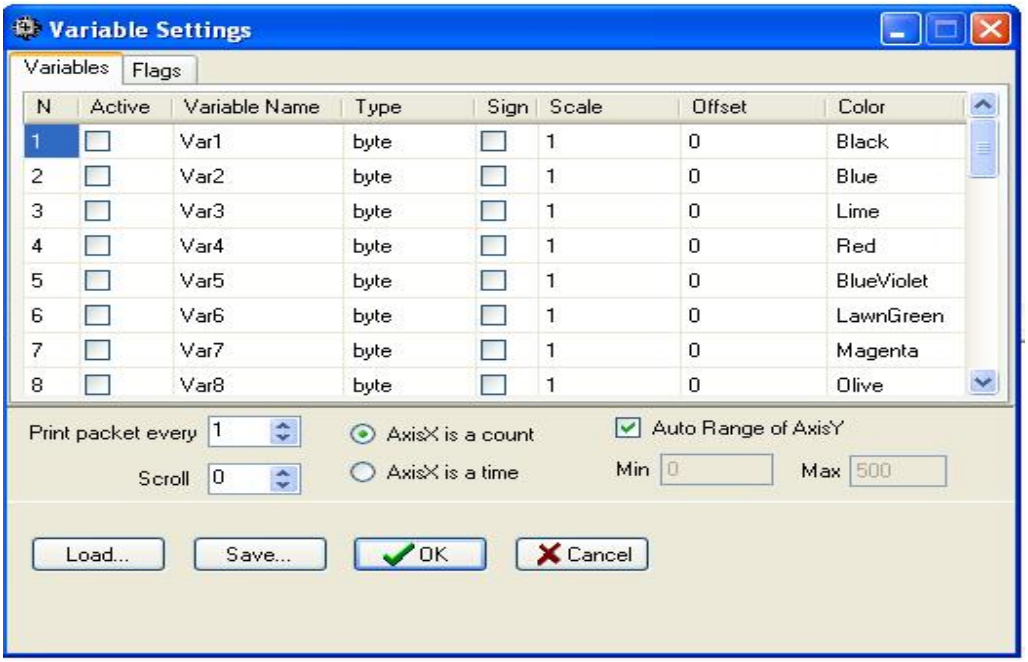
Figure 2. Bridge Control Panel Showing the slave Address



To set the variables in the Bridge Control Panel software, follow these steps:

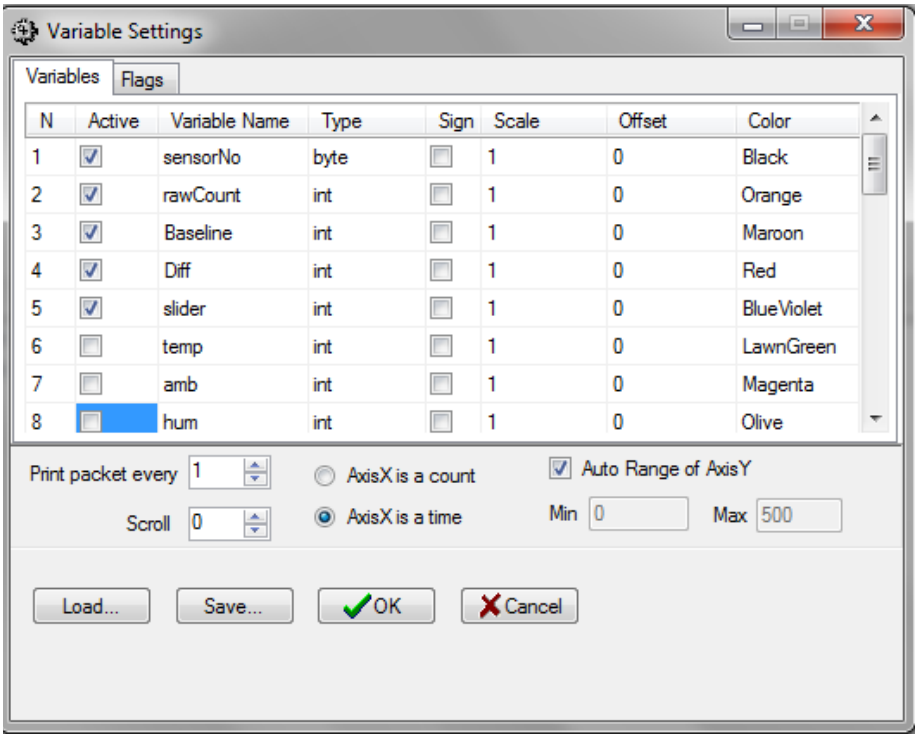
1. Click on **Chart** and select **Variable Settings**. The following window opens.

Figure 3. Variable Settings Window



2. Enter the Variable Name, Type, and Sign as shown in the following screenshot and activate all the variables by checking the box provided.

Figure 4. Variable Settings



3. Click OK.

   Note To get the Help document for the Bridge Control Panel software, follow the path **Help** > **Help Contents** in the Bridge Control Panel software.

4. To read a particular sensor's raw count, baseline, and diff value, do the following:

   - First, write the sensor number in the first byte of the I^2C buffer.
   - Next, read the value.

The following screenshot shows the byte values read by the master.

Figure 5. Bridge Control Panel

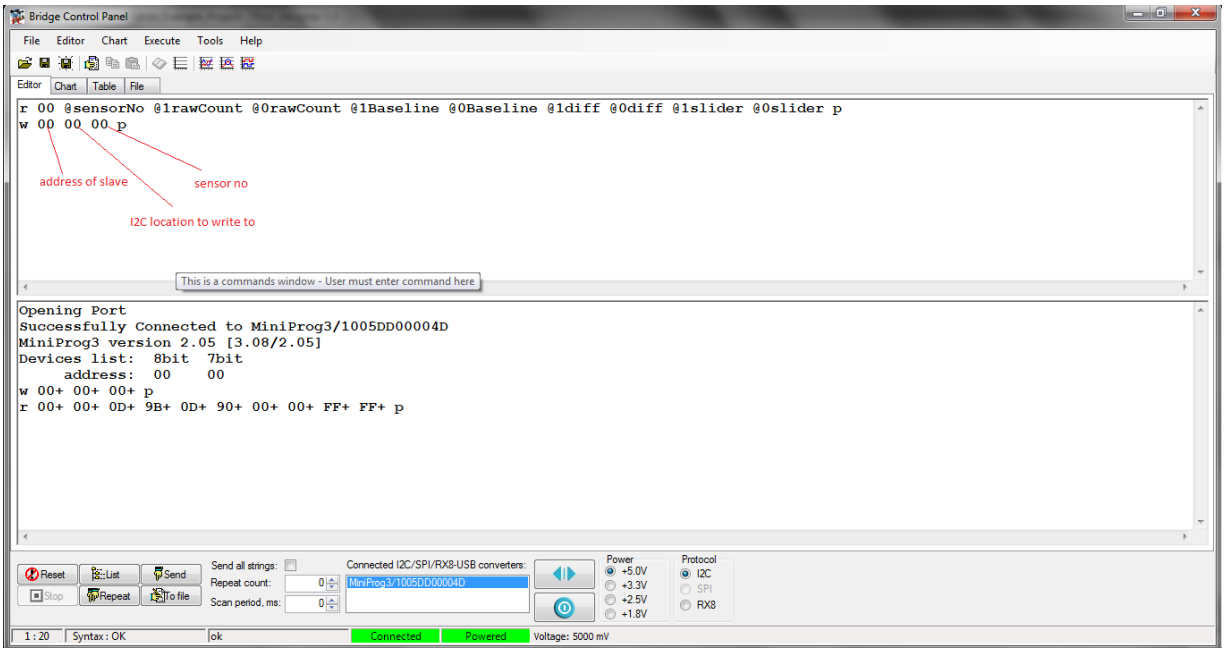
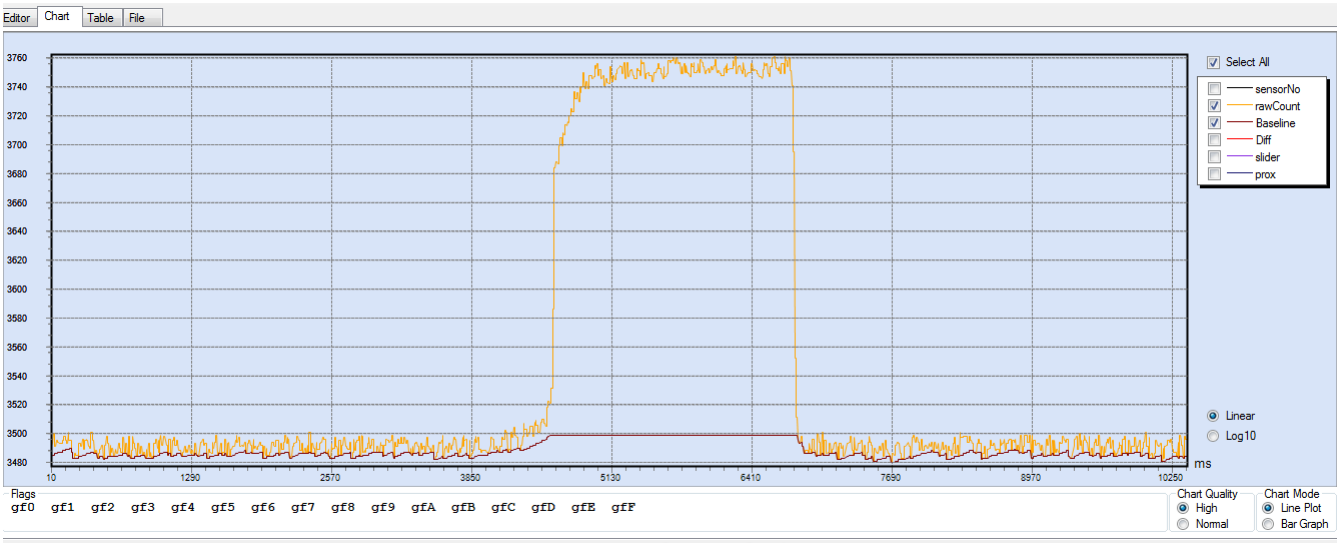


Figure 6. Change in rawcounts with finger touch



Document History

Document Title: Code Example for SmartSense™ User Module in PSoC® 1 - CE85976

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Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	3897918	KUK / TEJU	02/07/2013	New code example.
*A	4665772	ASRI	02/19/2015	Updated Software Version as “PSoC® Designer™ 5.4” in page 1. Updated to new template. Completing Sunset Review.
*B	4737457	SSHH	04/24/2015	Updated Document Title to read as “Code Example for SmartSense™ User Module in PSoC® 1 - CE85976”. Updated Related Hardware as “CY3280-22x45 UCC Board, CY3240-I2USB Bridge or MiniProg3, MiniProg1 or MiniProg3” in page 1. Replaced "I2CtoUSB Bridge" with "CY3240-I2USB bridge or MiniProg3" in all instances across the document.

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