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CE210289 - PSoC® 4 CapSense® Linear Slider

Objective

This example code demonstrates the PSoC 4 CapSense linear slider operation.

Overview

This code example implements a five-segment CapSense linear slider interface. The linear slider sensor debug data and slider position are read on a PC using the CapSense embedded Tuner GUI via I^2C communication. The CapSense tuner provides a quick and easy method for monitoring and updating CapSense parameters.

Requirements

Tool: PSoC Creator™ 4.1 and later versions

Programming Language: C (ARM® GCC 5.4.1, ARM MDK)

Associated Parts: All PSoC 4, PSoC 4 BLE and PRoC BLE devices

Related Hardware: CY8CKIT-042 and CY8CKIT-042 BLE

Design

This code example demonstrates the core functionality of the CapSense Component. Figure 1 shows the PSoC Creator schematics of this code example. This example uses the CapSense and EZI2C Slave Components.

The CapSense Component is configured with a five-segment linear slider widget. The project uses the SmartSense (Full Auto-Tune) tuning method to quickly implement a linear slider. The EZI2C Slave is used to monitor the sensor data and slider touch position information on a PC using the CapSense tuner available in the PSoC Creator integrated design environment (IDE) via I^2C communication.

The firmware flowchart is shown in Figure 2.

Figure 1. Top Design

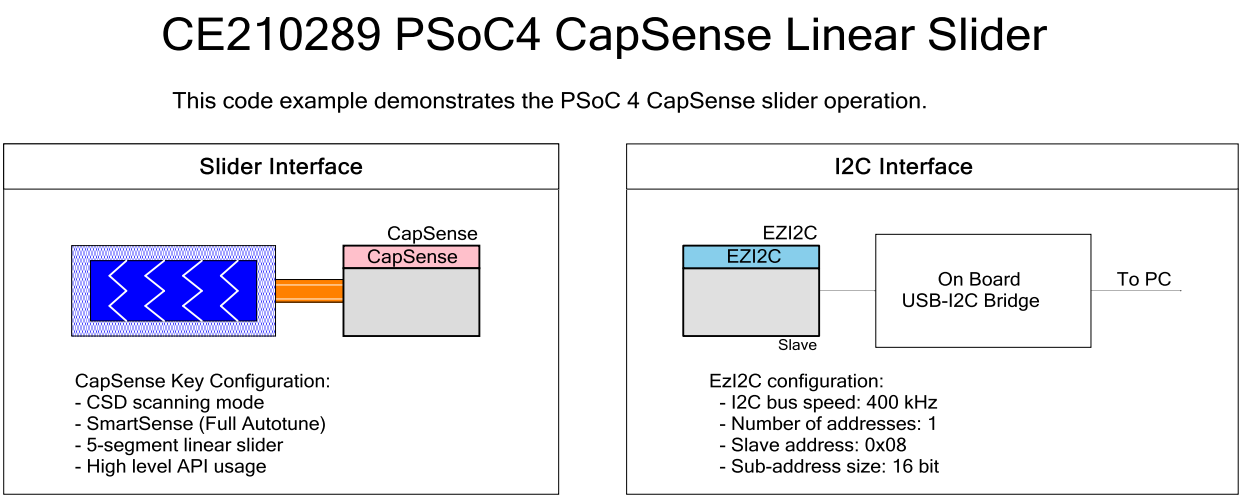
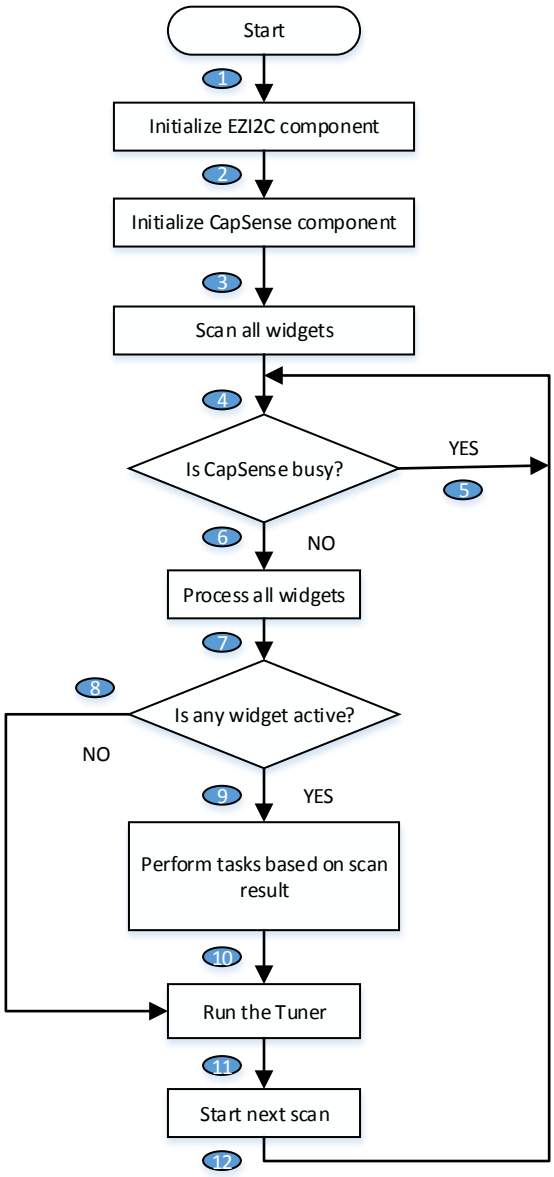


Figure 2. Firmware Flowchart



Design Considerations

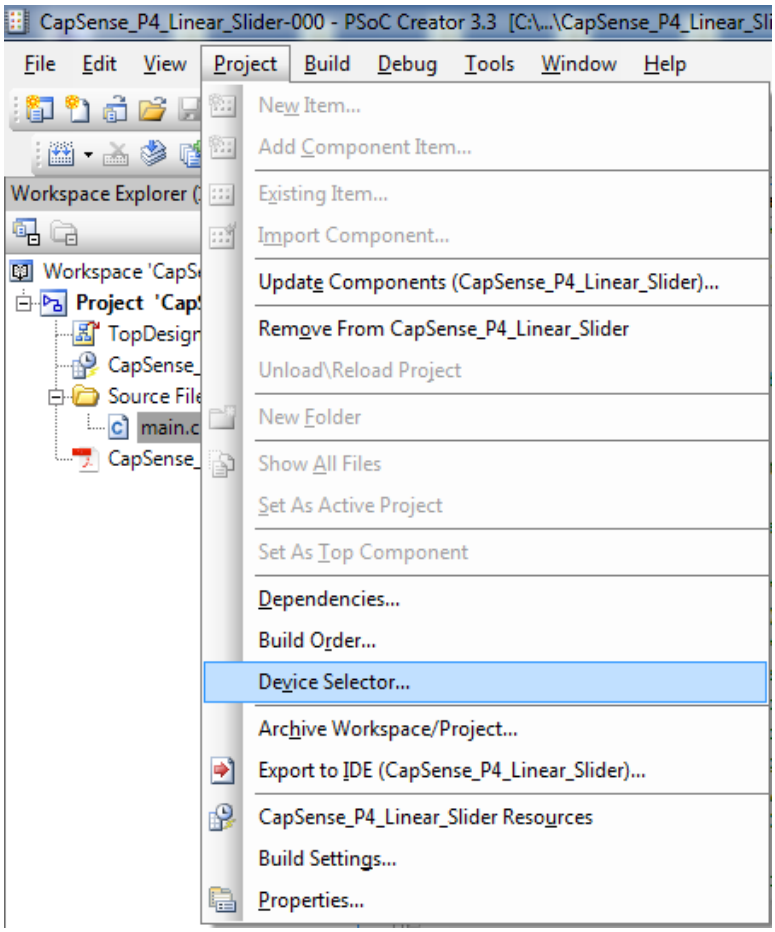
The code example is designed for the PSoC 4200 family and associated CY8CKIT-042 kit. The design is easily portable to other PSoC 4 devices and kits, typically by just changing the device and Components' pin assignment.

To switch from CY8CKIT-042 to other PSoC 4 pioneer kits, follow the steps below:

1. Select the appropriate device with the Device Selector called from the project's context menu (Figure 3). Table 1 lists the device number for each pioneer kit.
   When you select the device, pins are assigned automatically in design wide resource file according to the device selected.

   Note: If the assigned Component's pins are not as shown in Table 1 or you want to overwrite the existing pin assignment, double-click the project's design wide resource (DWR) file in the workspace explorer window and assign the pins. See the device datasheet to use other pins.

Figure 3. Select Device Selector from Project's Context Menu



2. Build the project and ensure that there are no errors or warnings.

Table 1. Pin Assignment for the PSoC4 CapSense Linear Slider Project

Pin name	Development Kit	
	CY8CKIT-042 (CY8C4245AXI-483)	CY8CKIT-042-BLE (CY8C4247LQI-BL483)/ (CYBL10563-56LQXI)
\Capsense: Cmod\	P4[2]	P4[0]
\CapSense: Sns[0]\ (LinearSlider0_Sns0)	P1[1]	P2[1]
\CapSense: Sns[1]\ (LinearSlider0_Sns1)	P1[2]	P2[2]
\CapSense: Sns[2]\ (LinearSlider0_Sns2)	P1[3]	P2[3]
\CapSense: Sns[3]\ (LinearSlider0_Sns3)	P1[4]	P2[4]

Pin name	Development Kit	
	CY8CKIT-042 (CY8C4245AXI-483)	CY8CKIT-042-BLE (CY8C4247LQI-BL483)/ (CYBL10563-56LQXI)
\CapSense: Sns[4]\ (LinearSlider0_Sns4)	P1[5]	P2[5]
\EZI2C: scl\	P3[0]	P3[5]
\EZI2C: sda\	P3[1]	P3[4]

PSoC Creator Component

Table 2 lists the PSoC Creator Components used in this example, as well as the hardware resources used by each component.

Table 2. List of PSoC Creator Components

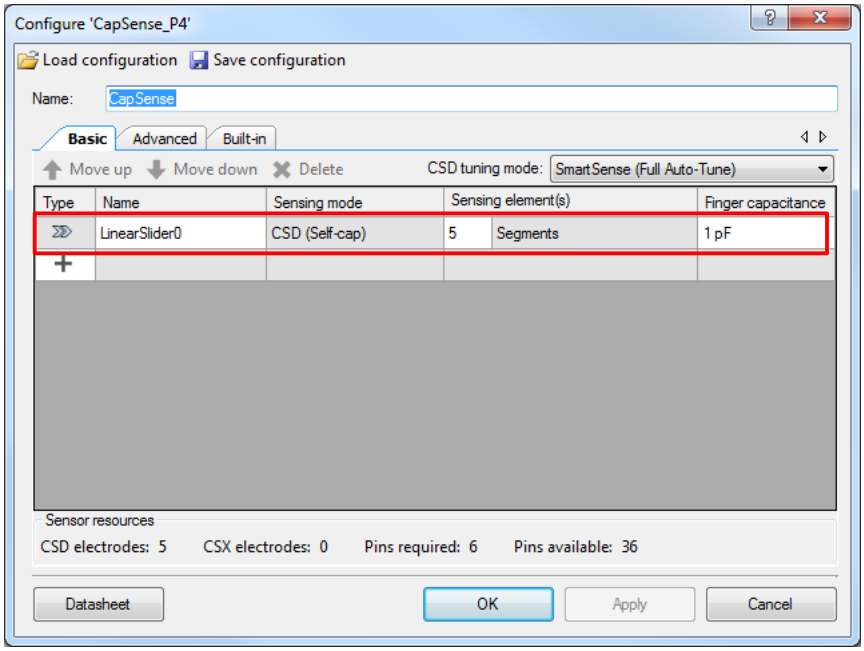
Component	Instance Name	Component Version	Hardware Resources
CapSense	CapSense	v4.0	CSD, and 2 GPIO pins
EZI2C Save (SCB mode)	EZI2C	v3.20	SCB, 2 GPIO pins

Parameter Settings

CapSense

Figure 4 shows the settings for the CapSense Component. See the CapSense Component datasheet for additional information.

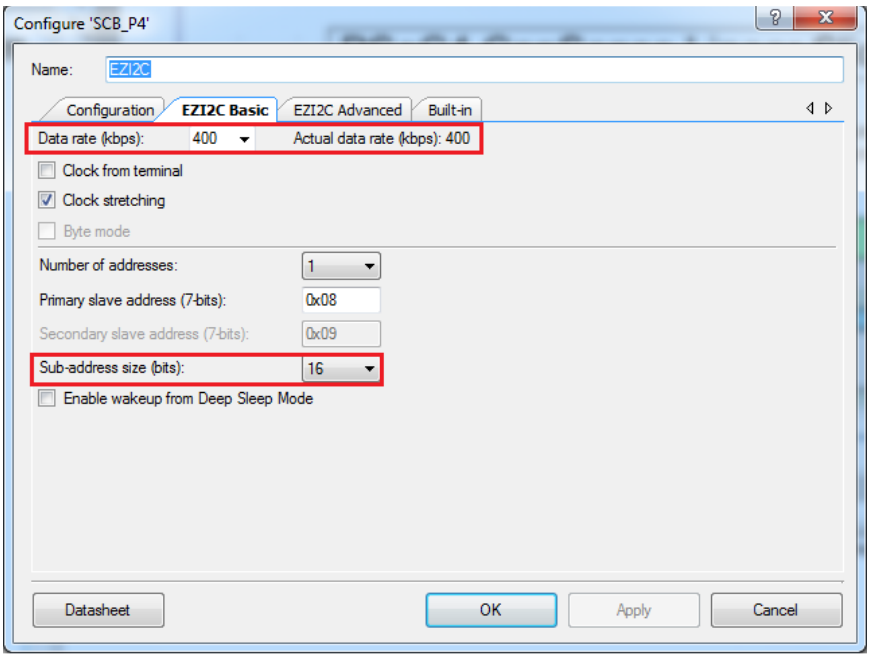
Figure 4. CapSense component's Basic tab



EZI2C Component

Figure 5 shows the settings for EZI2C Component. See the SCB Component datasheet for additional information.

Figure 5. EZI2C Slave Component Basic Settings

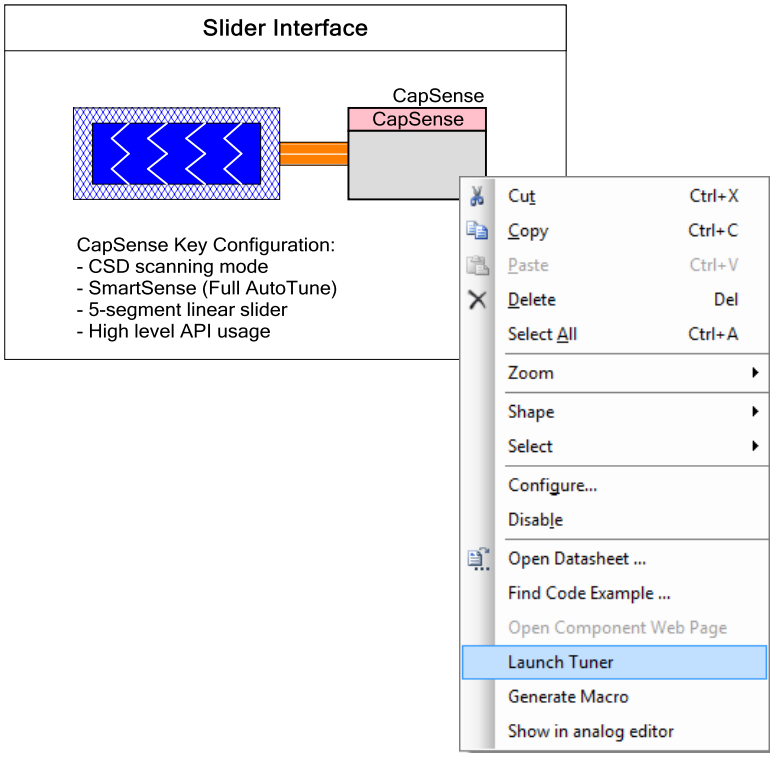


Operation

After you build and install the example in CY8CKIT-042, test the example by doing the following:

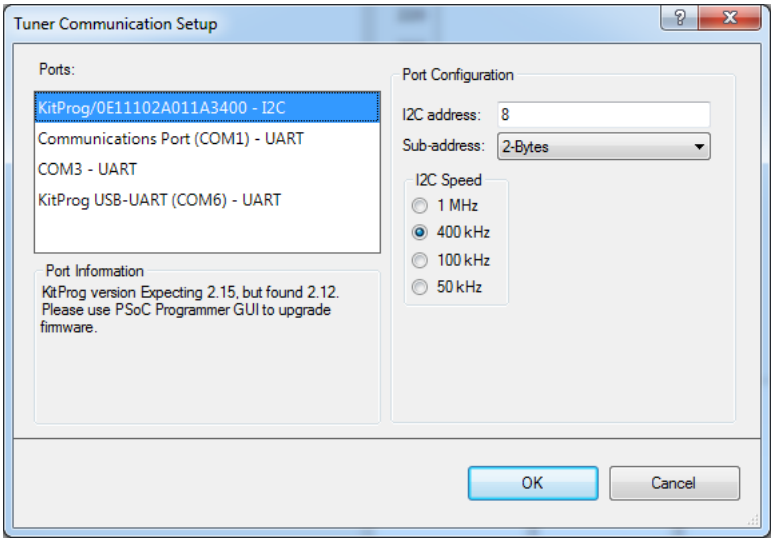
1. Launch the Tuner GUI. Right-click the CapSense Component as shown in (Figure 6). Select **Launch Tuner** in the menu.

Figure 6. Launch Tuner GUI



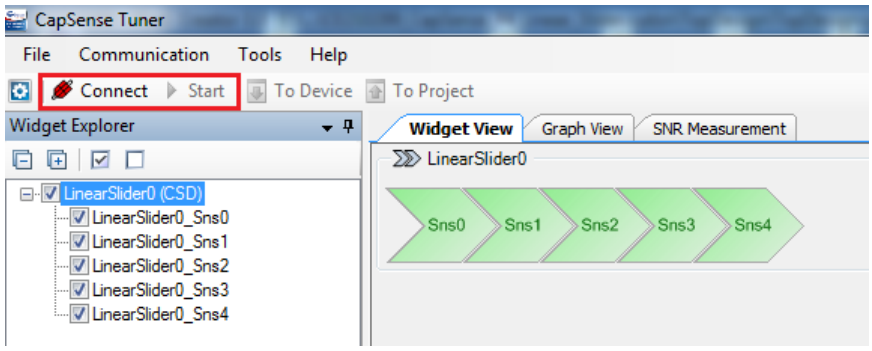
2. Navigate to **Tools/Protocol Settings** in the Tuner GUI menu to set up the I^2C communication (Figure 7).
   - Select the I2C device of the kit
   - Choose the I2C address, sub-address size, and speed as shown in Figure 7.

Figure 7. Setting Up I2C Communication



3. In the tuner GUI, click the Connect button followed by Start button (Figure 8).

Figure 8. Starting Communication



4. After establishing the I^2C communication between the device and Tuner GUI, you can observe the sensor data and the slider position when touching the slider. Figure 9 and Figure 10 show sensor debug data such as difference count, raw count, and baseline.

Figure 9. Tuner: Widget View

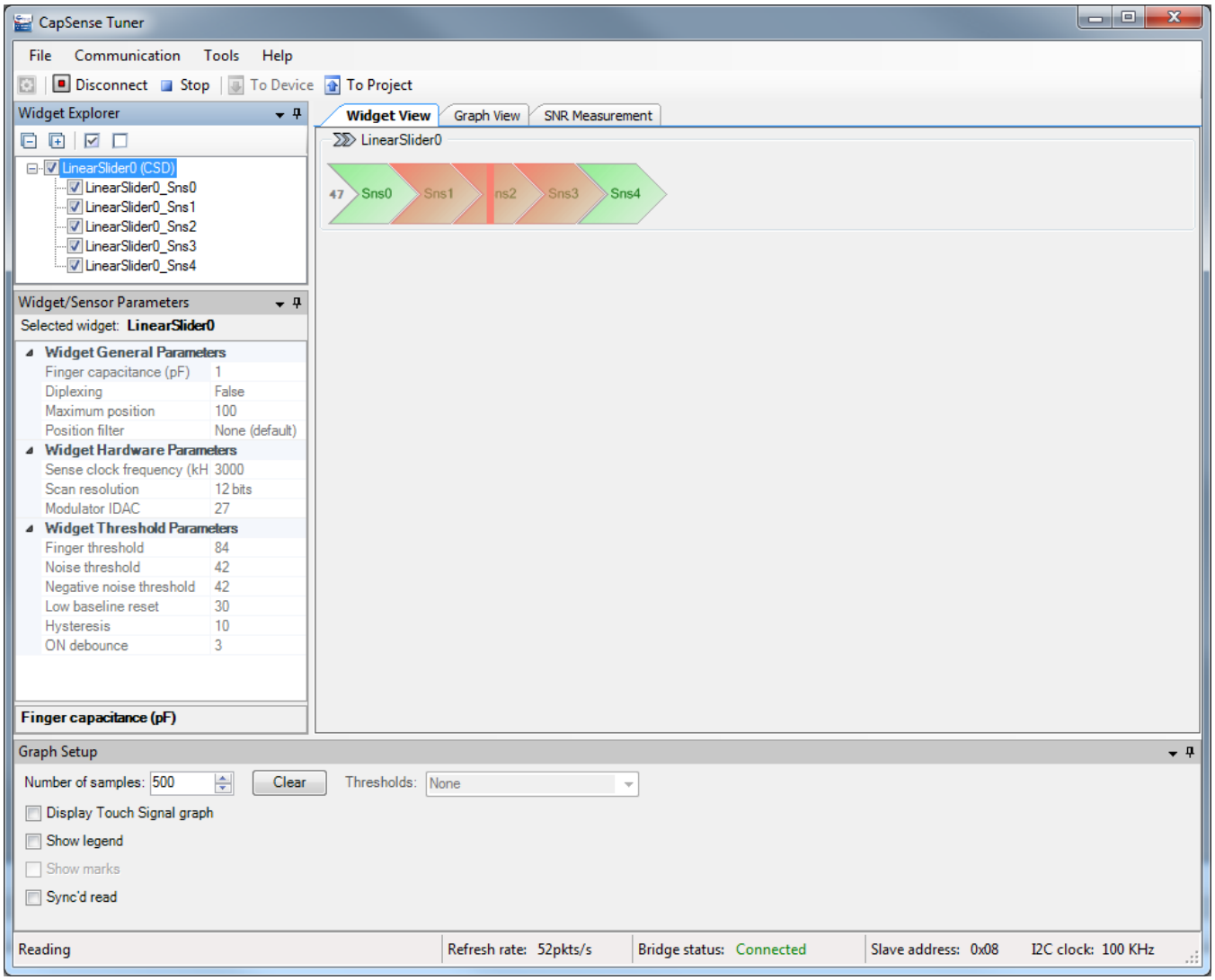
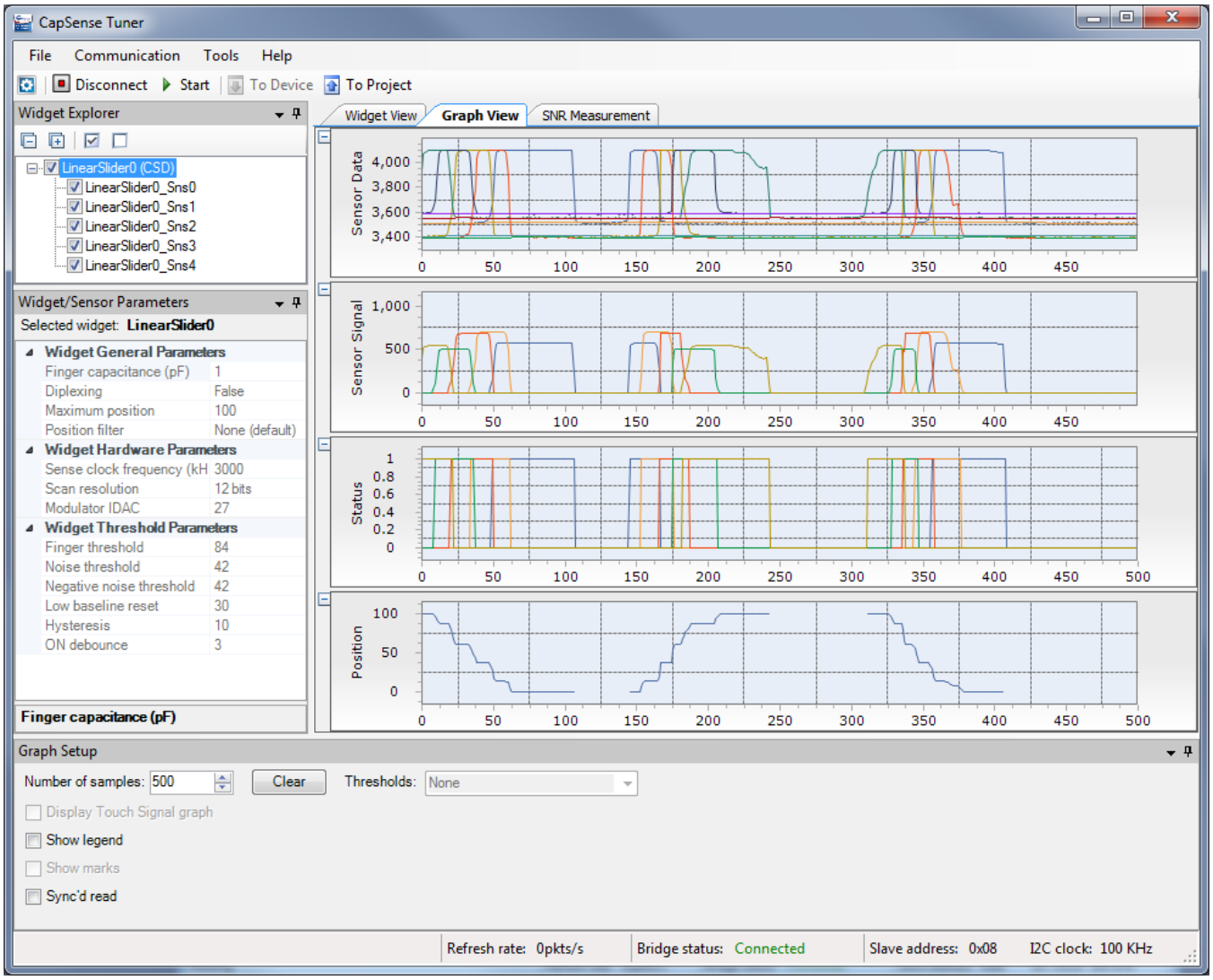


Figure 10. Tuner: Graph View



Related Documents

Table 3 lists relevant application notes, code examples, device datasheets, and PSoC Creator Component datasheets.

Table 3. Related Documents

Application Notes		
AN79953	Getting Started with PSoC 4	Describes the PSoC 4 and how to build a first PSoC Creator project.
AN85951	AN85951 - PSoC® 4 CapSense® Design Guide	Design Guide shows how to design capacitive touch sensing applications with the PSoC 4
Code Examples		
CE210291	PSoC® 4 CapSense® One Button	
CE210290	PSoC 4 CapSense Low-Power Ganged Sensor	
PSoC Creator Component Datasheets		
CapSense	Supports capacitive touch sensing	
EZI2C Slave	Supports I2C slave operation	
Device Documentation		
PSoC 4 Datasheets	PSoC 4 Technical Reference Manuals	
Development Kit (DVK) Documentation		
CY8CKIT-042 PSoC® 4 Pioneer Kit		
CY8CKIT-042-BLE Bluetooth® Low Energy (BLE) Pioneer Kit		

PSoC Resources

Cypress provides a wealth of data at www.cypress.com to help you select the right PSoC device, and quickly and effectively integrate the device into your design. For a comprehensive list of resources, see KBA86521, How to Design with PSoC 3, PSoC 4, and PSoC 5LP. The following is an abbreviated list for PSoC 4:

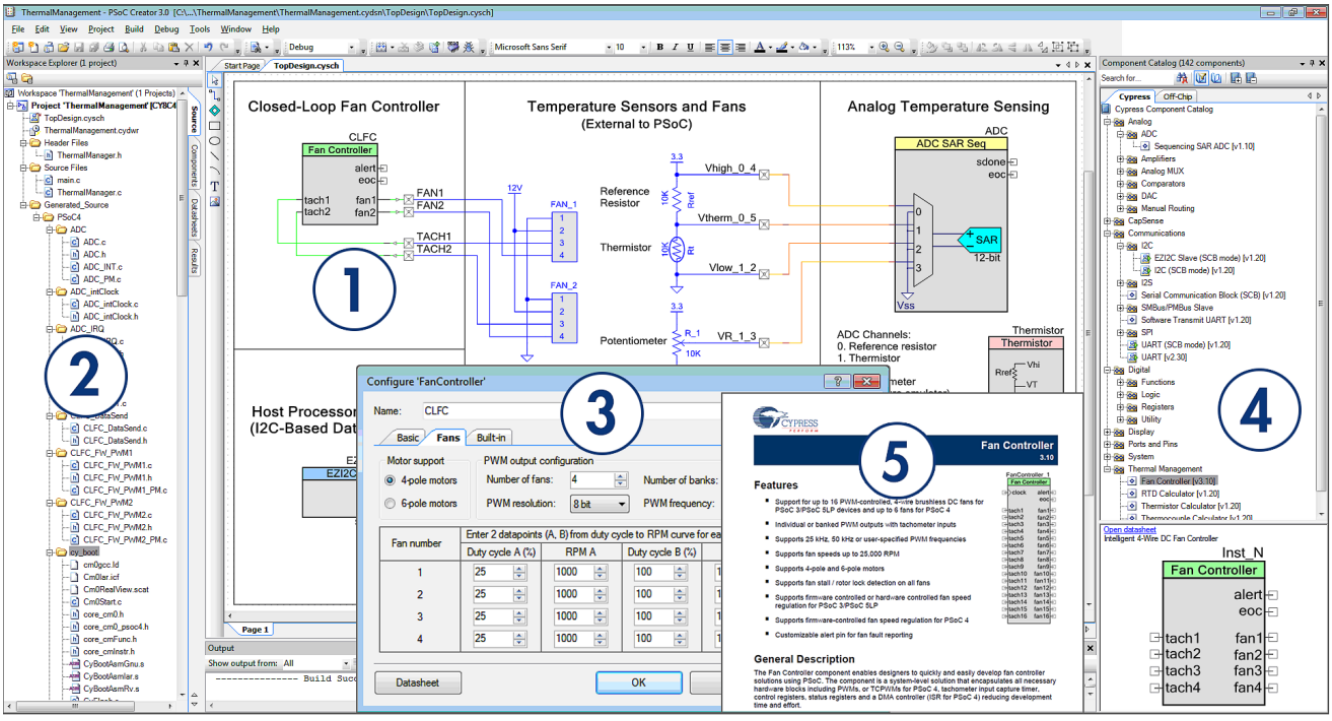
- **Overview:** PSoC Portfolio, PSoC Roadmap
- **Product Selectors:** PSoC 1, PSoC 3, PSoC 4, or PSoC 5LP. In addition, PSoC Creator includes a device selection tool.
- **Datasheets**: Describe and provide electrical specifications for the PSoC 4000, PSoC 4000S, PSoC 4100S, PSoC 4100, and PSoC 4200, PSoC 4xx7 BLE, PSoC 4200-M and PSoC Analog Coprocessor device families
- **CapSense® Design Guide:** Learn how to design capacitive touch-sensing applications with the PSoC 4 family of devices.
- **Application Notes and Code Examples:** Cover a broad range of topics, from basic to advanced level. Many of the application notes include code examples. PSoC Creator provides additional code examples.
- **Technical Reference Manuals (TRM):** Provide detailed descriptions of the architecture and registers in each PSoC 4 device family.
- **PSoC Training Videos**: These videos provide step-by-step instructions on getting started building complex designs with PSoC.
- **Development Kits:**
  - CY8CKIT-040, CY8CKIT-041-40xx, CY8CKIT-041-41xx, CY8CKIT-042, CY8CKIT-042-BLE, CY8CKIT-044 and CY8CKIT-048 PSoC Pioneer Kits are easy-to-use and inexpensive development platforms. These kits include connectors for Arduino™ compatible shields and Digilent® Pmod™ daughter cards.
  - CY8CKIT-049 is a very low-cost prototyping platform for sampling PSoC 4 devices.
  - CY8CKIT-001 is a common development platform for all PSoC family devices.
  - The MiniProg3 device provides an interface for flash programming and debug.

PSoC Creator

PSoC Creator is a free Windows-based Integrated Design Environment (IDE). It enables concurrent hardware and firmware design of systems based on PSoC 3, PSoC 4, and PSoC 5LP. See Figure 11 – with PSoC Creator, you can:

1. Drag and drop Components to build your hardware system design in the main design workspace
2. Codesign your application firmware with the PSoC hardware
3. Configure Components using configuration tools
4. Explore the library of 100+ Components
5. Review Component datasheets

Figure 11. PSoC Creator Features



Document History

Document Title: CE210289 - PSoC® 4 CapSense® Linear Slider

Document Number: 002-10289

Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	5142891	AKSM	02/22/2016	New code example
*A	5442393	VMED	09/20/2016	Updated components to the latest versions available in PSoC Creator 4.0
*B	5638792	VMED	03/02/2017	Updated components to the latest versions available in PSoC Creator 4.1 Minor edits throughout the document Updated template
*C	5740011	AESATP12	05/26/2017	Updated logo and copyright.

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