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## Objective

This code example demonstrates how to use the Delta Sigma ADC in Differential mode.

## Overview

The ADC measures the differential voltage between two voltage sources – an external variable resistor and an internal voltage source. The measurement results are displayed on the LCD.

## Requirements

**Tool:** PSoC Creator 4.1™ Update 1

**Programming Language:** C (Arm® GCC 5.4-2016-q2-update for PSoC® 5LP or DP8051 Keil 9.51 for PSoC 3)

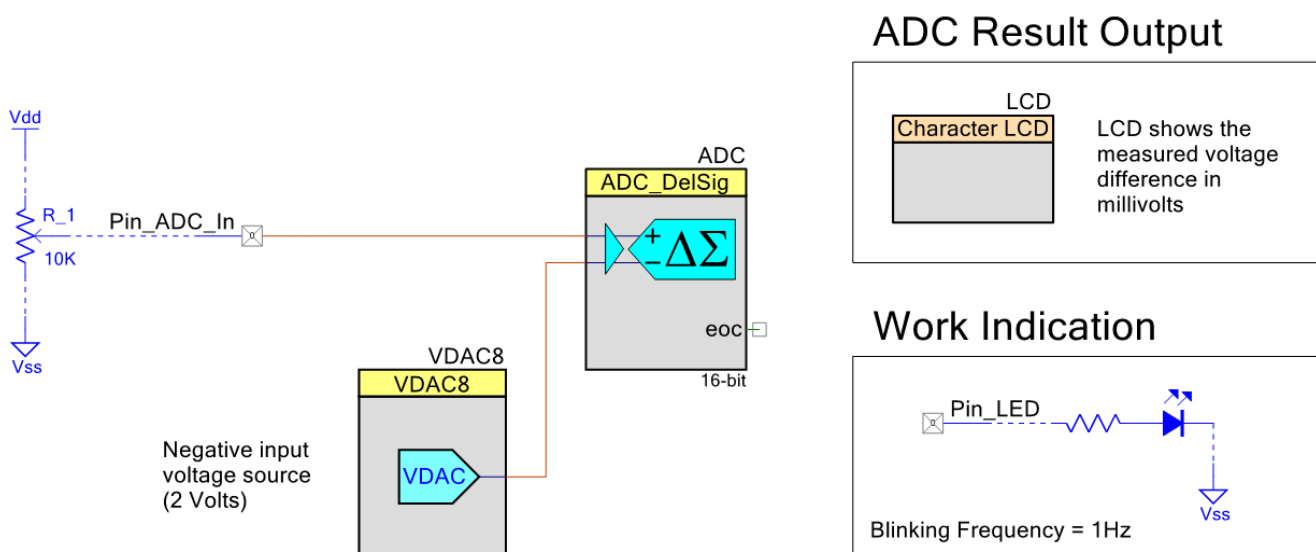
**Associated Parts:** All PSoC 3 and PSoC 5LP parts

**Related Hardware:** [CY8CKIT-030](#), [CY8CKIT-050](#)

## Design

The design in [Figure 1](#) consists of the Delta Sigma ADC and Voltage DAC (VDAC8) Components. The Delta Sigma ADC measures the differential voltage between Pin\_ADC\_In and VDAC8. Pin\_ADC\_In is connected to the positive input of the ADC. VDAC8 is configured to generate a constant voltage of 2 V on the ADC's negative input. The Character LCD Component shows the value of the measured voltage of the ADC. The pin LED drives the onboard LED and indicates that the code example works.

Figure 1. Delta Sigma ADC Code Example Schematic



The firmware:

- Initializes VDAC8, ADC, and LCD
- Starts ADC conversion
- Gets converted results
- Shows the results on the LCD
- Toggles the LED state

## Design Considerations

This code example is designed to run on [CY8CKIT-030](#) with PSoC 3 and [CY8CKIT-050](#) with PSoC 5LP. These kits have an onboard variable resistor to generate external voltage and an LCD to display measured values. For these kits, the project includes control files to automatically assign pins during the project build. To change the pin selection, over-ride the control file selections in the Pin Editor of the Design Wide Resources by selecting the new port or pin number.

This example is easily adapted to work with CY8CKIT-001 with the PSoC 3 or PSoC 5 processor module. To start, in Design Wide Resources Pin Editor, re-assign Pin\_ADC\_In and Pin\_LED to any of the Port 0 or Port 1 pins and connect them with jumper wires to the onboard variable resistor and LED pins accordingly. The [CY8CKIT-001](#) uses the same LCD connection as [CY8CKIT-030](#) and [CY8CKIT-050](#) – Port 2[6:0]. For details, see the [CY8CKIT-001](#) PSoC Development Kit Guide.

The design can also be adapted to work with any PSoC 3 or PSoC 5LP system that provides a variable voltage source. The LCD can be replaced with a communication port such a UART and SPI.

## Hardware Setup

This example uses the kit's default configuration. For more information, see the kit guide.

This example project is designed to run on the [CY8CKIT-030](#) kit. To switch from [CY8CKIT-030](#) to any other kit, go to the **Project > Device selector** and choose the target device of your development kit.

[Table 1](#) lists the supported kits, devices, and pin assignments.

Table 1. Supported Kits, Devices, and Pin Assignments

Kit	Device	Pin Assignments		
		Pin_ADC_In	\LCD:LCDPort[6:0]	Pin_LED
<a href="#">CY8CKIT-030</a>	CY8C3866AXI-040	P6[5]	P2[6:0]	P6[3]
<a href="#">CY8CKIT-050</a>	CY8C5868AXI-LP035	P6[5]	P2[6:0]	P6[3]

**Note:** For all supported kits, code example includes control files to automatically assign pins during the project build. To change the pin selection, over-ride the control file selections in the Pin Editor of the Design Wide Resources.

## Operation

1. Plug the [CY8CKIT-030](#) or [CY8CKIT-050](#) kit board into your computer's USB port.
2. Build the project and program it into the PSoC 3 or PSoC 5LP device. Choose **Debug > Program**. For more information about device programming, see the PSoC Creator Help.
3. Vary the input voltage by using the variable resistor and observe a digital value on the LCD. If the input voltage is 0 V, the displayed value will be equal to -2 V. If the input voltage is 2 V, the displayed value will be equal to 0 V.
4. Confirm that the kit's LED is blinking.

The following sections describe the components (parameter settings) and resources used to make this example.

## Components

[Table 2](#) lists the PSoC Creator components used in this example, the hardware resources used by each component and its non-default parameter settings.

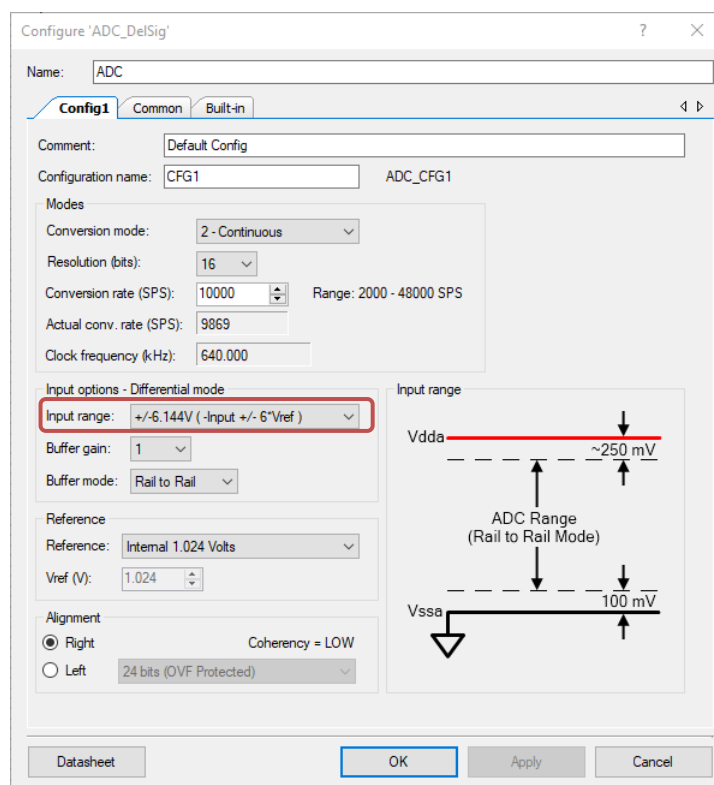
Table 2. PSoC Creator Components and Parameters

Component	Instance Name	Hardware Resources	Non-default Parameter settings
Delta Sigma ADC	ADC_DelSig	1 Delta-Sigma ADC 1 Interrupt	See <a href="#">Parameter Settings</a>
Voltage DAC	VDAC8	1 VIDAC	Range: 0 – 4.080 V (16 mV/bit) Value: 2000 mV
Analog Pin	Pin_ADC_In	1 pin	None
<b>Output system</b>			
Character LCD	LCD	7 pins	None
Digital Output Pin	Pin_LED	1 pin	HW Connection: OFF

## Parameter Settings

Figure 2 shows the Delta Sigma ADC Component configuration with the non-default settings highlighted.

Figure 2. Delta Sigma ADC Component Customizer Settings



## Related Documents

Application Notes	
<a href="#">AN54181</a> Getting Started with PSoC 3	Describes the PSoC 3, and how to build this code example.
<a href="#">AN77759</a> Getting Started with PSoC 5LP	Describes the PSoC 5LP, and how to build this code example.
PSoC Creator Component Datasheets	
<a href="#">Delta Sigma ADC</a>	Supports Delta Sigma Analog to Digital Converter
<a href="#">Pins</a>	Supports connection of hardware resources to physical pins
<a href="#">Voltage DAC</a>	Supports 8-Bit Voltage Digital to Analog Converter
<a href="#">Character LCD</a>	Supports Character LCD with Hitachi 44780 standard 4-bit interface
Device Documentation	
<a href="#">PSoC 3 Datasheets</a>	<a href="#">PSoC 3 Technical Reference Manuals</a>
<a href="#">PSoC 5LP Datasheets</a>	<a href="#">PSoC 5LP Technical Reference Manuals</a>
Development Kit (DVK) Documentation	
<a href="#">CY8CKIT-030</a>	
<a href="#">CY8CKIT-050</a>	
<a href="#">CY8CKIT-001</a>	

## Document History

Document Title: CE195271 – PSoC 3/PSoC 5LP Delta Sigma ADC in Differential Mode

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Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	5891446	MYKZTMP1	10/27/17	New code example

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