Applications for depletion MOSFETs

About this document

Scope and purpose

Depletion MOSFETs, unlike enhancement MOSFETs, are in an on-state even at 0 V of gate-to-source voltage ($V_{GS}$). This feature makes them suitable for using as a constant current source as well as in other ways. This application note explains how depletion MOSFETs can be used in different applications.

Intended audience

This document is intended for electronic equipment designers.

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What is a depletion MOSFET?

As depletion MOSFETs are in the on-state (i.e. they can conduct current) even when the gate-to-source voltage ($V_{GS}$) is zero. This can be best shown by comparing the transfer characteristics of enhancement and depletion MOSFETs.

![Transfer characteristics of enhancement (BSS127) and depletion (BSS126) MOSFETs](image)

Figure 1  Transfer characteristics of enhancement (BSS127) and depletion (BSS126) MOSFETs

<table>
<thead>
<tr>
<th>Static characteristics</th>
<th>$V_{BRDss}$</th>
<th>$V_{GS(th)}$</th>
<th>$I_D$</th>
<th>$I_R$</th>
</tr>
</thead>
<tbody>
<tr>
<td>Drain-source breakdown voltage</td>
<td>$V_{BRDss}$</td>
<td>$V_{GS(th)}$</td>
<td>$I_D$</td>
<td>$I_R$</td>
</tr>
<tr>
<td>Gate threshold voltage</td>
<td>$V_{GS(th)}$</td>
<td>$V_{GS(th)}$</td>
<td>$I_D$</td>
<td>$I_R$</td>
</tr>
</tbody>
</table>

Figure 2  Screenshot from the datasheet for BSS126

Figure 1 illustrates an example of the transfer characteristics of both devices. For a MOSFET, the gate-to-source voltage ($V_{GS}$) should be higher than the gate-to-source threshold voltage ($V_{GS(th)}$) in order to conduct current through it. For an N-channel enhancement MOSFET $V_{GS(th)}$ is above 0 V. Therefore, even at $V_{GS}$ of 0 V, a depletion MOSFET can conduct current. To turn off a depletion MOSFET the $V_{GS}$ should be lower than the (negative) $V_{GS(th)}$. This is clearly shown in schematic symbols for both. Figure 3 below shows the schematic symbols for enhancement and depletion MOSFETs respectively.

![Differences in schematic symbols – enhancement (left) and depletion (right) MOSFET](image)

Figure 3  Differences in schematic symbols – enhancement (left) and depletion (right) MOSFET

The symbols for enhancement and depletion MOSFETs show a difference in the second vertical line from the left, marked in red. For the enhancement MOSFET this line is discontinuous. This shows that the MOSFET is switched off with a $V_{GS} = 0$ V. With a $V_{GS} = 0$ V a depletion MOSFET can conduct current, and this is shown by the dashed line.
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Shorting gate and source, as shown in Figure 4, results in a load current independent of the applied drain-to-source input voltage. For a BSS126 this current is typically 22.5 mA.

![Figure 4](image)

**Figure 4** Typical BSS126 drain current with $V_{GS} = 0$ V

### 2.1 Adjusting the drain current

If the drain current at $V_{GS} = 0$ V is too high a simple source resistor must be added to the circuit. Figure 5 shows this arrangement. Assuming a drain current of 4 mA the gate-to-source must be -1 V, i.e. the resistor’s value is calculated using Ohm’s law by $R = 1 \, V / 4 \, mA = 250 \, \Omega$.

This is valid for a typical BSS126. In reality the device’s tolerances have to be taken into account.

![Figure 5](image)

**Figure 5** BSS126 with adjusted drain current

### 2.2 Depletion MOSFET in a start-up circuit

Depletion MOSFETs are widely used in a start-up circuit of auxiliary power supplies. To power up a PWM IC in a flyback circuit a very common method is shown in Figure 6.

A resistor is connected between the positive rail (for example the rectified mains voltage or a PFC voltage) and the IC’s capacitor, $C$. Typically this capacitor’s value is between 10 µF and 100 µF. The start-up time shouldn’t be too long.
How to use a depletion MOSFET

Example: \( V_{in} = 400 \text{ V}, \ C = 47 \mu \text{F}, \) IC’s start-up voltage \( V_IC = 10 \text{ V} \)

Assuming the use of SMD resistors the power losses are limited to 0.25 W, i.e. \( R \geq (400 \text{ V})^2 / 0.25 \text{ W} \approx 640 \text{ k}\Omega \).

The resulting current is calculated by \( T_{start} \approx 400 \text{ V} / 640 \text{ k}\Omega = 625 \mu \text{A} \).

The start-up time \( T_{start} = C \times V / I = 22 \mu \text{F} \times 10 \text{ V} / 625 \mu \text{A} = 0.352 \text{ s} \).

If this circuit is used at low-line voltage, e.g. \( \approx 110 \text{ V} \), the start-up time is increased to \( \approx 1 \text{ s} \), which in many applications is too long.

Additionally most PWM ICs already sink several hundred \( \mu \text{A} \) before they reach the start-up threshold, resulting in an even longer start-up time with this resistor arrangement.

The start-up time can be optimized by decreasing the values of \( R \) and/or \( C \).

Decreasing the resistance increases the losses, while decreasing \( C \) is sometimes not possible.

A much more elegant solution is the use of a depletion MOSFET, as shown in Figure 7.

Now the start-up time is independent of the input voltage.

This circuit can be further optimized by adding a few SMD devices – see Figure 8.
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How to use a depletion MOSFET

![Diagram of Flyback with control IC with a depletion MOSFET, adjusted drain current and without standby losses]

Figure 8 Flyback with control IC with a depletion MOSFET, adjusted drain current and without standby losses

The depletion MOSFET with adjusted drain current powers up the IC. If the voltage delivered by the auxiliary winding is high enough the NPN transistor is triggered, pulling down the gate of the depletion MOSFET below its threshold voltage and switching it completely off.

Example: The depletion MOSFET BSS126 (600 V, 700 Ω, SOT-23) is a good choice. A reasonable start-up time for many applications is 200 ms or shorter. Assuming a PWM IC’s standby current of 100 µA and a capacitor’s value of 47 µF the start-up current \( I_0 \) is calculated to:

\[
I_0 = C \cdot V / t + 100 \, \mu A = 22 \, \mu F \cdot 10 \, V / 200 \, ms + 100 \, \mu A = 1.2 \, mA
\]

The depletion MOSFET is switched off shortly after the start-up time of 200 ms so the \( Z_{th,JA} \) can be used to check if the device is used within its thermal limits.

![Diagram of 4 Max. transient thermal impedance]

Figure 9 \( Z_{th,JA} \) diagram – BSS126

For 200 ms the \( Z_{th,JA} \) is around 120 K/W on a standard PCB. With \( V_{in} = 400 \, V \) and an \( I_0 = 1.2 \, mA \) the losses are \( P = 400 \, V \cdot 1.2 \, mA = 480 \, mW \). The temperature rise during the start-up time is now calculated to

\[
\Delta T = 480 \, mW \cdot 120 \, K/W = 57.6 \, K
\]

A temperature rise of 57.6 K is absolutely acceptable.
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2.3 Linear regulators

A depletion MOSFET can also be used as a pass transistor for a linear regulator. Figure 10 shows a linear regulator circuit using a depletion MOSFET plus a Zener diode.

In the circuit, Q₁ acts as a source follower where the source voltage follows the gate voltage (V_G) minus the gate-to-source voltage. V_GS increases with increasing drain current. Thus, with a fixed gate voltage, the source voltage will drop with increasing load current. For design purposes, V_GS under saturation and cut-off conditions (0 V and V_GS(th), respectively) can be used. These values can be readily obtained from the datasheet, and Figure 11 shows the cut-off and saturation conditions of the Infineon BSS169. Bias current for the Zener diode is determined by V_GS/R. The Zener diode sets the gate voltage and should be selected to provide a source voltage within the range determined, taking into account the variances of V_GS with load.

![Depletion MOSFET as a linear regulator](image)

Figure 10 Depletion MOSFET as a linear regulator

The point to be noted here is that the MOSFET’s saturation current must be higher than the required load and bias currents, otherwise the device can be stressed or destroyed due to over-current.

<table>
<thead>
<tr>
<th>Static characteristics</th>
<th>V_{G(S)}</th>
<th>V_GS=10 V, I_D=250 mA</th>
<th>100</th>
<th>-</th>
<th>-</th>
<th>V</th>
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<tbody>
<tr>
<td>Drain-source breakdown voltage</td>
<td>V_{G(S)}</td>
<td>V_P=2 V, I_D=50 μA</td>
<td>-2.9</td>
<td>-2.2</td>
<td>-1.8</td>
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</tbody>
</table>

Figure 11 Gate threshold voltage for BSS169

2.3.1 Example

The application above can be explained more clearly with an example. Consider a regulator with 5 V output at 5 mA of output current from 24 V input using Infineon’s depletion MOSFET BSS169.

With a 3.3 V Zener diode the output voltage varies due to the MOSFET’s V_{G(S(th))} spread of 4.7 V to 5.5 V. The resulting power dissipation of the BSS169 is calculated to 0.1 W, which is an acceptable value and well below the specified power limit.

2.3.2 Other examples

A constant current source can be used in many different applications. A few examples are listed below:

- A constant current source is useful to generate a bias current that is independent of the voltage across it. It can be used to charge a capacitor at a constant rate, generating a linear ramp for timing purposes.
- A constant current source can be used as a trickle charger to maintain battery charge state.
- A constant current source can be used as a current limiter when the current is below the limit.
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- A constant current source can also be used as a linear LED driver for driving LED strings typically up to 20 mA. Figure 12 shows a linear LED driver circuit using a depletion MOSFET and a series resistor.

Figure 12  Depletion MOSFET as a linear LED driver
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Depletion MOSFETs from Infineon

3 Depletion MOSFETs from Infineon

Infineon offers depletion MOSFETs in SOT-223 and SOT-23 packages, with breakdown voltages ranging from 60 V to 600 V. Table 1 shows Infineon’s depletion MOSFET portfolio:

Table 1

<table>
<thead>
<tr>
<th>Part number</th>
<th>Package</th>
<th>BV_DSS [V]</th>
<th>R_DS(on) [Ω]</th>
<th>V_GS(th),max [V]</th>
<th>V_GS(th),min [V]</th>
<th>I_DSS,min at V_GS = 0 V [mA]</th>
</tr>
</thead>
<tbody>
<tr>
<td>BSP149</td>
<td>SOT-223</td>
<td>200</td>
<td>1.8</td>
<td>-1</td>
<td>-2.1</td>
<td>140</td>
</tr>
<tr>
<td>BSP129</td>
<td>SOT-223</td>
<td>240</td>
<td>6</td>
<td>-1</td>
<td>-2.1</td>
<td>55</td>
</tr>
<tr>
<td>BSP179</td>
<td>SOT-223</td>
<td>400</td>
<td>24</td>
<td>-1</td>
<td>-2.1</td>
<td>20</td>
</tr>
<tr>
<td>BSP135</td>
<td>SOT-223</td>
<td>600</td>
<td>45</td>
<td>-1</td>
<td>-2.1</td>
<td>20</td>
</tr>
<tr>
<td>BSS159N</td>
<td>SOT-23</td>
<td>60</td>
<td>3.5</td>
<td>-2.4</td>
<td>-3.5</td>
<td>130</td>
</tr>
<tr>
<td>BSS169</td>
<td>SOT-23</td>
<td>100</td>
<td>6</td>
<td>-1.8</td>
<td>-2.9</td>
<td>90</td>
</tr>
<tr>
<td>BSS139</td>
<td>SOT-23</td>
<td>250</td>
<td>14</td>
<td>-1</td>
<td>-2.1</td>
<td>30</td>
</tr>
<tr>
<td>BSS126</td>
<td>SOT-23</td>
<td>600</td>
<td>7</td>
<td>-1.6</td>
<td>-2.7</td>
<td>7</td>
</tr>
</tbody>
</table>

For Infineon’s depletion MOSFETs $V_{GS(th)}$ is spread across five sorted bands. Each band has a distribution of 0.2 V and is assigned with an identification letter. Limits for each sorted band vary from $V_{GS(th),min}$ to $V_{GS(th),max}$. Figure 13 shows an example of the $V_{GS(th)}$ bands of BSP149.

Figure 13  Threshold voltage bands

Please note:

A single specific band cannot be ordered separately. However, it can be requested that each reel contains only one band of $V_{GS(th)}$. 
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Revision history

<table>
<thead>
<tr>
<th>Document version</th>
<th>Date of release</th>
<th>Description of changes</th>
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